

CS 343

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# Chapter 1

## Control Flow

**Control Flow** is the order or flow in which individual statements are executed evaluated in a program.

**Multi-Exit Loops** have at least one **break** in the middle, not just in the end.

```
for (;;) {
    cin >> d;
    if (cin.fail()) break;
    ...
}
```

**Flag Variables** are used to explicitly implement control flow.

```
bool flag1 = false;
while (!flag1) {
    cin >> d;
    if (cin.fail()) flag1 = true;
    else {
        ...
    }
}
```

They are the variable equivalent to a **goto**, since they can be set, reset, and tested at arbitrary locations in a program.

**Static Multi-Level Exit** occurs when the program exits (**returns** or **gotos**) at multiple levels which are known exit points are known at compile-time.

```
L1 : {
    C1
    L2: switch ( ... ) {
        L3: while (true) {
            ... break L1;
            ... break L2;
            ... break L3;
        }
    }
}
```



We can use multi-level exit to remove flag variables and to remove duplicate code.

**goto** goes to a line.

Only use goto to perform static multi-level exit (i.e. to simulate a labeled break and continue).

## Chapter 2

# Exceptions

### 2.1 Dynamic Multi-Level Exit

We can extract code to create new methods (called *modularization*), but this doesn't work with labels since labels have only routine scope.

```
// To illustrate the note, this will not compile.
void rtn ( ... ) {
    B2: for ( ... ) {
        if ( ... ) break B1;
    }
}
B1: for ( ... ) {
    rtn ( ... );
}
```

**Dynamic Multi-Level Exit** allows us to extend call and return semantics to go in the *reverse* direction, so given *A* calls *B* calls *C*, we *can* transfer from *C* back to *A*, skipping *B*.

**Non-Local Transfer** allows a routine to return from *C*, skip *B* and go directly to *A*. It's a generalization of the *multi-exit loop* and *multi-level exit*. We can accomplish this using a *label variable*.

```
label L;
void a (int i) {
    if ( ... ) goto L;
}
void b (int i) {
    // L1 is a label.
    L1: L = L1;
}
void c (int i) {
    // L2 is a label.
    L2: L = L1;
}
```

We go directly to the stack position that corresponds to the label, then change the PC to go to the label (*transfer point*) in the routine.

Since it can be set dynamically, control flow can't always be statically determined. Similar to `gotos`, we can break the stack really easily using labels.

In C, `jmp_buf` declares a label variable, `setjmp` initializes a label variable, and `longjmp` goes to a label variable.

## 2.2 Traditional Approaches

Without non-local transfer, we have a few options:

**return codes** are special values returned that signal the caller to perform “special” control flow logic.

This mixes exceptions with normal return values and makes code difficult to read. Additionally, it's easy to “not expect” these directives as output.

**status flags** are values in shared global status flag being modified.

This can be delayed or overwritten by concurrent threads.

**fix-up routines** are global or local routines called for an exception event to handle errors.

This increases the number of parameters, this increases the cost of each call when they're not used.

## 2.3 Exception Handling

Exceptional events are events that occur at low frequency, and are ancillary to an algorithm.

Exception handling mechanisms (EHM) actively force programmers to work with exceptions, which allows programs to become more robust.

## 2.4 Execution Environment

How exception handling mechanisms are implemented depends on the environment they're placed in.

The `finally` clause in Java and other languages exists to allow cleanup and deallocation code. This makes it hard to unwind the stack, because it sometimes needs to drop back into “finally world” before continuing propagating the error thrown.

Given multiple stacks, exception handling becomes incredibly sophisticated - can exceptions be propagated between stacks?

## 2.5 Terminology

**execution** is a language unit where exceptions can be raised.

**execution type** is the type of an execution.

**exception** is an instance of an exception type.

**raise (throw)** is the operation that causes an exception.

**propagation** directs control flow from a raise in the source to a handler.

**propagation mechanism** is the mechanism (or rules) used to locate the handler for a thrown exception.

Most mechanisms give precedence to handlers most recently created in the call stack.

**handler** is a nested code block responsible for handling a raised exception. It can handle by either returning, re-raising the same exception, or by raising a new exception.

**guarded block** is a language block with associated handlers (i.e. a **try-catch-block**).

**unguarded block** is a language block with no associated handlers.

**termination** is when control cannot return to where it was raised and the stack is unwound.

**resumption** is when control flow can return to the raise point.

**stack unwinding** is when all blocks on the faulting stack from the raise block to the guarded block are terminated, and destructors of objects are called.

**EHM** refers to the overall Exception Handling Mechanism. It refers to the 4-tuple of: Exception Type + Raise + Propagation + Handlers + Exception Instance.

## 2.6 Control Flow Types

Routine and exceptional control-flow can be characterized by two properties:

- Static/Dynamic call: routine/exception being called is looked up statically or dynamically
- Static/Dynamic return: after a routine or handler completes, it returns to its static or dynamic context (definition or caller).

We can tabulate this as the following:

	static call/raise	dynamic call/raise
static return	sequel	termination exception
dynamic return	routine	routine-pointer, virtual-routine, resumption

## 2.7 Static Propagation (Sequel)

**Sequels** are routines with no return value that code continues at the end of the block where the sequel is declared<sup>1</sup>.

---

<sup>1</sup>Though relevant, a discussion with classmates came to the conclusion that mentioned that he doesn't know of any languages that implement this feature.

```

void foo() {
    int i = 0;
    {
        sequel S1( ... ) { printf("one"); i++; }
        {
            sequel S2( ... ) { printf("two"); i++; }
        } // S2 returns to here.
        printf ("foo");
    } // S1 returns to here.
    printf ("bar")
    if (i == 0) {
        S1();
    } else if (i == 1) {
        S2();
    }
}
/*
 * prints:
 *     foo
 *     bar
 *     one
 *     bar
 *     two
 *     foo
 *     bar
 */

```

An advantage is that the handler is statically known, so it is super-duper efficient. A disadvantage is that sequels only work for monolithic programs because it must be statically nested where it is used (no re-use of “generic” sequels). This also prevents code from being separately compiled.

## 2.8 Dynamic Propagation

Both **termination** and **resumption** have dynamic raise; this works for separately-compiled programs, but is slower at runtime (lookup is not known statically).

### 2.8.1 Termination

Termination allows us to pass control from the start of propagation to a handler, then returns to a predefined position.

Three different basic termination forms for *non-recoverable* operations:

1. **nonlocal** - general mechanism for block transfer on the call stack, but has a goto problem.
2. **terminate** - limited mechanism for block transfer on the call stack.

```
label L;
```

```

void f ( ... ) {
    ...
    goto L;
}
int main() {
    L = L1;
    f (...);
L1:
S1: L = L2;
    f (...);
L2:
S2: ;
}

```

3. **retry** - combination of termination with special handler semantics

```

char readfiles( char *files[], int N) {
    int i = 0, value;
    ifstream infile;
    infile.open( files[i] );
    while (true) {
        try {
            ... infile >> value ...;
        } retry (Eof) {
            i += 1;
            infile.close();
            if (i == N) goto Finished;
            ... infile.open( files[i] ) ...; // try again.
        }
    }
    : Finished;
}

```

Since this is easily simulated, it's not usually supported directly.

Exception handlers can generate an arbitrary number of exceptions, and so can destructors.

Destructors that throw errors during propagation cause the program to terminate:

```

struct E {}
struct C {
    ~C() { throw E(); }
}
try {
    C x;
    throw E(); // Program terminates
} catch ( E ) { ... }

```

This is generally because we cannot start the second exception without a handler to deal with the first exception, the first one is left hanging.

## 2.8.2 Resumption

In resumption, control transfers to a handler, and dynamically returned.

```
_Event E {}; // uC++ exception label
void f() {
    _Resume E();
    cout << "control returns here" << endl;
}
void uMain::main() {
    try {
        f();
    } CatchResume ( E ) { cout << "handler 1" << endl; }
    try {
        f();
    } CatchResume ( E ) { cout << "handler 2" << endl; }
}
/*
 * output:
 *     handler1
 *     control returns here
 *     handler2
 *     control returns here
 */
```

## 2.9 Implementation

To implement termination and resumption, the raise needs to know the last guarded block with a handler for the raised exception type.

One approach is to associate a label variable with each exception type, re-setting the label variable whenever you enter and exit guarded blocks. This is millions of operations.

For termination, it is necessary to unwind the stack due to activations that contain objects with destructors and finalizers; we linearly unwind the stack this way.

If we assume there are very few exceptions compared to try entries and exits, we should choose to unwind the stack when implementing this for ourselves.

## 2.10 Exceptional Control-Flow

In this section we give an example of what control flow looks like for the following snippet

```
{
    try {
        try {
            try {
                {
```

```

        try {
            throw E5();
        } catch ( E7 ) {
            ...
        } catch ( E8 ) {
            ...
        } catch ( E9 ) {
            ...
        }
    }
} catch ( E4 ) {
    ...
} catch ( E5 ) {
    ...
} catch ( E6 ) {
    ...
}
} catch ( E3 ) {
    ...
}
} catch ( E5 ) {
    ... resume/retry/terminate ... // where do these go?
} catch ( E2 ) {
    ...
}

```

## 2.11 Additional Features

### 2.11.1 Derived Exception Type

**derived exception types** is a word for inherited exception types which allows us to catch exceptions with different levels of specificity. Exception type inheritance allows the handlers to match multiple exceptions.

When subclassing, it is best catch an exception by reference, because the exception will be truncated otherwise.

```

struct B {};
struct D : public B {};
try {
    throw D();
} catch ( B & e ) {
    ... dynamic_cast<D>(e); ...
}

```

### 2.11.2 Catch-Any

**Catch-any** is a mechanism to match anything, so we can finalize and deallocate variables.



In java, this is a simple “finally” block, or mimed within an `catch` (Exception) block.

### 2.11.3 Exception Parameters

**Exception parameters** allow passing information from the raiser to a handler, usually in ivars of the exception object.

### 2.11.4 Exception List

**Exception List** is a part of a routine’s prototype that specifies about what types of exceptions can be thrown by the routine by its caller. This helps detect static detection of invalid exceptions, and runtime detection of where the exception can be converted into a special failure exception.

# Chapter 3

## Coroutine

A **Coroutine** is a routine that can suspend at some point, and be resumed from that point when control returns. Think of it as a routine which uses pauses to hold state, and continues from the same point.

In exams and assignments, coroutines containing *any* state usually receive zero marks.

The state of a coroutine is a 3-tuple:

**Execution location** which is the *PC* starting at the beginning, and remembered at each suspend.

**Execution state** which is the stack for that coroutine

**Execution status** is a flag indicating if the coroutine is **active**, **inactive**, or **terminated**.

There are two different types of coroutines:

**Semi-Coroutines** have the ability to return execution to the caller, or call sub-routines.

**Full-Coroutines** have the ability to pass execution to any other coroutine.

Internally, the implementation of both types of coroutines in  $\mu\text{C}++$  are the same, but how we use them is different.

### 3.1 Coroutine Structure in $\mu\text{C}++$

There is a  $\mu\text{C}++$ -extension `_Coroutine` class that looks like the following:

```
_Coroutine Fibonacci {  
    int fn;  
    void main() {  
        int fn1, fn2;  
        fn = 0; fn1 = fn;  
        suspend();  
        fn = 1; fn2 = fn1; fn1 = fn;  
    }  
}
```

```

        suspend();
        while (true) {
            fn = fn1 + fn2;
            fn2 = fn1;
            fn1 = fn;
            suspend();
        }
    }
    public:
        int next() {
            resume();
            return fn;
        }
}

```

There's no execution state, and a main method that is suspended & resumed. Each instance of a coroutine has its own stack.

On initialization, `main` is executed until the first `suspend` call. Subsequent `resume` calls continue from the previous `suspend` call.

We can recursive functionality of coroutines to write simple iterators.

### 3.1.1 Coroutine Construction

The simplest way to write a coroutine is to write a standalone program, and convert it to a coroutine.

We can convert a normal program to a coroutine by:

- Putting processing code into the main
- Converting reads and writes to suspend calls.
- Use interface members and variables to transfer data to & from the coroutine.

### 3.1.2 Full-Coroutines

Semi-coroutines activate the member routine that activated it. Full-coroutines activate (and re-activate) any other coroutine.

The method `resume` activates the current coroutine (`uThisCoroutine`), and `suspend` activates the last resumer. In other words, we can call `B->resume` from the execution stack of `A->resume`, and control passes to coroutine B from A.

To have references from A to B and vice versa, we can pass in pointers, or creating a setter for the `ivar` in the one instantiated first.

When terminating a coroutine, execution control returns to its creator.

## 3.2 Coroutine Implementation

We can implement coroutines using either the callers stack (stackless), or creating a separate stack (stackful). Stackless coroutines can only suspend to the main coroutine.

### 3.2.1 Python

Python implements the `yield` keyword that allows coroutines to return values, but not to pass control to other coroutines. It thus has no full coroutine implementation.

# Chapter 4

## Concurrency

**Threads** schedule execution separately and independently from other threads.

**Processes** are program components that have at least one thread, and has the same state information as a coroutine.

**Tasks** are similar to processes, except it shares memory with other tasks. Tasks are sometimes called light-weight processes (LWP).

**Parallel Execution** is when 2 or more operations occur simultaneously, which only occurs with multiple CPUs.

**Concurrent Execution** is any situation where parallel execution appears to happen.

### 4.1 Why Concurrency

By dividing work between multiple threads, we can capitalize on resources available to us to decrease the time it takes to execute our program.

### 4.2 Why Concurrency is Hard

People can do a small number of things concurrently, but fail at large numbers of things, and even more when they interact with each other.

We need to be able to determine how and why to break up a problem into parts, decide how they react, and how reactions occur.

We finally need to reason about and debug multiple execution paths that execute in a non-deterministic order.

### 4.3 Concurrent Hardware

All types of concurrency<sup>1</sup> is trivially possible for a single processor (**uniprocessor**). We only need to context and switch threads on the CPU, and use pointers to share memory between tasks to have concurrent problems in uniprocessor systems. The bigger issue is that every computer has multiple CPUs these days.

In **multiprocessor** systems, we can still share memory using pointers. In distributed systems, pointers don't point to the same things, so we're pretty much screwed in that case.

### 4.4 Execution States

A thread can be in any of the states {new, ready, running, blocked, halted }, which are switched between in response to events.

Since events are non-deterministic, basic operations (such as increment) are unsafe.

### 4.5 Threading Model

**Threading Model** defines the relationship between CPUs and threads in a system.

**Kernel Threads** <sup>2</sup> are provided by the OS to manage CPUs. Kernel threads are scheduled across the CPUs.

Having more kernel threads than CPUs allows the OS to provide multiprocessing. A process may have multiple kernel threads to provide parallelism. User threads are a low-cost structuring mechanism.

This relationship between user threads, kernel threads, and CPUs can be compared as the following:

**Kernel Threading** 1:1:C - 1 user thread maps to 1 kernel thread

**Generalized Kernel Threading** M:M:C -  $M$  user threads map to  $M$  kernel threads <sup>3</sup>.

**User Threading** N:1:C -  $N$  user threads map to 1 kernel thread.

**User Threading** N:M:C -  $N$  user threads map to  $M$  kernel threads<sup>4</sup>.

Often, we omit the number of CPUs in our ratio. We can even add **nano threads** on top of user threads, and **virtual machines** under the OS.

- 1:1:C

---

<sup>1</sup>the three types are multithreading for multiple threads, multitasking for multiple tasks, and multiprocessing for multiple processes

<sup>2</sup>also known as **Virtual Processes**

<sup>3</sup>Java works this way

<sup>4</sup> $\mu$ C++ works this way

## 4.6 Concurrent Systems

Concurrent systems can be split into 3 major types:

1. Systems that attempt to discover concurrency - there is a limit to how much can be discovered.
2. Systems that provide concurrency through implicit constructs - concurrency is built using specialized mechanisms
3. Systems that provide concurrency through explicit constructs - concurrency is explicitly managed

In fact, both of these are complementary, and can be built into the same system.  $\mu C++$  has only explicit mechanisms. Some systems only have a single technique, but this is limited and awkward; when it comes to concurrency controls, more is better.

## 4.7 Speedup (Amdahl's Law)

Program speedup can be denoted  $S_c = \frac{T_1}{T_C}$ , where  $C$  is the number of CPUs, and  $T_1$  is the time taken for sequential execution.

$$\begin{aligned} S_c &= \frac{T_1}{T_C} \\ &= \frac{1}{(1 - P) + \frac{P}{C}} \end{aligned}$$

Where  $P$  is the proportion of a program that can be made parallel, and  $C$  is the degree of concurrency.

As we take  $\lim_{C \rightarrow \infty}$ , we get the maximum speedup:

$$S_{max} = \frac{1}{1 - P}$$

### 4.7.1 Sample Question

This is a sample question from the W12 Midterm:

**Question:** A program has 4 sequential stages, where each stage takes the following  $N$  units of time to execute:  $S_1 = 5$ ,  $S_2 = 20$ ,  $S_3 = 15$ ,  $S_4 = 60$ . Stages  $S_2$  and  $S_4$  are modified to increase their speed (i.e., reduce the time to execute) by 10 and 20 times, respectively. Show the steps in computing the total speedup for the program after the modification.

**Solution:** We can calculate the speedup  $S_C$  as the sequential speed  $T_1$  over the concurrent speed  $T_C$ :

$$\begin{aligned}
 S_C &= \frac{T_1}{T_C} \\
 &= \frac{S_1 + S_2 + S_3 + S_4}{S_1 + \frac{S_2}{10} + S_3 + \frac{S_4}{20}} \\
 &= \frac{5 + 20 + 15 + 60}{5 + 2 + 15 + 3} \\
 &= 4
 \end{aligned}$$

Thus the overall speedup is by 4 times.

## 4.8 Thread Creation

We need the following 3 things to adequately specify concurrency:

1. Thread creation<sup>5</sup>
2. Synchronization between threads
3. Communication between threads

In  $\mu C++$ , we use `_Tasks` to emulate a `cobegin` segment.<sup>6</sup>

The slowest path through all tasks synchronizing is called the **critical path**.

### 4.8.1 $\mu C++$ `_Tasks`

Tasks are threads in  $\mu C++$ . Calling the destructor causes the current thread to wait until the task completes execution.

```

_Task T1 {
    void main() {}
}
_Task T2 {
    void main() { int temp = 1; }
}

void uMain::main() {
    T1 *t1 = new T1; // start execution of T1 from its main.
    ...
    T2 *t2 = new T2; // start execution of T2 from its main.
    ...
    delete t1; // wait for T1 to complete
    ...
    delete t2; // wait for T2 to complete
}

```

---

<sup>5</sup>Thread creation is a primitive operation that cannot be made by fitting other operations together.

<sup>6</sup>These segments are effectively parallelism between threads.



This structure allows us to “kick off” the same `_Task` multiple times with different arguments.

## 4.9 Termination Synchronization

A thread finishes when

- It completes execution
- It throws an error
- It is killed by its parent (not in  $\mu\text{C}++$ )
- The parent terminates (not in  $\mu\text{C}++$ )

We may trigger/react to termination to implement functionality.

## 4.10 Divide and Conquer

We use divide-and-conquer to take advantage of work that can be done individually then merged. Using divide and conquer, work done individually should look the same.

Task creation order doesn’t matter, but deletion order may, depending on the critical path through the tasks.

## 4.11 Synchronization and Communication During Execution

Synchronization happens when one thread happens when one thread waits for another to execute until a certain point. This is useful for threads waiting to transfer information from one thread to another.

One way to do this is using a **busy wait**, which is bad.

## 4.12 Communication

After synchronization, threads can transfer information many ways. In the same memory, the information can be transferred by value or address. In different memories<sup>7</sup>, transferring information by value is easiest.

## 4.13 Critical Section

Threads may want to modify shared resources (such as a linked list). Multiple threads operating on the same object is problematic. While this is not a problem while operations are **atomic**, so we need to find a way to do this for our code.

---

<sup>7</sup>For example, they are in a distributed system.

We call area inside an atomic operation the **critical section**, and the act of preventing simultaneous execution **mutual exclusion**.

We can serialize all access, but this fails when there are many readers.

## 4.14 Static Variables

Static variables are shared between all objects of that class, and may need mutual exclusion.

The only exception for this is in task constructors, which are naturally mutually exclusive in  $\mu\text{C}++$ . It's highly suggested not to use static variables in a concurrent program.

## 4.15 Mutual Exclusion

Mutual exclusion requires that all the following clauses are true.

1. Only one thread can be in a critical section at a time
2. The underlying system guarantees all threads get some CPU time.
3. Threads not in critical sections should not prevent threads from executing critical sections.
4. We should always return to select a thread to enter a critical section.<sup>8</sup>
5. The number of threads allowed to enter a critical section after a given thread requests to enter it should be capped, so there is no **starvation** going on.

## 4.16 Self-Testing Critical Section

We can create self-testing critical sections by setting critical sections that abort if another thread is currently messing up our stuff:

```
void criticalSection () {
    ::CurrTid = &uThisTask();
    for (int i=1; i<100; i++) {
        if (::CurrTid != &uThisTask()) {
            uAbort("interference");
        }
    }
}
```

## 4.17 Software Solutions

Software Solutions to this problem must solve the problems presented in the list above.

---

<sup>8</sup>I'm not sure what this is about. The terms **Liveness** and **indefinite postponement** are in this.

### 4.17.1 Lock

Locks have a *status* flag that indicates if a thread is in a critical section:

```
foo() {  
    while (lock == CLOSED) {}  
    lock = CLOSED;  
    criticalSection();  
    lock = OPEN;  
}
```

This strategy breaks rule 1.

### 4.17.2 Alternation

The alternation strategy is one where a thread will go only if it is not the last one to progress through:

```
foo() {  
    while (last == me) {}  
    criticalSection();  
    last = me;  
}
```

This strategy breaks rule 3.

### 4.17.3 Declaration of Intent

The declaration of intent strategy is one where threads politely wait until no others want to enter.

```
foo() {  
    me = WANT_IN;  
    while (you == WANT_IN) {}  
    criticalSection();  
    me = DONT_WANT_IN;  
}
```

This strategy breaks rule 4.

### 4.17.4 Retract Intent

The retraction of intent strategy is one where threads politely submit & resubmit requests to execute the critical section.

```
foo() {  
    while (true) {  
        me = WANT_IN;  
        if (you == DONT_WANT_IN) break;  
        me = DONT_WANT_IN;
```

```

        while (you == WANT_IN) {}
    }
    criticalSection();
    me = DONT_WANT_IN;
}

```

This strategy breaks rule 4.

#### 4.17.5 Prioritized Entry

From subsection 4.17.4, we can add the ability to be a high priority thread:

```

foo() {
    if (this.priority == HIGH) {
        me = WANT_IN;
        while (you == WANT_IN) {}
    } else {
        while (true) {
            me = WANT_IN;
            if (you == DONT_WANT_IN) break;
            me = DONT_WANT_IN;
            while (you == DONT_WANT_IN) {}
        }
    }
}

```

This strategy breaks rule 5.

#### 4.17.6 Dekker

The Dekker algorithm doesn't break any rules. It uses a mixture of declared intention and alternation to achieve its goals:

```

foo() {
    while (true) {
        me = WANT_IN;
        if (you == DONT_WANT_IN) break;
        if (last == &me) {
            me = DONT_WANT_IN;
            while (last == &me) {} // wait for last == somebody else.
        }
    }
}

```

This strategy makes no assumptions about atomicity, and works on a machine where bits are scrambled during simultaneous assignment.

### 4.17.7 Peterson

The Peterson algorithm doesn't break any rules. It uses a pointer to the last requester, and checks that no others want to go into the critical section:

```
foo() {
    while (true) {
        me = WANT_IN;
        last = &me;
        while (last == me && you == WANT_IN) {} //spin
        criticalSection();
        me = DONT_WANT_IN;
    }
}
```

While this doesn't break any rules, it assumes atomicity in assignment, and fails when bits are scrambled during simultaneous assignment.

### 4.17.8 $n$ -Thread Prioritized Entry

In a case where  $n$  threads want to enter a critical section, we modify the Peterson algorithm to use an array for priorities: We wait for all intents that are more important than us, then wait for all less-important coroutines to complete.

```
foo() {
    // step 1: wait for intents with higher priority to run:
    do {
        intents[priority] = WANT_IN;
        for (j = priority - 1; j >= 0; --j) {
            if (intents[j] == WANT_IN) {
                intents[priority] = DONT_WANT_IN;
                while (intents[j] == WANT_IN) {} // spin
                break;
            }
        }
    } while (intents[priority] == DONT_WANT_IN);
    // step 2: wait for intents with lower priority to complete:
    for (j = priority + 1; j < N; j++) {
        while (intents[j] == WANT_IN) {} //spin
    }
    criticalSection();
    intents[priority] = DONT_WANT_IN;
}
```

**This strategy breaks rule 5.** There are no algorithms that use only  $n$  bits<sup>9</sup>, are deterministically (non-probabilistically) correct, and assume atomic assignment.

---

<sup>9</sup>No, this is not  $O(n)$

### 4.17.9 *n*-Thread Bakery (Tickets)

In this implementation, we find the maximum value as “ticket number” in the array, and set our value to that. We then wait until we have the lowest ticket number in the array. In the case that multiple values are found in the array with the same ticket number, we also ensure we are the lowest priority with that value.

```
foo() {
    ticket[priority] = 0;
    int max = 0;
    for (int j = 0; j < n; j++) {
        int v = ticket[j];
        if (v != INT_MAX && max < v) max = v;
    }
    max += 1;
    ticket[priority] = max;
    for (int j = 0; j < n; j++) {
        while (ticket[j] < max || (ticket[j] == max && j < priority)) {}
        // spin
    }
    criticalSection();
    ticket[priority] = INT_MAX;
}
```

Since tickets cannot increase indefinitely, this is probabilistically correct. This also takes  $nm$  bits ( $m = 32$  for an int, for example).

### 4.17.10 *N*-Thread Peterson

This modification of the Peterson algorithm uses a round-based race section to find the “loser”.

```
foo() {
    for (int i = 1; i < N; i++) {
        intents[myId] == i; //current round
        turns[i] = myId; // MULTI-WAY RACE ALLCAPS
L:    for (int k=1; k <= n; k++) {
        if (k != myId && intents[k] >= i && turns[i] == myId) goto L;
    }
    criticalSection();
    intents[myId] = 0;
}
```

There are  $n - 1$  rounds, and each round has a loser, which implies that  $n - 1$  round winners are promoted. This can be implemented in only  $2n \lceil \lg n \rceil$  bits, but assumes atomic assignments.

### 4.17.11 Tournament (Taubenfeld-Buhr)

TODO: Determine how this works. TODO: High-Priority: Determine how this works. Explaining this strategy was on the F13 midterm and the W13 final.

### 4.17.12 Arbiter

We can always create an *arbiter* task that controls entry to the critical section.

```
client () {
    intent[me] = true;
    while (!serving[me]) {} //spin
    criticalSection();
    intent[me] = false;
    while (serving[me]) {} // wait for the arbiter to unblock me.
}
arbiter () {
    while (true) {
        for (; intent[i]; i = (i+1)%5) {} // busy wait
        serving[i] = true;
        while (intent[i] = true) {} // busy wait
        serving[i] = false;
    }
}
```

This implements mutual exclusion between the arbiter and each waiting client.

## 4.18 Hardware Solutions

Software solutions are limited by relying only on shared information between threads. Hardware solutions introduce a level below the software level, which allow us to make assumptions about execution (mainly atomicity). This only works on a single CPU - distributed programs don't have this hardware benefit.

We use hardware instructions to get great operations that are able to observe values *while* modifying them.

### 4.18.1 Test/Set Instruction

The test/set instruction just does that. It returns the old value while setting the value to the new value:

```
testSet(type newValue, type* lock) {
    ret = lock;
    lock = newValue;
    return ret;
}
foo() {
    while (testSet(CLOSED, lock) == CLOSED) {}
    criticalSection();
    lock = OPEN;
}
```

In a multi-CPU computer, somewhere in hardware<sup>10</sup> must guarantee multiple CPUs produce consistent output for this instruction.

---

<sup>10</sup>The hardware bus, specifically

### 4.18.2 Swap Instruction

The swap instruction performs a swap of two values:

```
swap(type& a, type& lock) {
    int temp;
    temp = a;
    a = lock;
    lock = temp;
}
foo() {
    dummy = CLOSED;
    do {
        swap(dummy, lock);
    } while (dummy == CLOSED);
    criticalSection();
    lock = OPEN;
}
```

### 4.18.3 Compare/Assign Instruction

The compare and assign (caa) instruction does assignment only if the two values being compared are equal<sup>11</sup>.

```
bool caa(type& value, type comp, type newValue) {
    if (value == comp) {
        val = newValue;
        return true;
    }
    return false;
}
foo() {
    while (!caa(lock, OPEN, CLOSED)) {}
    lock = OPEN;
}
```

### 4.18.4 Mellor-Crummey and Scott (MCS)

MCS provides a service bound by linking waiting threads on a queue and servicing queue in FIFO.

```
struct MCS::Node {
    size_t waiting;
    Node *next;
}
MCS::acquire(Node &n) {
    Node *pred;
    n.next = NULL;
    pred = fetchStore(&last, &n); // pred = last, last = n
```

---

<sup>11</sup>Some alternate implementations use inequality instead of equality.



```
    if (pred != NULL) {
        n.waiting = true;
        pred->next = &n;
        while (n.waiting) {} // spin
    }
}

MCS::release(Node &n) {
    if (n.next == NULL) {
        if (caa(&last, &n, NULL)) return; // the last is NULL, so nobody
            is waiting.
        while (n.next == NULL) {};
    }
    n.next->waiting = false; // start the next node.
}

foo() {
    MCS::Node n;
    Lock.acquire(n);
    criticalSection();
    Lock.release(n);
}
```

# Chapter 5

## Lock Abstraction

To build synchronization or mutual exclusion mechanisms, we build locks.

### 5.1 Lock Ontology

There are a bunch of different types of lock:

**Spinning Locks** busy wait until an event occurs. In uniprocessor systems, this lock can explicitly terminate its time slice by calling `yield`. In multiprocessor systems, it's better to begin yielding after  $n$  event checks fail.

**Blocking (Queueing) Locks** do not busy wait, but are unblocked by a mechanism until an event occurs.

### 5.2 Spin Lock

Spin locks are implemented using busy waiting, which loops checking for an event:

```
while (testSet(lock) == CLOSED); // spin
```

This is slow, since it loops until someone else opens the lock, or until it is pre-empted (i.e. when its time-slice ends). We can increase efficiency by yielding after the checking fails:

```
while (testSet(lock) == CLOSED) uThisTask().yield();
```

Even better, adaptive spin-locks modify the number of times that they fail before they yield<sup>1</sup>.

#### 5.2.1 Implementation

$\mu$ C++ provides the non-yielding spin lock `uSpinLock` and a yielding spin lock `uLock`:

---

<sup>1</sup>Apparently this is usually implemented using a \*shudder\* PID controller.

```

class uSpinLock {
public:
    uSpinLock(); // initializes to open
    void acquire();
    void tryacquire();
    void release();
}
class uLock {
public:
    uLock( unsigned int value = 1);
    void acquire();
    bool tryacquire();
    void release();
}

```

Starvation can theoretically occur, but it's rarely a problem.

Since `uSpinLock` is non-preemptive, no other tasks may execute on that processor once the lock is acquired<sup>2</sup>. The `uLock` provided is non-preemptive and so can be used for both synchronization and mutual exclusion.

The method `tryacquire` makes one attempt, but does not wait.

There is no problem with calling `release` extra times. In fact, `release` can be used to signal availability.

## 5.3 Blocking Locks

Blocking locks only make one check for openness before blocking. The releaser only needs to detect the blocked thread and transferring the lock.

In general, all blocking locks have:

- State to facilitate lock semantics
- A list of blocked acquirers
- A spin lock to protect list access and state modification

### 5.3.1 Synchronization Lock

The sync lock is used solely to block tasks waiting to be synchronized, and it only has the ability to block.

The acquiring task always blocks, and releases are lost when there is no waiting task.

Often, these are called condition locks.

---

<sup>2</sup>Apparently it is used extensively in the `μC++kernel`.

## Implementation

There are two types of synchronization locks:

**External Locking** uses external locks to protect task lists

**Internal Locking** uses internal locks to protect task lists

For both implementations, we need to use a binary semaphore to modify or read the task list.

For external implementations, we need to acquire the right to modify the list beforehand. Since we block after modifying the list, we need to release the modification right inside the acquire method:

```
uSpinLock* m = ...;
foo() {
    m.acquire();
    syncLock.acquire(m);
}

acquire(uSpinLock & m) {
    // add to list
    m.release();
    // Point A
    // yield and block
}
```

This is so awkward, and can still be interrupted at point *A*.

## uCondLock

$\mu$ C++ provides an internal synchronization lock, `uCondLock`.

```
class uCondLock {
public:
    uCondLock();

    /**
     * Returns false if there are tasks blocked on the queue and true
     * otherwise.
     */
    bool empty();

    /**
     * Used to block a task from the queue of a condition.
     * This is a blocking call, and re-acquires its argument
     * owner-lock before returning.
     */
    void wait(uOwnerLock &lock);

    /**
```

```

    * Used to un-block a task from the queue of a condition.
    * Tasks are blocked in FIFO order.
    */
    void signal();

    /**
     * Un-blocks all tasks.
     */
    void broadcast();
}

```

### 5.3.2 Binary Semaphore

Dijkstra invented the binary semaphore as a blocking equivalent of a yielding spin-lock.

This provides synchronization *and* mutual exclusion, since it remembers state about an event.

The man who invented this concept is dutch, and so are the names for acquire and release:

**Prolagen (P)** is the acquire method. It is called prior the critical section.

**Verlagen (V)** is the release method.

Semaphores with only two states (open/closed) are called **binary semaphores**.

#### Implementation

The implementation is really just a 3-tuple of:

- Blocking task list
- cnt indicates if event has occurred (i.e. if it is open).
- A spin lock to protect the state.

$\mu$ C++ does not provide a binary semaphore, since all binary semaphores are really just counting semaphores that only count up to 1.

```

BinSem::P() {
    lock.acquire();
    if (closed == true) {
        // add to blocked list
        // yield, block, and release lock
        lock.acquire(); // re-acquire the lock.
    }
    closed = true;
    lock.release();
}

```

```

BinSem::V() {
    lock.acquire();
    if (blocked.isEmpty()) {
        closed = false;
    } else {
        // remove from blocked list, and make it ready.
    }
    lock.release();
}

```

### 5.3.3 Mutex Lock

Restricting a lock to only performing mutual exclusion allows us to separate lock usage between synchronization and mutual exclusion while allowing us to optimize based on the singular function of the lock.

Mutex locks are divided into two types:

**Single Acquisition** locks are ones that can only be acquired by the lock owner once (recursively).

**Multiple Acquisition** locks are ones that can be acquired by the lock any number multiple times<sup>3</sup>.

#### Implementation

The easiest implementation is just to add an owner state to a binary semaphore. Some other implementations put the owner at the front of the queue, but this is messier.

#### uOwnerLock

$\mu$ C++ provides a multiple-acquisition mutex-lock, `uOwnerLock`.

```

class uOwnerLock {
public:
    uOwnerLock();

    /**
     * Returns NULL if there is no owner.
     */
    uBaseTask *owner();

    /**
     * Returns the number of times the lock has been acquired by the
     * owner.
     */
    unsigned int times();
}

```

---

<sup>3</sup>Apparently some implementations can be released any number of times, while some only need to be released once.

```

/**
 * Refer to uLock.acquire();
 */
void acquire();

/**
 * Refer to uLock.tryacquire();
 */
bool tryacquire();

/**
 * Refer to uLock.release();
 */
void release();
}

```

## Stream Locks

$\mu$ C++ offers a special mutex for I/O based on `uOwnerLock` named `osacquire` and `isacquire` for output and input streams respectively.

The two are classes that acquire the object at the beginning of their lives (constructor), and release the object at the end of their lives (destructor).

### 5.3.4 Counting Semaphore

By changing the boolean in the binary semaphore to an integer that represents the number of remaining “events”, we can:

- Have critical sections that allow  $n$  simultaneous tasks.
- Allow  $n$  tasks to execute only after a certain task has completed.

## Implementation

We can implement this by changing the flag into a counter and setting it to the maximum on creation. Then we’d decrement on acquisition, and increment it on release:

```

CntSem::P() {
    lock.acquire();
    --cnt;
    if (cnt < 0) {
        // add self to lock's blocked list
        // magically yield, block, and release lock.
        lock.acquire();
    }
}

```

```

    lock.release();
}
CntSem::V() {
    lock.acquire();
    ++cnt;
    if (cnt <= 0) {
        // remove task from blocked list and make ready
    }
    lock.release();
}

```

## uSemaphore

$\mu$ C++ provides a counting semaphore named `uSemaphore` which offers more than just a binary semaphore:

```

class uSemaphore {
public:
    uSemaphore(unsigned int counter = 1);
    /**
     * Decrements the semaphore counter.
     * If the semaphore counter is >= 0 the caller continues, otherwise
     * it blocks.
     */
    void P();

    /**
     * TryP returns true if the semaphore has been acquired and false
     * otherwise (it basically is P without blocking)
     */
    bool tryP();

    /**
     * Wakes up the task blocked for the longest time if there are tasks
     * blocked on the semaphore.
     * [times is the number of tasks woken up]
     */
    void V(unsigned int times = 1);

    /**
     * Returns the value of the semaphore counter:
     * n<=0 means abs(n) tasks are blocked and the semaphore is locked
     * n>0 means that there are n tasks that are allowed to acquire the
     * semaphore, and it is unlocked.
     */
    int counter() const;

    /**
     * Returns false if there are threads blocked on the semaphore (and
     * true otherwise).

```



```

    */
    bool empty() const;
}

```

### 5.3.5 Barrier

A barrier coordinates a group of tasks performing a concurrent operation surrounded by sequential operations. Thus, it is only for synchronization and not mutual exclusion.

Barriers are initialized to  $n$ , the number of tasks they will hold before allowing them to continue.

Tasks call `block`. The  $n$ th task will allow all tasks to continue.

These can only be used for mutual exclusion.

#### **uBarrier**

$\mu$ C++ barriers are thread-safe coroutines where the main can be resumed by the final task arriving at the barrier<sup>4</sup>.

```

_Cormonitor uBarrier {
protected:
    void main { for (;;) { suspend(); } }
public:
    uBarrier( unsigned int total );

    /**
     * returns the number of tasks being synchronized
     */
    unsigned int total() const;

    /**
     * returns the number of currently waiting tasks
     */
    unsigned int waiters() const;

    /**
     * resets the number of tasks synchronizing to to 'total'
     */
    void reset( unsigned int total );

    /**

```

---

<sup>4</sup>The macro named “\_Cormonitor” is defined to be “\_Mutex \_Coroutine”. Not that you’d know what a monitor is yet, see Section 7.3.

```

    * Wait for the nth thread. The nth thread unblocks and calls the
      last.
    */
virtual void block();

/**
 * This is implicitly called by the last task to arrive to the
   barrier.
 */
virtual void last() { resume (); }
}

```

We can create a barrier by inheriting `uBarrier`, and redefining `main` and possibly the `block` method. We may even want to initialize `main` from a constructor. Here's an example of us creating a barrier:

```

_Cormonitor Accumulator : public uBarrier {
    int total_;
    uBaseTask* nth_;
    void main() {
        nth = &uThisTask();
        suspend();
    }
public:
    Accumulator( int rows ) : uBarrier ( rows ), total_(0), nth_(0) {}
    void block( int subtotal ) {total += subtotal; uBarrier::block(); }
    int total() { return total_; }
    uBaseTask* nth() { return nth_; }
}

_Task Adder {
    int *row, size;
    Accumulator &acc;
    void main() {
        int subtotal = 0;
        for (unsigned int r = 0; r<size; r++) subtotal += row[r];
        acc.block(subtotal);
    }
public:
    Adder (int row[], int size, Accumulator &acc) :
        size( size ), row( row ), acc( acc ) {}
}

```

## 5.4 Lock Programming

### 5.4.1 Synchronization Locks

Synchronization locks are weak, since we need to provide external mutual exclusion because they are weak.

### 5.4.2 Precedence Graph

P and V in conjunction with COBEGIN are as powerful as the START and WAIT commands.

Given a list of statements where the result is compounded in some parts, we can use the graph to analyze which code and data depend on each other:

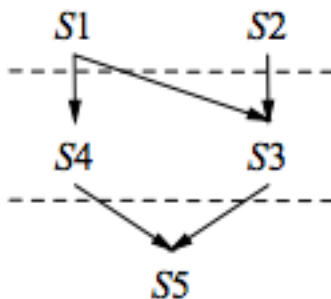
```

S1: a := 1
S2: b := 1
S3: c := a + b
S4: d := 2 * a
S5: e := c + d

```

By analyzing what data and code depends on each other, we can create a graph of dependencies, as seen in Figure 5.4.2. We can equivalently express this initial code using a bunch of semaphores:

Figure 5.1: Sample Precedence Graph



```

Semaphore L1(0), L2(0), L3(0), L4(0)
COBEGIN
    BEGIN a:= 1; v(L1); END;
    BEGIN b:= 2; v(L2); END;
    BEGIN P(L1); P(L2); c :=a + b; v(L3); END;
    BEGIN P(L1); d := 2 * a; v(L4); END;
    BEGIN P(L3); P(L4); e := c + d; END;
COEND

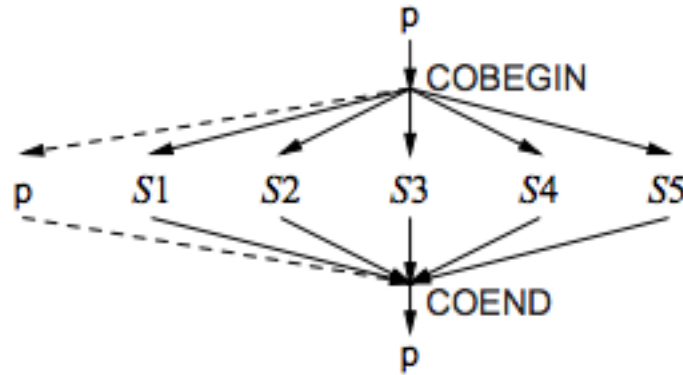
```

Similarly, we can create a process graph as seen in Figure 5.4.2.

### 5.4.3 Buffering

In most cases, tasks communicate in a single direction using a queue, where producers push to the queue, and consumers pop from the queue.

Figure 5.2: Sample Process Graph



## Unbounded Buffer

Two tasks will communicate through a queue of unbounded length. The producer may work faster than the consumer, but this is ok since the buffer is infinite length. Consumers need to wait for producers to add if they work faster than the producer.

```

#define QueueSize infinity

int front = back = 0;
int Elements[QueueSize];
uSemaphore signal(0);

void Producer::main() {
    while (true) {
        // append to queue
        signal.V();
    }
    queue.append(END_SIGNAL);
}

void Consumer::main() {
    while (true) {
        signal.P();
        value = queue.pop();
        if (value == END_SIGNAL) break;
        // use the value
    }
}
  
```

This is an instance where a semaphore is used for synchronization.

The problem with unbounded buffers is that they take infinite memory<sup>5</sup>.

<sup>5</sup>A producer moving faster than a consumer will cause the buffer to grow with respect to time. See Sub-Section 5.4.3.

## Bounded Buffer

Because the queue is like bounded, producers need to wait if the buffer is full.

We're going to use two counting semaphores for the finite length of the shared queue:

```
uSemaphore full(0), empty(QueueSize);
void Producer::main() {
    for (...) {
        item = ...
        empty.P(); // Reserve a space in the queue to append our value.
        queue.append(item);
        full.V(); // Indicate that there is a takeable item in the queue
    }
}
void Consumer::main() {
    for (...) {
        full.P(); // reserve the right to take a value
        x = queue.pop();
        empty.V(); // increase the number of values remaining
        ...
    }
}
```

This produces decent concurrency, but definitely not maximum concurrency. This also allows multiple producers and multiple consumers.

### 5.4.4 Lock Techniques

We want to implement a split binary semaphore - a collection of semaphores which at most one has the value 1.

We use a technique named **baton passing** which passes a (conceptual) baton<sup>6</sup> between different tasks that wait on it. The baton is acquired in entry and exit protocol, and is passed from signaller to signalled task.

```
class BinSem {
    queue<Task> blocked;
    bool inUse;
    SpinLock lock;
public:
    BinSem( bool usage = false ) : inUse( usage ) {}
    void P() {
        lock.acquire(); // Pick up baton. Now we are 'allowed' to access
                        // state.
        if ( inUse ) {
            // add self to lock's blocked list
            // yield, block and release lock at the same time

            // When unblocked:
```

---

<sup>6</sup>The baton is implemented as a semaphore.

```

        // We've been passed the baton. Now we can access state.
    }
    inUse = true;
    lock.release();
}
void V() {
    lock.acquire(); // Pick up the baton. Now we are 'allowed' to
                    // access state.
    if (!blocked.isEmpty()) {
        // remove the task from the blocked list and make it ready.
        // At this point, we've passed the baton, and cannot access
        // state
    } else {
        inUse = false;
        lock.release();
    }
}
}
}

```

#### 5.4.5 Readers and Writer Problem

When there are multiple tasks that share reading and writing to a resource, we want to ensure that we are able to allow multiple concurrent readers while serializing access for writer tasks (writers may read as part of their write process).

We're going to use split binary semaphores to segregate 3 kinds of tasks: arrivers, readers, and writers.

#### Solutions 1-6

These solutions have various problems. Here is an itemized list:

- If we allow the readers to go first, it starves the writers.
- If we allow the writers to go first, it starves the readers<sup>7</sup>.
- When tasks exit, they should activate the type that isn't their own. The problem this creates is that due to the way that they can be condensed, readers that arrive after a writer may be reading stale data. We should service readers and writers in **temporal order**.
- When groups arrive, we should concatenate spans readers with no writers in between. The textbook argues that *Now we lose kind of waiting task!*. I'm unsure of what this means<sup>8</sup>.
- If we create a "next up" chair... I don't really understand this one.
- If we create a ticked method (See Sub-Section 4.17.9), we can get readers and writers to take a ticket before releasing the baton. Starvation is not an issue, but this isn't efficient.
- If we had a list of private semaphores... I don't really understand this one either.

<sup>7</sup>If it is 80% readers and 20% writers, course notes claim this works experimentally.

<sup>8</sup>I'd postulate that this means that we'll have more waiters, but I'm not sure.

**Solution 7**

Generally speaking, we want a solution that provides:

1. Execution in temporal order
2. A smaller (or simpler) solution
3. An efficient solution

We are going to create an ad-hoc solution that uses questionable split-binary semaphores and baton-passing.

Tasks wait in temporal order for an entry semaphore. Writers wait on the writer chair until readers leave the resource, holding the baton until all readers leave. Semaphore lock is only used for mutual exclusion.

```

uSemaphore entry_q(1);
uSemaphore lock(1), writer_q(0);
void Reader::main() {
    entry_q.P(); // entry protocol.
    lock.P();
    r_cnt++;
    lock.V();
    entry_q.V(); // put the baton down
    ...
    lock.P(); // exit protocol
    r_cnt--;
    criticalSection();
    if (r_cnt == 0 && w_cnt == 1) { // if last reader and there is a
        writer waiting
        lock.V();
        writer_q.V(); // pass the baton.
    } else {
        lock.V();
    }
}
void Writer::main() {
    entry_q.P(); // entry protocol
    lock.P();
    if (r_cnt > 0) { // are there readers waiting?
        w++;
        lock.V();
        writer_q.P(); // wait for readers
        w_cnt--; // unblock with baton
    } else {
        lock.V();
    }
    criticalSection();
    entry_q.V();
}

```

# Chapter 6

## Concurrent Errors

### 6.1 Race Condition

Race conditions occur when we are missing synchronization or mutual exclusion. Two or more tasks race along assuming that synchronization or mutual exclusion has occurred. The easiest way to locate errors is through thought experiments, which are personally taxing.

### 6.2 No Progress

#### 6.2.1 Live-Lock

Live-lock is when there is indefinite postponement. This is essentially caused by poor scheduling in an entry protocol. To fix this, there always is some mechanism to break ties on simultaneous arrival that deals effectively with live-lock.

#### 6.2.2 Starvation

When a selection algorithm ignores  $n \geq 1$  tasks so they are never executed, the  $n$  tasks are starved.

While infinite starvation is very rare in real codebases, short-term starvation can occur and is problematic.

Like a live-lock (see Sub-Section 6.2.1), this includes situations where the starving task may only really switch between active, ready, and possibly blocked states.

#### 6.2.3 Dead-Lock

Deadlock is a state when  $\geq 1$  processes are waiting for an event that will never occur.



## Synchronization Deadlock

This occurs when  $\geq 1$  processes are waiting for synchronization that will never occur.

## Mutual Exclusion Deadlock

This occurs when processes fail to acquire resources protected by mutual exclusion. There are 5 conditions for mutex-based deadlock to happen:

1. There exists more than 1 shared resource requiring mutual exclusion.
2. A process holds a resource while waiting for access to a resource held by another process (hold and wait).
3. Once a process has gained access to a resource, the runtime system cannot get it back (no preemption).
4. There exists a circular wait of processes on resources.
5. These conditions must occur simultaneously.

## 6.3 Deadlock Prevention

We want to eliminate at least one<sup>1</sup> of the conditions required for a deadlock from an algorithm to force deadlock to never occur.

### 6.3.1 Synchronization Prevention

We can eliminate all synchronization from the program to prevent synchronization-based deadlock. This removes communication, which means that they must generate results through side-effects (ew).

### 6.3.2 Mutual Exclusion Prevention

We can eliminate deadlock by eliminating any 1 or more of the 5 conditions:

**No mutual exclusion** In many cases, it is impossible to do this while maintaining concurrency.

**No hold and wait** We can implement this by not giving any resources to a process unless all requested resources can be given. This poorly utilizes resources, and introduces the possibility that we may starve a thread.

**Allow Preemption** Since preemption is dynamic, we cannot apply this statically.

**No Circular Wait** We can prevent circular wait from happening by only acquiring resources according to an ordering. Threads can only acquire a resource  $R_j$  if they hold no resources  $R_i$  where  $i \geq j$ .

---

<sup>1</sup>Hopefully we can eliminate more than only one of the conditions. Doing this will increase concurrency.

**Prevent Simultaneous Occurrence** We can do this by proving that the four previous rules cannot occur at the same time<sup>2</sup>.

## 6.4 Deadlock Avoidance

Unlike deadlock prevention, deadlock avoidance monitors all blocking and allocation and detects the formation of deadlocks. This gives us better resource allocation at the expense of overhead.

The difference between Deadlock past exams.

### 6.4.1 Banker's Algorithm

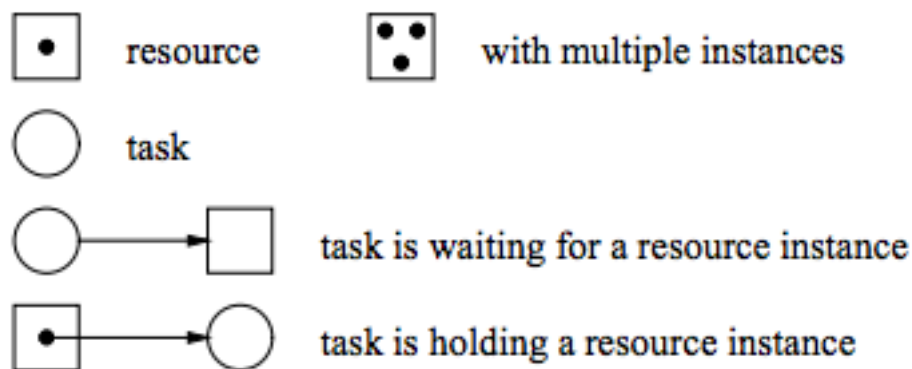
The bankers algorithm is an iterative approach: We require threads  $T_i$  to declare the maximum number of each type of resource that they need to complete<sup>3</sup> execution. Knowing the total available resources, we check that at least one thread is able to complete to execution<sup>4</sup> every time we allocate a resource.

This algorithm works, but it is limited to instances where all threads will declare how much they need prior to execution.

### 6.4.2 Allocation Graphs

One way to check for potential allocation is by analyzing graphs of resource allocation, as seen in Figure 6.4.2. Once we have our graph, we use reductions from a complicated graph to less complicated but equivalent

Figure 6.1: Sample Allocation Graph



graphs to locate deadlocks. Cycles existing in our graph indicate that we have a deadlock.

Since detecting cycles is slow, and it needs to be done at every allocation and de-allocation step, this is expensive.

<sup>2</sup>There's always one dumb rule.

<sup>3</sup>For example,  $T_3$  needs 5 of  $R_1$ , 9 of  $R_2$ , 0 of  $R_3$ , and 1 of  $R_4$ .

<sup>4</sup>We check that releasing resources allows others to complete until all threads are done.

## 6.5 Deadlock Detection and Recovery

The idea is instead of preventing deadlocks from happening, let's just recover when they do.

We only really have to check for a deadlock when a resource can't be allocated immediately<sup>5</sup>. Recovery involves preempting<sup>6</sup>  $\geq 1$  processes, and restarting them at their beginning, or at a safe point. This isn't safe, since the victim may have made changes prior to preemption.

---

<sup>5</sup>Checking every  $t$  seconds is a second option, but I sraig postulates that a two-pronged approach is best. There is no comment about a two-pronged approach in the course notes.

<sup>6</sup>Preemption here means basically killing, restarting, or rewinding to a *safe point*.

## Chapter 7

# Indirect Communication

P and V are low-level primitives that protect critical sections, and establish synchronization between locks. This can be complicated, and can be incorrectly placed. Split-binary semaphores, and baton passing is hard too. We need higher language-level facilities that give us these things for free.

### 7.1 Critical Regions

Using the pseudocode-like language which we used for COBEGIN and COEND (see SubSection 5.4.2):

- We can indicate shared variables (i.e. `v` is protected by the `MutexLock` named `v_lock`:

```
VAR v: SHARED INTEGER MutexLock v_lock;
```

- Access to shared variables is only possible from within a `REGION` statement:

```
REGION v DO                v_lock.acquire();
    // critical section    v++; // etc
END REGION                 v_lock.release();
```

- As explained in SubSection 6.2.3, ordering these `REGION` calls can create deadlock.

This implementation prevents simultaneous reads. If we modified it so we can read outside the critical section, we may be reading partially updated information (ew).

### 7.2 Conditional Critical regions

In our make-believe language, we're going to introduce a condition that must be true inside the mutual exclusion blocks:

```

REGION v DO
    AWAIT conditional-expression
    ...
END REGION

```

If `conditional-expression` is false, the lock is released, and entry is re-started.

## 7.3 Monitor

A monitor is an abstract data type that combines shared data with serializing modification. The key feature offered by a monitor is its differentiating set of **mutex members**<sup>1</sup>. Of the mutex members, only one may be actively executed at a time. Managing tasks entering and exiting from the mutex is managed automatically by the mutex.

Basically, each monitor has a lock which is `P`ed on entry to a monitor member, and `V`ed on exit.

```

class MonitorDemo {
    MutexLock mLock;
    int v;
public:
    int x() {
        mLock.P();
        int temp;
        try {
            ...
            temp = retVal;
            mLock.V();
        } catch (Err &e) {
            mLock.V();
            throw; // re-throw
        }
        return temp;
    }
}

```

Unhandled exceptions implicitly release the lock so the monitor can continue to function. Recursive entry is allowed<sup>2</sup>. Also, the destructor is blocked by a mutex, so threads can't be caught inside a monitor.

## 7.4 Scheduling (Synchronization)

A monitor may want to schedule tasks in an order different from the order they arrive.

**External Scheduling** occurs outside the monitor and is accomplished using the `accept` statement.

---

<sup>1</sup>Short for mutual-exclusion member.

<sup>2</sup>Otherwise monitors wouldn't be able to call their own methods, and recursion would be impossible without boilerplate code.

**Internal Scheduling** occurs inside the monitor and is accomplished using the condition variables with signal and wait.

### 7.4.1 External Scheduling

In a nutshell, accept statements block the active task on the acceptor stack and makes a task ready from the specified mutex member queue. Signals move a task from the specified condition to the signaled stack.

We use the  $\mu\text{C++}$  `_Accept` statement to control which mutex can accept calls. By preventing members from accepting calls at different times, we can control scheduling of tasks. The `_Accept` statement defines what cooperation must occur for the accepting task to proceed:

```
_Monitor BoundedBuffer {
    int front, back, count;
    int elements[20];
public:
    BoundedBuffer() : front(0), back(0), count(0) {}
    _Nomutex int query() { return count; }
    [_Mutex] void insert( int elem );
    [_Mutex] int remove();
};

void BoundedBuffer::insert(int elem) {
    if (count == 20) _Accept( remove );
    elements[back] = elem;
    back = (back+1) % 20;
    count ++;
}

void BoundedBuffer::remove() {
    if (count == 0) _Accept( insert );
    // waits for insert to be called, then continues.
    int elem = elements[front];
    front = (front + 1)%20;
    count -= 1;
    return elem;
}
```

This implicitly queues tasks that wait outside the monitor for either `insert` or `remove` operations. Accepters block until a call to the specified mutex member occurs. When the accepter blocks, it is added to a stack of blocked accepters (across all methods). External scheduling is simple because unblocking (signalling) is implicit.

### 7.4.2 Internal Scheduling

In a nutshell, implicit scheduling occurs when a task waits in or exits from a mutex member, and a new task is selected first from the A/S<sup>3</sup> stack, then the entry queue. Scheduling occurs for tasks inside the monitor, where **conditions** are used to create a queue of waiting tasks. A task waits by waiting for a `uCondition x`

---

<sup>3</sup>I believe that A/S is accept/signal.

to be true by calling `x.wait()`. This atomically puts it at the back of the condition queue, and allows another task into the monitor by releasing the monitor lock.

The `uCondition.empty` method returns false if there are tasks blocked on the queue. Similarly, the `uCondition.front` method returns an integer value stored with the task at the front of the condition queue.

A task on a condition queue can be made ready by signaling the condition `x.signal()`<sup>4</sup>. This readies the task, but it waits until the currently executing task is out of the monitor before continuing. The dual of the `signal` method, `x.block()` unblocks the thread and blocks the signaler.

Generally, the entry queue is a fifo list of calling tasks to the monitor.

## 7.5 Readers/Writer

See Subsection 5.4.5 to see the statement.

We can use monitors to implement solutions to the readers/writer problem much more elegantly than if we were only using the `uSemaphore` construct. Here is a sample of the final solution proposed, using monitors instead:

```
_Monitor ReadersWriter {
    int rcnt, wcnt;
public:
    ReadersWriter() : rcnt(0), wcnt(0) {}
    void endRead() {
        --rcnt;
    }
    void endWrite() {
        wcnt = 0;
    }
    void startRead() {
        if (wcnt > 0) _Accept( endWrite );
        rcnt++;
    }
    void startWrite() {
        if (wcnt > 0) _Accept ( endWrite );
        else while ( rcnt > 0 ) _Accept( endRead );
        wcnt = 1;
    }
}
```

## 7.6 Condition Signal and Wait vs Counting Semaphore P and V

We'd like to draw the distinction between these different types:

---

<sup>4</sup>Empty signals are lost.

Calling the `wait` method always blocks, while `P` only blocks if the semaphore's value is less than or equal to 0.

Calling a `signal` is lost, while calling `V` before `P` affects the `P`.

Calling a `V` may start multiple simultaneous tasks while multiple `signals` only start one task at a time since they must exit serially through the monitor.

We can simulate `V` and `P` through a monitor:

```
_Monitor semaphore {
    int sem;
    uCondition semcond;
public:
    semaphore(int cnt = 1) : sem (cnt) {}
    void P() {
        if (sem == 0) semcond.wait();
        --sem;
    }
    void V(int incr = 1) {
        sem += incr;
        for (int i=0; i<incr; i++) semcond.signal();
    }
}
```

## 7.7 Monitor Types

Through different languages and implementations, there are subtle different ways that monitors can be implemented. These are usually based on their scheduling of the monitor when tasks wait, signal, and exit.

Scheduling is the ordering of priorities for re-entering the mutex by different thread categories:

**C** are the calling threads that haven't entered the mutex yet.

**W** signalled (waiting) threads are the threads that were blocked then moved to a ready queue.

**S** signaller threads are the ones that have signalled another thread and released control until completion<sup>5</sup>.

To say that  $C < W < S$  means that when the mutex is choosing the next thread, it will choose the **S** threads before **W**, and before **C** threads.

Monitors may either implicitly (statement) or explicitly signal (automatic signal). Monitors that implicitly signal may wait on condition variables or an explicit signal statement like `waitUntil logicalExpression`. Monitors that explicitly signal call methods similar to `signal/signalWait`.

Additionally, some monitors may be constrained to always return (leave the monitored method) as they signal.

Refer to Table 7.1 for ten different types of control flow that are practically useful.

---

<sup>5</sup>This doesn't make intuitive sense. TODO: verify.



## 7.8 Java and C#

Java's concurrency constraints are descendants of Modula-3.

## 7.9 Threading Model

It basically defines a thread that extends a runnable:

```
interface Thread implements Runnable {
    public Thread();
    public Thread(String name);
    public String getName();
    public void setName(String name);
    public void run();
    public synchronized void start();
    public final void join(); // waits for the thread to die
    public static Thread currentThread(); // returns the current
        thread
    public static void yield(); // yields the processor immediately
}
```

Similarly in  $\mu\text{C}++$ , we have the `uBaseTask` that all tasks inherit from:

```
class myTask extends Thread {
    private int arg;
    private int result;
    public myTask( ... ) { ... } // task constructor
    public void run() { ... } // task main
    public void result() { return result; }
}
```

Java's implementation starts threads when users call the `start` method, and termination synchronization is accomplished by calling `join`. Returning a result on thread termination is accomplished by adding member methods to the thread.

## 7.10 The Synchronized Keyword

In Java, `synchronized` class members help preserve mutual exclusion (should be named something along the lines of `_Mutex`).

## 7.11 Wait, Notify, and NotifyAll

All classes have one implicit condition variable and these routines to manipulate it:

```
public wait();
```

```
public notify();
public notifyAll();
```

### 7.11.1 Implementing a Barrier

Let's walk through some common pitfalls of implementing barriers (or anything) using `wait/notify/notifyAll`.

```
class Barrier {
    private int n;
    private count = 0;
    private generation = 0;
    public Barrier(int n) { this.n = n }
    public synchronized void block() {
        int mygen = generation;
        count++;
        if (count < n) {
            // We need the while loop because interrupted exceptions can
            // cause unwanted operations.
            // Yes, a better way to do this would be to 'deal with' the
            // InterruptedException on occurrence.
            while (mygen == generation)
                try {
                    wait();
                } catch (InterruptedException e) {}
        } else {
            // If we merely decrement the counter, an un-blocker that
            // re-enters before others leave causes... bad things.
            count = 0;
            generation++;
            notifyAll();
        }
    }
}
```

### 7.11.2 Implementing Conditions

We can't implement `_Mutex`-style conditions in Java, since that would allow us to hold the mutex while waiting for a condition to be true (that someone in turn needs access to the monitor to modify. You get the idea.).

Table 7.1: Ten Different Types of Control Flow That Are Practically Useful

Signal Type	Priority	No Priority	notes
Blocking	Priority Blocking (Hoare) $C < S < W$ ( $\mu C++$ 's signalBlock)	No Priority Blocking $C = S < W$	The blocking variant requires the signaller to recheck the waiting condition in the case of a barging task. The non-blocking variant requires the signalled task to check the waiting condition in the case of a barging task.
Non-Blocking	Priority Non-Blocking $C < W < S$ ( $\mu C++$ 's signal)	No Priority Non-Blocking $C = W < S$ (Java/C#)	Both types have no barging (more on this later). The non-blocking variant optimizes signal before return, and the blocking variant handles internal cooperation within the monitor.
Quasi-Blocking	Priority Quasi-Blocking $C < W = S$	No Priority Quasi-Blocking $C = W = S$	This makes cooperation incredibly difficult.
Immediate Return	Priority Return $C < W$	No Priority Return $C = W$	Not powerful to handle most cases, but are simple to use in the most common case.
Implicit Signal	Priority Implicit Signal $C < W$	No Priority Implicit Signal $C = W$	Good for prototyping but poor performance.

## Chapter 8

# Direct Communication

While monitors work well for shared objects that therefore need mutual exclusion. Communication using a monitor is indirect, and clunky.

### 8.1 Task

A task is like a coroutine since it has a distinguished member which has its own execution state. Unlike coroutines, tasks have their own thread. Public members of a task implicitly have the `_Mutex` term; an external scheduler blocks the task's thread.

Refer to Table 8.1 to see some differences between different constructs.

Table 8.1: Execution, Member, and Object Properties

Object Properties		Member Routine Properties	
Thread	Stack	No S/ME <sup>1</sup>	S/ME
No	No	class	monitor
No	Yes	coroutine	coroutine-monitor
Yes	No	<i>reject</i> <sup>2</sup>	<i>reject</i>
Yes	Yes	<i>reject</i> <sup>?</sup>	coroutine-monitor

### 8.2 Scheduling

A task may want to schedule access to itself by other tasks in a non-temporal order. Similar to monitors, we can do this through either internal or external scheduling.

#### 8.2.1 External Scheduling

Just like a monitor, tasks can use the `_Accept` statement to control which mutex members of a task can accept calls.

The when-accept setup can be expressed as the context-free-grammar:

```

S -> T | T ELSE
T -> WHEN ACCEPT CODE | T or T
WHEN -> _When(CONDITION) | {nothing}
ACCEPT -> _Accept(METHOD)
METHOD -> [method] | METHOD, METHOD
ELSE -> _Else { ... }

```

In the CFG, CODE is executed after only after the `_Accept` returns.

Like a switch statement, if the accepts are conditional and false the statement does nothing.

The optional if-like-statement `_When` only allows execution of the `_Accept` to progress if calls to the method exist, and the condition evaluates to true.

If there is an `_Else` clause and no `_Accept` can be executed immediately, the `_Else` is executed.

## Accepting Order

Whenever a thread accepts, this is what happens:

- Acceptor calls `_Accept(M1)`.
- Acceptor is pushed on the acceptor/signalled stack.
- The accepted method is added to the acceptor/signalled stack.
- Normal/implicit scheduling occurs according to  $C < W < S$  (See Section 7.7 on monitor types/notation).
- After that accept call has completed or the caller waits<sup>3</sup>

### 8.2.2 Accepting the Destructor

To terminate tasks, we sometimes want to have a `join` method. We could implement this as part of our main method's bajillion `_Accept` statements, since it allows us to access the contents of the task after termination.

Alternatively, we can just `_Accept` on the destructor like many of our other methods:

```

void Foo::main() {
    while (true) {
        _Accept( ~Foo ) {
            break;
        } or ....
    }
}

```

---

<sup>3</sup>I don't know of a reason. TODO: when does this happen? TODO: this doesn't make grammatical sense.

The semantics for accepting a destructor aren't the same as a normal mutex member. When destructors are called, the caller is pushed on the acceptor/signalled stack instead of the acceptor.

This allows it to clean up before it terminates.

After destruction, the task behaves a monitor since threads can only enter once at a time anyways.

Through an unspecified process, the destructor can reactivate any blocked tasks on condition variables and/or the acceptor/signalled stack.

### 8.2.3 Internal Scheduling

This is almost identical to monitor scheduling. See SubSection 8.2.3.

## 8.3 Increasing Concurrency

Given that you have  $\geq 2$  tasks involved in direct communication<sup>4</sup>, it is still possible to increase concurrency on both sides.

### 8.3.1 Server Side

When using `_Tasks`, you have some concurrency when you're using `_Accept` statements for methods. You have a bit more concurrency by only doing administration in the external method and doing work in the `_Task` of executing the method as follows:

```
_Task server {
  public:
    void mem1(...) { S1.copy-in}
    void main() {
      _Accept( mem1 ) { S1.work }
    }
}
```

### Internal Buffers

We can use internal buffers to send messages between client and server. Since the size is greater than 1, clients can get in and out of the server faster.

The problem is that unless the average production and consumption time is the same, the buffer will always be full or empty. Since the buffer is inside the task, clients still need to wait for the task to append things to it. Clients that require responses are super messed as well.

One way is to have a worker task that aggregates and “handles” calls for the server. The number of workers needs to balance between the number of clients to maximize concurrency throughout the application (or else we have the unbounded buffer problem).

---

<sup>4</sup>A good example is a server-client relationship.

## Administration

Administrators are servers that do nothing other than manage multiple client and worker tasks. They delegate, receive, verify, route, and check work.

Administrators make no calls since that may block them. Since they are the heart of the communication channels, this would be bad.

Typical worker types are:

**timer** prompts the administrator at specified intervals

**notifier** performs a potentially blocking wait for an external event

**simple worker** performs work given to them and returns results to an administrator

**complex worker** performs work given to them and interacts directly to the of the work

**courier** performs a potentially blocking call on behalf of the administrator

### 8.3.2 Client Side

While servers can try to make a clients delay as short as possible, not all servers do it.

We can overcome variable wait for the server to process a request by using asynchronous calls. These calls require implicit buffering between client and server to store the client's arguments from the call. Though  $\mu\text{C++}$  doesn't provide this functionality, we can (simply?) construct asynchronous from synchronous and vice versa.

## Returning Values

If a client doesn't need results, asynchronous calls are simple.

When we need to return results, life isn't as simple. We can divide calls into two calls:

```
callee.start(args);
// do other work
x = callee.finish(); // equivalent to join.
```

The caller blocks on the finish statement for the result. Depending on implementations, sometimes we need to implement a polling system where the `finish` method polls the server for status until it's completed.

## Tickets

Another form of protocol is a token or a ticket.

It calls twice:

- Transmits the arguments and immediately returns the ticket.

- The second call passes the ticket and blocks for the result.

This can be bad if the caller doesn't retrieve the result.

### Call-Back Routine

Callers can register a callback routine with the server task. Usually, callers will release a mutex lock in the callback, and wait for it in the `finish` call.

The advantage is that servers don't need to store the result and can drop it off immediately. The other advantage is that the client can write the callback routine.

The disadvantage is that the client gets to write the callback routine.

### Futures

A future prevents exposing the explicit protocol on how this returning values is implemented. This means that callers don't need to know how to:

- Poll
- Handle callbacks

Futures are sub-types of the objects that they extend.

We can use futures as follows:

```
future = callee.work(args);
...
// obtains result, blocking if necessary
i = future + ...
```

Futures are guaranteed to return empty results immediately. They are lazily loaded by another thread in the future, when the result is ready. Callers using the future before it is filled are blocked implicitly.

$\mu$ C++ implements two types of templated futures:

**Explicit-Storage-Management future** (`Future_ESM<T>`) must be allocated and deallocated by the client.

**Implicit-Storage-Management future** (`Future_ISM<T>`) allocates and frees storage when no longer in use.

We focus on `Future_ISMs`, since they're simpler, but less efficient.

### 8.3.3 Using Futures

We can use futures on either client or server code.



## Futures for Client Code

Here is an example of using a future for client-side stuff.

```
#include <uFuture.h>
Server s;
Future_ISM f[10];
for (int i=0; i<10; i+=1)
    f[i] = server.perform(i); // start async call

...
for (int i=0; i<10; i+=1)
    osacquire(cout) << f[i] << " " << f[i]() << endl;
```

The header for Future\_ISM is as follows:

```
template <typename T>
class Future_ISM/Future_ESM { // _Mutex?
public:
    /**
     * Returns true iff the async call has completed.
     */
    bool available();

    /**
     * Returns a read-only copy of the future.
     * Blocks if the future is unavailable.
     * Raise an exception if one comes from the server.
     */
    operator()();

    /**
     * Returns a read-only copy of the future result.
     * Can only be performed if available is true
     */
    operator T();

    /**
     * Returns true iff the future is cancelled
     */
    bool cancelled();

    /**
     * Attempts to cancel the async call the future refers to.
     * Clients are unblocked and thrown a Cancellation exception is
     * thrown.
     */
    void cancel();

    /**
     * uC++ error thrown.
```

```

    */
    _Event Cancellation{};

    /**
     * Marks the future as empty so it can be re-used.
     */
    void reset();

    /**
     * Make the result available in the future.
     * No documentation on what the return type is.
     */
    bool delivery(T result);

    /**
     * Forcibly enclose the exception into the future
     */
    bool exception(uBaseEvent *ex);
}

```

### **`_Select Statement`**

Like `_Accept` statements, `_Select` statements are provided to  $\mu\text{C}++$  for our immense benefit.

Essentially, they wait for some boolean combination of futures to be true.

```

_Select(f1 || f2) {
    ...
}

```

The block will only be executed once either `f1` or `f2` has returned.

```

_When(conditions) _Select (f1) {
    ...
} or _When (conditions) _Select (f2) {
    ...
}

```

We can prevent `_Select` statements from being blocking using a terminating `_Else` clause:

```

_Select(cond1)
    ...
_When(cond2) _Else
    ...

```

As usual, `_Else` clauses must be the last clause of a select statement. If a `_While` guard is omitted, then the `_Else` clause is executed, and control continues.

## 8.4 Go

Go is a cool language:

Feature	Provided?	Notes
Threads	Yes	goroutines are started with the go command
Synchronization	Yes	Channel with buffer size 0
Direct Communication	Yes	Channel with buffer size 0
Buffered communication	Yes	Channel with buffer size $n > 0$
Internal Scheduling	???	
External Scheduling	???	

## Chapter 9

# Other Approaches to Concurrency

### 9.1 Atomic Data Structures

Using CAA/V (See SubSection 4.18.3) to build custom things. We can use this to implement linked lists and queues. This is lock free (no locks), and is wait free (provides a bound).

### 9.2 Coroutines

#### 9.2.1 C++ Boost

Provides coroutines with:

1. Stacks
2. Semi and Full coroutines
3. No recursion
4. Single interface

These coroutines are passed as parameters to the method (a coroutine object that you call “yield” on)

#### 9.2.2 Simula

Simula has coroutines with:

1. Stacks
2. Semi and Full coroutines
3. Recursion

## 9.3 Languages With Concurrency Constructs

### 9.3.1 Ada 95

This language is like  $\mu\text{C}++$ .

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Yes	
Internal Scheduling	No	Requeue can be used to make a blocking call to another mutex member.
External Scheduling	Yes	

### 9.3.2 Concurrent C++

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	??	
Buffered Communication	No	
Internal Scheduling	Yes	
External Scheduling	Yes	

### 9.3.3 Linda

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Yes	
Internal Scheduling	??	
External Scheduling	??	

### 9.3.4 Actors

Actors are administrators.

Scala

### 9.3.5 OpenMP

Uses `#pragma` to communicate concurrency to the compiler.

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Yes	
Internal Scheduling	n/a	
External Scheduling	n/a	

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Yes	Threads are tasks with a public atomic message-queue.
Internal Scheduling	Yes	
External Scheduling	No	

## 9.4 Threads and Locks Library

### 9.4.1 Java Concurrency

sraig believes he's used concurrency in Java, so he didn't really bother taking notes for this section.

### 9.4.2 PThreads

PThreads is to C as  $\mu$ C++ is to C++. See Table 9.4.2.

### 9.4.3 C++11

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Yes	This is a C++ extension - you can build it.
Internal Scheduling	???	
External Scheduling	???	

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Yes	
Internal Scheduling	Yes	
External Scheduling	No	

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Self-implemented	
Internal Scheduling	Yes	No-priority, nonblocking
External Scheduling	Self-implemented	

Feature	Provided?	Notes
Threads	Yes	
Direct Communication	Yes	
Buffered Communication	Yes	
Internal Scheduling	Yes	
External Scheduling	Yes	

# Chapter 10

## Optimization

We want things to be fast, because slowness is bad. There are three types of speedup:

**Reordering** data and code is reordered

**Eliding** remove unnecessary data, accesses and computation

**Replication** (concurrency) allows us to duplicate stuff because of physical limitations (light, etc)

### 10.1 Sequential Model

Program execution is in sequential program order.

In this model, we can reorder and elide operations that can be reordered or are unnecessary. We can even overlap code execution by executing concurrently (parallelism), but concurrency is limited.

### 10.2 Concurrent Model

In the concurrent model, there's everything we've talked about this term. Most concurrent applications are large sections of sequential code followed by small sections of concurrent code.

Concurrent sections can be corrupted by implicit serial optimizations, so we need to identify concurrent code and restrict its optimization.

#### 10.2.1 Disjoint Reordering

TODO: Not sure what all the  $W \rightarrow R$ ,  $W \rightarrow W$ ,  $R \rightarrow W$  stuff is. It's in section 10.2

There are two concurrency model terms we need to know:

**Atomically Consistent** there is an absolute ordering and a global clock. In this model, every operation



is ordered by real time and everyone must see the exact same ordering. If A happens before B in real time, then everyone must observe that A happens before B.

**Sequentially Consistent** A sequence  $S$  is said to be Sequentially Consistent (SC) if there exists at least one sequential (serial) re-ordering  $S'$  of tasks such that an assignment of a variable  $X$  by task  $T_1 \in S$  then read by task  $T_2 \in S$  is the same as the value of  $X$  written and read by  $T_1$  and  $T_2$  in  $S'$ .

In this model, there is a global ordering of writes, but no global clock of the time of the write. In a sequentially consistent model, everyone must see the same ordering of writes. However, this ordering does not have to match the real time ordering. Even though a write A happens before a write B in real time, as long as everyone observes the same ordering (say B before A), the system is still sequentially consistent. *But*, writes from a single task cannot be reordered.

### 10.2.2 Eliding

Eliding (See the beginning of Chapter 10) can cause errors due to copying flags to registers, and removing sleeps necessary to wait for signals<sup>1</sup>.

### 10.2.3 Overlapping

Implicit parallelism must synchronize before using values modified in multiple threads.

## 10.3 Corruption

We make implicit assumptions about sequential execution that fails in concurrent code.

### 10.3.1 Memory

To have sequential consistency, hardware must serialize memory access and modification.

Reads and writes can arrive in any order, and the reordering we implement support must be no different than the range a time-slice reordering provides. To read  $A$ , we need to wait for writing  $A$  to complete before this happens.

One way to do this is to buffer writes. CPUs wait on reads until data arrives.

We can increase performance by disjointly reordering from  $W \rightarrow R$  to  $R \rightarrow W$ . In fact, reads can bypass a buffer if the address is not waiting to write, which is bad.

As the number of processors increase, we'd want to use more buffers, but this would allow parallel reads.

To increase concurrency, we can eliminate serializing writes, which would allow  $R \rightarrow W$  reordering from  $W \rightarrow R$  and  $W \rightarrow R$  reordering from  $R \rightarrow W$ . These are bad, so we can't do it.

---

<sup>1</sup>While it isn't the best thing to do, it is still valid code.

### 10.3.2 Cache

### 10.3.3 Review

CPUs are fast, memory is slow. The disk is the slowest.

We have billions of bytes, but only few registers, so we move frequently accessed data to registers. When we have more data than registers, we load memory dynamically. To prevent multiple threads from thrashing memory when they context switch, we use a hardware cache to stage data without pushing to memory.

By using multi-level caches, we get more and more size, but less speed.

### 10.3.4 Cache Coherence

Caches exist for each processor, which means that values are duplicated. As we move things up the hierarchy, it becomes invalid as other ones write them and needs to be invalidated.

**Cache Coherence** is a hardware protocol ensuring duplicate values are updated

**Cache Consistency** is the name of the entire topic, and the time that caches eventually become consistent.

### 10.3.5 Registers

Each processor has a bunch of registers that aren't concurrent.

Loading a variable into a register hides its value from the main memory. It's impossible to peek at registers of a processor.

The **volatile** keyword means that variables updating the main memory happens faster.

### 10.3.6 Out of Order Execution

Cache and memory buffers will perform simple instruction reordering for reads and writes over small sections of code.

This can mess up things like entry protocols, etc.

## 10.4 Selectively Disabling Optimizations

We can do a few things to prevent optimizations, but most of these are compiler-specific, and diverse.

Refer to the course notes for the list of compiler-specific modifications.