# CHIRAG GIRISH SHAH

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#### **Education**

Dec 2016 University of Florida. Master in Business Administration (MBA)

Gainesville, Florida

**Relevant Coursework**: Marketing Management, Organizational Behavior, CRM, Managerial Statistics, Marketing Management, Corporate Finance, Operations and Supply Chain Management, Accounting, Economics of Business Decisions. Information Systems in Organizations

May 2011 Georgia Institute of Technology (MS – ECE)

Atlanta, Georgia

**Relevant Coursework**: Advanced VLSI, Advanced Computer Architecture, Advanced Digital System Testing, Digital System Testing, Basics of VLSI, MEMS, Multimedia Communication

### Experience

May 2011 - Intersil Corporation

Melbourne, Florida

Present

# Project Manager and Sr. ATE Test Engineer – Precision and Automotive (ITAR Approved)

- Legacy Platform Conversion: Managed a \$200,000 project to convert programs on legacy platforms to highly reliable modern platforms. Performed a requirements and risk-reward analysis, developed the project plan and established a road map with clear dates and clear deliverables. Used a Scrum framework to manage the project. Recruited an offshore test house to manage the conversion, held weekly meetings and required daily updates, and monitored the progress per the Key Performance Indicators at each step. In addition, coordinated with Marketing and Supply Chain to save an additional \$22,000 in conversions.
- Proficient in doing trend assessment and large scale Data Analysis with JMP, SPSS (IBM) and SAS E-miner(Oracle) and R (OpenSource)
- Lead Automation in data collection using python
- Assist Marketing manager and Product Definers by doing an industry analysis and prepare a sales pitch for next-gen products to become relevant in the IoT realm.
- Performed a Customer Lifetime Value analysis for the Channel Marketing team to maximize the profitability of each new product marketing campaign
- Lead the New Product Development (NPD) cycle in a fast paced cross-divisional, cross-cultural team setting interfacing with Design, Marketing, Reliability, Quality and Applications Engineering team to define a viable high performance test solution with guaranteed first pass success
- Actively participate in cross-functional team meetings for DFT (Design for Test) and DFM (Design for Manufacturing) to minimize Test and Quality issues.
- Perform CPK, GRR (Gauge Repeatability and Reproducibility) and Transient Analysis before offshore transfer. Familiar with Automotive AEC Q100 spec.
- Teradyne certified IG-XL programmer, Advantest certified Verigy 93K programmer.
- **Product Portfolio**: SAR Analog to Digital Converters, Buck Boost Controllers, LED backlight Drivers for Automotive applications, Half Bridge Drivers, Digital Power Monitors, Digitally Controlled Potentiometers, PLC Line Drivers, RS-232 and RS-485 Transceivers, Space and Rad-Hard products
- Mentor and Recruiter: Recruited an intern from Georgia Tech for a Test Engineering position. Defined the project and guided the intern through the Test Engineering process.

Jan-Apr 2011 Intersil-Quellan

Atlanta, Georgia

# **Test Engineering Intern (part time)**

• Tested High Speed Active Cables and ASIC to maintain the signal integrity of HSD signals

May-Dec 2010 Intersil

Melbourne, Florida

### Test Engineering Internship and Co-op

- Establish SAR bench test capability with primary focus on linearity and SINAD.
- Design DIB using CADENCE for Teradyne Flex and programmed in IG-XL to evaluate ATE performance for 16 bit SAR ADC's

May 2008-2009 Siemens Ltd.

Mumbai, India

#### Internship

• PLC based operation of conveyor belt for bi-directional applications using SIEMENS S7-300 PLC and WinCC for HMI

#### In Addition

Data Analytics: JMP, R, R-Miner, SPSS, SAS E-Miner

**Software:** C, C++, VBT, Python, SQL, MATLAB, LabView, PyVisa

Protocols: GPIB, I2C, RS-232, RS-485

General: MS excel, word, power point, visio, and project. Excellent presentation and communication skills

**Project Management**: Expert in Agile development