



[illegible]

[illegible]

[illegible]

[illegible]

[illegible]

[illegible]

[illegible]



[illegible]

[illegible]

[illegible]

[illegible]

		IW input bits								Control output bits																	
INAME	27	26	25	24	23	22	21	20	RFW	EXTop1	Extop0	ALUsrcB	ALUop2	ALUop1	ALUop0	PCsel1	PCsel0	RR0sel1	RR0sel0	RR1sel	WRsel	WDsel	MR	MW	input bits	output bits	

[illegible]

[illegible]

[illegible]



[illegible]

[illegible]

[illegible]

[illegible]