				Input				Control Outpu	uts										Input bits				Output bits
INUM	Instruction	Mnemonic	Format	opcode	funct3	funct7	funct6	RFW	ALUOp	ALUSrcA	ALUSrcB	PCsel	WDsel	BROp	Size	MR	store	load	opcode	funct3	funct7	funct6	
	addi	ADDI	1	0010011	000	XXXXXX	XXXXXXXX	1	0000	0	001	0	0	000	00	0	0	0	0b0010011	0ь000	0bxxxxxx	0bxxxxxx	0b1000000010000000000
	addi	ADDR	R	0110011	000	0000000	XXXXXXXX	1	0000	0	000	0	0	000	00	0	0	0	0b0110011	0ь000	0ь0000000	0bxxxxxx	0ь10000000000000000000
	jal	JAL	J	1101111	xxx	XXXXXX	XXXXXXXX	1	0000	1	101	1	1	000	00	0	0	0	0b1101111	0bxxx	0bxxxxxx	0bxxxxxx	0b1000011011100000000
	jalr	JALR	1	1100111	000	XXXXXX	XXXXXXXX	0	0000	0	001	1	1	000	00	0	0	0	0b1100111	0ь000	0bxxxxxx	0bxxxxxx	0ь0000000011100000000
	sll	SLLR	R	0110011	001	0000000	XXXXXXXX	1	0100	0	000	0	0	000	00	0	0	0	0b0110011	0b001	0ь0000000	0bxxxxxx	0ь10100000000000000000
	sll	SLLI	1	0010011	001	xxxxxxx	000000	1	0100	0	110	0	0	000	00	0	0	0	0ь0010011	0b001	0ь0000000	0bxxxxxx	0b1010001100000000000
	srl	SRLR	R	0110011	101	0000000	XXXXXXXX	1	0101	0	000	0	0	000	00	0	0	0	0b0110011	0b101	0ь0000000	0bxxxxxx	0b101010000000000000000
	srl	SRLI	1	0010011	101	XXXXXX	000000	1	0101	0	110	0	0	000	00	0	0	0	0b0010011	0b101	0ь0000000	0bxxxxxx	0b10101011000000000000
	beq	BEQ	В	1100011	000	XXXXXXXX	xxxxxxx	0	0000	1	011	0	0	001	00	0	0	0	0b1100011	0ь000	0bxxxxxxxx	0bxxxxxx	06000001011000010000
	bne	BNE	В	1100011	001	XXXXXXXXX	XXXXXXXX	0	0000	1	011	0	0	010	00	0	0	0	0b1100011	0b001	0bxxxxxxxx	0bxxxxxx	0ь000001011000100000
	blt	BLT	В	1100011	100	XXXXXXXXX	XXXXXXXX	0	0000	1	011	0	0	100	00	0	0	0	0b1100011	0b100	0bxxxxxxxx	0bxxxxxxx	0ь000001011001000000
	bge	BGE	В	1100011	101	XXXXXXXX	xxxxxxx	0	0000	1	011	0	0	011	00	0	0	0	0b1100011	0b101	0bxxxxxxxx	0bxxxxxx	06000001011000110000
	mul	MUL	R	0110011	000	0000001	xxxxxxx	1	0010	0	000	0	0	000	00	0	0	0	0b0110011	0ь000	0ь0000001	0bxxxxxx	0b1001000000000000000
	sub	SUB	R	0110011	000	0100000	XXXXXXXX	1	0001	0	000	0	0	000	00	0	0	0	0b0110011	0ь000	0b1000000	0bxxxxxxx	0ь1000100000000000000
	lui	LUI	U	0110111	xxx	XXXXXXXX	xxxxxxx	1	1011	0	100	0	0	000	00	0	0	0	0b0110111	0bxxxx	0bxxxxxxx	0bxxxxxx	0b1101101000000000000
	lb	LB	1	0000011	000	XXXXXXXXX	XXXXXXXX	1	0000	0	001	0	0	000	01	1	0	1	0b0000011	0ь000	0bxxxxxxx	0bxxxxxx	0b100000001000000110
	lw	LW	1	0000011	010	XXXXXXXXX	XXXXXXXX	1	0000	0	001	0	0	000	10	1	0	1	0b0000011	0b001	0bxxxxxxx	0bxxxxxxx	0b100000001000001010
	sb	SB	S	0100011	000	XXXXXXXX	xxxxxxx	0	0000	0	010	0	1	000	01	0	1	1	0ь0100011	0ь000	0bxxxxxxx	0bxxxxxx	0b000000010010000101
	SW	SW	S	0100011	010	XXXXXXXXX	XXXXXXXX	0	0000	0	010	0	1	000	10	0	1	1	0b0100011	0b010	0bxxxxxxx	0bxxxxxx	0b000000010010001001
	and	AND	R	0110011	111	0000000	XXXXXXXX	1	1000	0	000	0	0	000	00	0	0	0	0ь0110011	0b111	0ь0000000	0bxxxxxx	0ь11000000000000000000
	or	OR	R	0110011	110	0000000	xxxxxxx	1	1001	0	000	0	0	000	00	0	0	0	0b0110011	0b110	0ь0000000	0bxxxxxx	0b11001000000000000000
	ld	LD	1	0000011	011	XXXXXXXXXX	XXXXXXX	1	0000	0	001	0	0	000	11	1	0	1	0ь0000011	0ь011	0bxxxxxxx	0bxxxxxx	0ь100000001000001110
	sd	SD	S	0100011	011	XXXXXXXXXX	XXXXXXXX	0	0000	0	010	0	1	000	11	0	1	1	0ь0100011	0ь011	0bxxxxxxxx	0bxxxxxx	06000000010010001101
	addiw	ADDIW	1	0011011	000	xxxxxxxx	XXXXXXXX	1	1010	0	001	0	0	000	00	0	0	0	0b0011011	0ь000	Obxxxxxxx	0bxxxxxxx	0b1101000010000000000