

CS3281 / CS5281

Exceptional Control Flow

CS3281 / CS5281 Spring 2025

*Some lecture slides borrowed and adapted from CMU's "Computer Systems: A Programmer's Perspective"



This Lecture

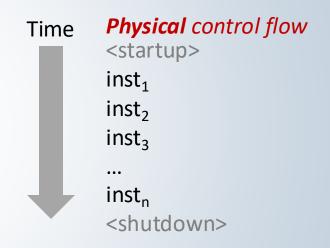
- Exceptional Control Flow
- Exceptions
- Processes
- Process Control





Control Flow

- Processors do only one thing:
 - From startup to shutdown, a CPU reads and executes (interprets) a sequence of instructions, one at a time
 - This sequence is the CPU's control flow (or flow of control). The control flow is a sequence of control transfers from instruction to another







Altering the Control Flow

- You know two mechanisms for changing control flow:
 - Jumps and branches
 - Call and return
- Insufficient for a useful system: difficult to <u>react</u> to events and changing system state
 - Examples of changes in system state:
 - Data arrives from a disk or a network adapter
 - Instruction divides by zero
 - User hits Ctrl-C at the keyboard
 - System timer expires
- System needs mechanisms for "exceptional control flow"





Exceptional Control Flow

- Exists at all levels of a computer system
- Hardware: hardware detects events and transfers control to handlers

- Software
 - OS: When a timer goes off, OS transfers control from one process to another to use CPU time (virtualization)
 - User process: sends a signal to another process which handles the signal with a handler





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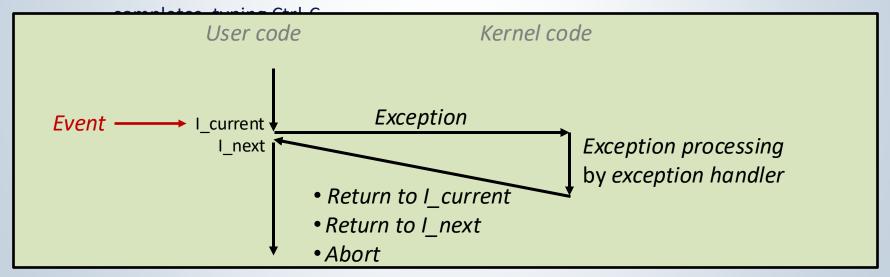
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Exceptions

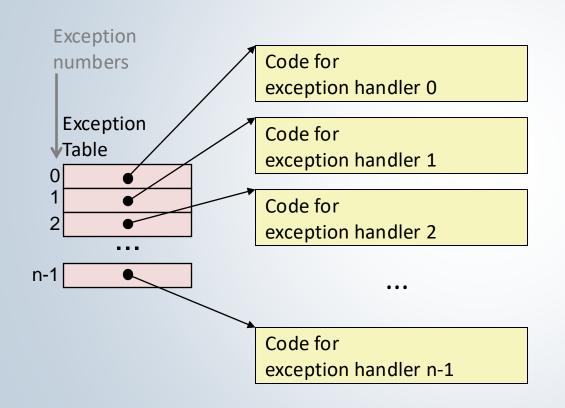
- An exception is an abrupt transfer of control from the user mode to the kernel mode in response to some event (i.e., change in processor state: program counter, registers, memory)
 - Examples of events: Divide by 0, arithmetic overflow, page fault, I/O request







Exception Tables



- Each type of event has a unique exception number k
- k = index into exception table
 (a.k.a. interrupt vector)
- Handler k is called each time exception k occurs

Asynchronous Exceptions

- Asynchronous exceptions are called interrupts
- Caused by events external to the processor
 - Indicated by setting the processor's interrupt pin
 - Handler returns to "next" instruction
- Examples:
 - Timer interrupt
 - Every few ms, a timer chip trigger an interrupt
 - Used by kernel to take back control from user programs
 - I/O interrupt from external device
 - Arrival of network packet
 - Arrival of data from disk





Synchronous Exceptions

- Caused by events that occur as a result of executing an instruction:
 - Traps
 - Intentional (explicit)
 - Examples: system calls
 - Returns control to "next" instruction in the user program

Faults

- Unintentional but possibly recoverable
- Examples: page faults (recoverable), protection faults (unrecoverable), floating-point exceptions (unrecoverable)
- Either re-execute faulting instruction or abort

Aborts

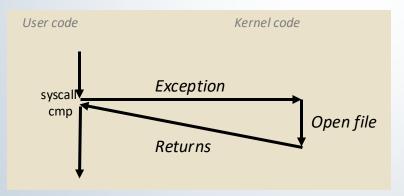
- Unintentional and unrecoverable
- Examples: illegal instruction, faulty hardware
- Aborts current program





System Call Example: Opening File

- User calls: open (filename, options)
- Calls __open function, which invokes system call instruction syscall



- %eax contains syscall number
- Other arguments in %rdi, %rsi, %rdx, %r10, %r8, %r9
- Return value in %rax
- Negative value is an error corresponding to negative errno

Fault Example: Page Fault

- User writes to memory location
- That portion (page) of user's memory is currently on disk

80483b7: c7 05 10 9d 04 08 0d movl \$0xd,0x8049d10

```
int a[1000];
main ()
{
    a[500] = 13;
}
```

```
Exception: page fault

Copy page from disk to memory reexecute movl
```

Fault Example: Invalid Memory Reference

- Buffer overflow
- Sends SIGSEGV signal to user process
- User process exits with "segmentation fault"

```
80483b7: c7 05 60 e3 04 08 0d movl $0xd,0x804e360
```

```
int a[1000];
main ()
{
    a[5000] = 13;
}
```

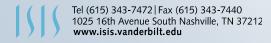
```
Exception: protection fault

Detect invalid address

Signal process
```

This Lecture

- Exceptional Control Flow
- Exceptions
- Processes
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Processes

- Definition: A process is an instance of a running program
 - One of the most profound ideas in computer science
 - Not the same as "program" or "processor"
- Process provides each program with two key abstractions:
 - Logical control flow
 - Each program seems to have exclusive use of the CPU
 - Provided by kernel mechanism called context switching
 - Private address space
 - Each program seems to have exclusive use of main memory
 - Provided by kernel mechanism called <u>virtual memory</u>



Stack

<u>Heap</u>

Data

Code

CPU

Registers





Multiprocessing Example

- Running program "top" on Mac
 - System has 123 processes, 5 of which are active
 - Identified by Process ID (PID)

0	0	0	X xterm

Processes: 123 total, 5 running, 9 stuck, 109 sleeping, 611 threads

Load Avg: 1.03, 1.13, 1.14 CPU usage: 3.27% user, 5.15% sys, 91.56% idle

SharedLibs: 576K resident, OB data, OB linkedit.

MemRegions: 27958 total, 1127M resident, 35M private, 494M shared.

PhysMem: 1039M wired, 1974M active, 1062M inactive, 4076M used, 18M free.

VM: 280G vsize, 1091M framework vsize, 23075213(1) pageins, 5843367(0) pageouts.

Networks: packets: 41046228/11G in, 66083096/77G out.

Disks: 17874391/349G read, 12847373/594G written.

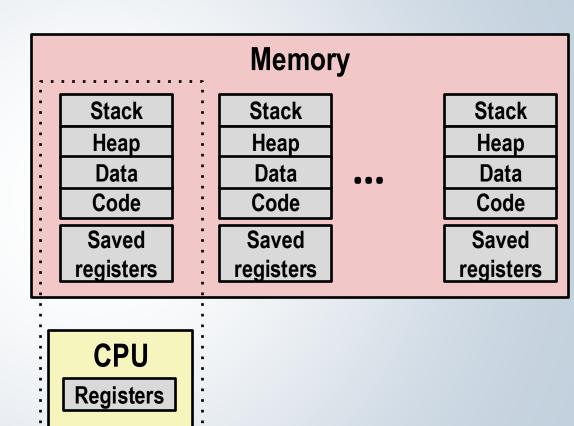
PID 99217- 99051 99006 84285 55939- 54739 54739 54737 54719 54701 54661 54659 53818	COMMAND Microsoft Of usbmuxd iTunesHelper bash xterm Microsoft Ex sleep launchdadd top automountd ocspd Grab cookied mdworker	%CPU 0.0 0.0 0.0 0.0 0.0 0.3 0.0 0.0 6.5 0.0 0.0	TIME 02:28.34 00:04.10 00:01.23 00:00.11 00:00.83 21:58.97 00:00.00 00:00.00 00:02.53 00:00.02 00:00.05 00:02.75 00:00.15 00:01.67	3 2 1 1 10 1 2 1/1 7 4 6	#WQ 1 1 1 0 0 3 0 1 0 1 1 1	#PORT 202 47 55 20 32 360 17 33 30 53 61 222+ 40 52	#MREG 418 66 78 24 73 954 20 50 29 64 54 389+ 61 91	RPRVT 21M 436K 728K 224K 656K 16M 92K 488K 1416K 860K 1268K 1268K 13316K 7628K	RSHRD 24M 216K 3124K 732K 65M 212K 220K 216K 216K 2644K 26M+ 224K 7412K	RSIZE 21M 480K 1124K 484K 692K 46M 360K 1736K 2124K 2124K 2184K 3132K 40M+ 4088K 16M	VPRVT 66M 60M 43M 17M 9728K 114M 9632K 48M 17M 53M 53M 50M 75M+ 42M 48M	VSIZE 763M 2422M 2429M 2378M 2382M 1057M 2370M 2409M 2413M 2413M 2426M 2556M+ 2411M 2438M
				_	1						. —	
50878	mdworker	0.0	00:01.07		1	53	91	2464K	6148K	9976K	44M	2434M
50410 50078	xterm emacs	0.0	00:00.13		0	32 20	73 35	280K 52K	872K 216K	532K 88K	9700K 18M	2382M 2392M
E4410		^ ^			^	^^	0.1	14000	7700	A.T.	470	077011

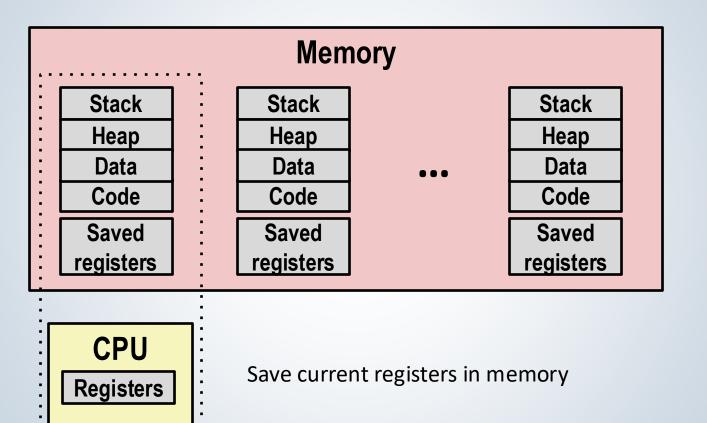


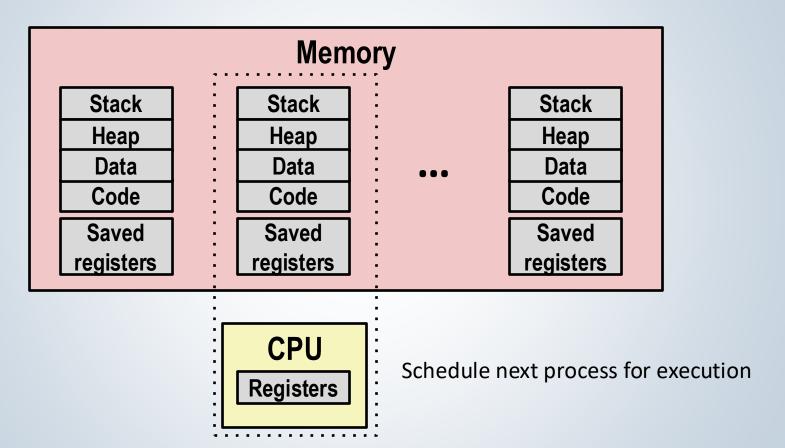


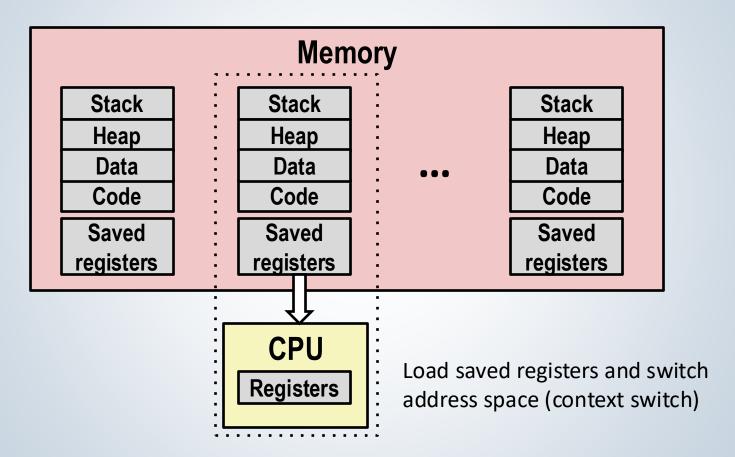
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- Single processor executes multiple processes concurrently
 - Process executions interleaved (multitasking)
 - Address spaces managed by virtual memory system (later in course)
 - Register values for nonexecuting processes saved in memory





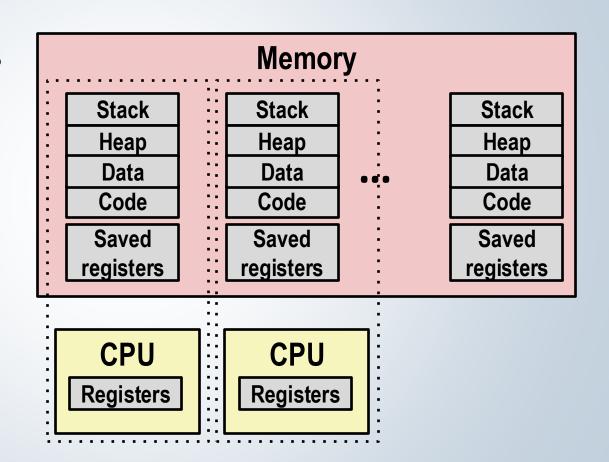




Multiprocessing: The (Modern) Reality

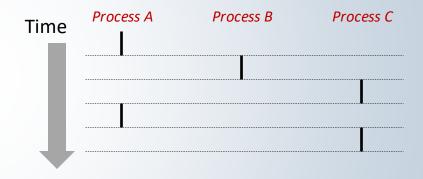
Multicore processors

- Multiple CPUs on single chip
- Each can execute a separate process.
- Run multiple processes simultaneously
- Scheduling of processors onto cores done by kernel



Concurrent Processes

- Each process is a logical control flow
- Two processes run concurrently (are concurrent) if their flows overlap in time
- Otherwise, they are sequential
- Examples (running on single core):
 - Concurrent: A & B, A & C
 - Sequential: B & C



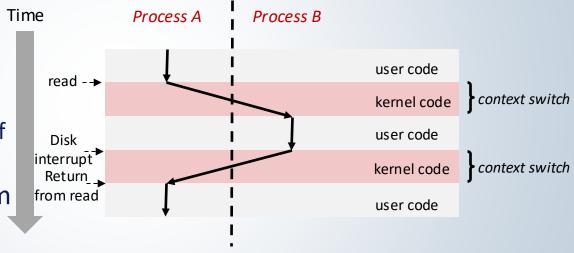


Context Switching

 Processes are managed by a shared chunk of memory-resident OS code called the kernel

> Important: the kernel is not a separate process, but rather runs as part of some existing process

 Control flow passes from one process to another via a context switch







Summary

Exceptions

- Events that require nonstandard control flow
- Generated externally (interrupts) or internally (traps and faults)

Processes

- At any given time, system has multiple active processes
- Only one can execute at a time on a single core, though
- Each process appears to have total control of processor + private memory space

