

## Connectivity and MST

last time: memory per machine  $M = n^{1+\varepsilon}$

today:

$$M = n^\alpha \quad \alpha \in (0,1)$$

$$M = \tilde{O}(n)$$

$$|V| = n \quad |E| = m$$

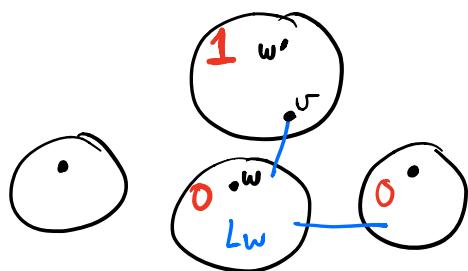
Each node  $v$  maintain label  $l(v)$

$L_v \subseteq V$ : set of vertices labeled  $v$   
 — connected component containing  $v$

$\Gamma(v)$ : neighbors of  $v$   
 $S \subseteq V, \Gamma(S) = \bigcup_{v \in S} \Gamma(v)$

$$\Gamma'(v) \triangleq \Gamma(L_v)$$

1. Every node  $u \in V$  active with label  $l(u) = u$
2. For  $i = 1, 2, 3, \dots O(\log n)$  do
  - (a) Call each active node a leader  $w$ - prob.  $1/2$
  - (b) For every active non-leader  $w$   
 find  $w^* = \min_{\substack{l(v) \\ l(v) \text{ leader}}} \{l(v) \mid v \in \Gamma'(w)\}$
  - (c) If  $w^*$  not empty, mark  $w$  passive  
 relabel each node with label  $w$  by  $w^*$



Note: Label is not necessarily min. node in component!

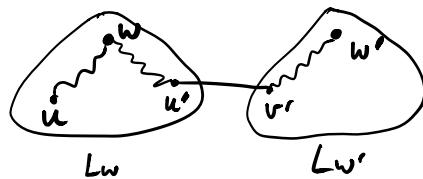
Lemma: At any point in algorithm,  $\forall u \in V$   
 there is a path from  $u$  to  $l(u)$

Pf: By induction on # rounds.

True initially :

Suppose this is true at beginning of round  
 If  $l(u)$  does not change,  $\exists$  path from  $u$  to  $l(u)$

Suppose  $l(u) = w$  initially  
 relabeled to  $l(u) = w'$



Corollary: If at any point, 2 nodes  $s$  &  $t$  have same label  
 then  $\exists$  path from  $s$  to  $t$  in  $G$

Lemma: Every connected component of  $G$  has unique label  
 after  $O(\log n)$  rounds w.h.p.

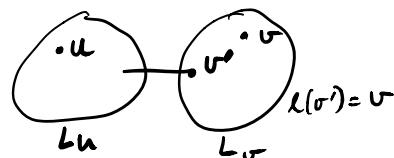
Pf Will show that within each connected component,  
 # labels decreases by constant factor in expectation  
 in every round, till every vertex has same label  
 $\# \text{labels} = \# \text{connected comp} = \# \text{active nodes}$

Fix active node  $u$

If component containing  $u$  has more than one  
 active node, then

$\exists v' \in \Pi'(u)$  with label different from  $u$   
 $l(v') = v$

With prob.  $\frac{1}{4}$ , active node  $v$  is  
 selected as leader,  $u$  is a non-leader



$\Rightarrow u$  will be relabeled and become passive.

Prob. [ Active node survives a round  $\left| \begin{array}{l} \text{more than one label} \\ \text{in conn. component} \end{array} \right. ] \leq \frac{3}{4}$

How to implement in MPC?

memory  $M = n^\alpha$   $\alpha < 1$

$n^{1-\alpha}$  machines for vertex status : 

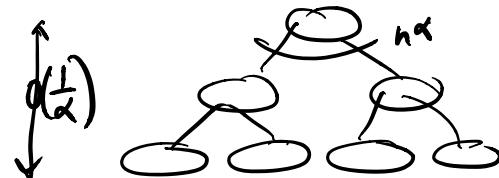
label	active / passive
leader	non-leader

For each active non-leader node  $w$   
find  $w^* = \min \{ l(v) \mid v \in \Gamma'(w), l(v) \text{ leader} \}$

for each edge  $\{u, v\}$ , if  $l(u) \neq l(v)$ ,  
 $l(u)$  non-leader  
 $l(v)$  leader

$l(v)$  is potential label for  $l(u)$

For each active non-leader node  $w$   
first compute # candidate labels (w. duplicates)



then compute minimum over set of candidate labels.

$O(\frac{1}{\alpha})$  rounds

Broadcast new labels to all nodes in  $O(\frac{1}{\alpha})$  rounds

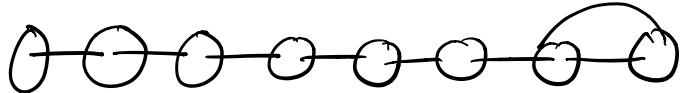
Overall:  $O(\frac{\log n}{\alpha})$  rounds in MPC model w. memory  $n^\alpha$

MST : Boruvka's algorithm ['26]

Maintain connected components

Repeat  $O(\log n)$  times :

Choose cheapest edge out of each current component  
& merge components



Problem: merging could take large # rounds!

Fix: use idea of random leaders.

Mark each component leader w. prob  $\frac{1}{2}$

Each non-leader chooses cheapest outgoing edges  
only if it goes to leader component

Cannot have long chain of merges.

### Open Problem:

Conjecture: connectivity requires  $\Omega(\log n)$  rounds with  
memory  $n^\alpha$   $\alpha < 1$

Hard Case? Distinguish between cycle on  $n$  nodes  
vs. 2 cycles on  $\frac{n}{2}$  nodes

### $O(1)$ rounds with $\tilde{\Theta}(n)$ memory

Claim: Sorting in  $O(\frac{1}{2})$  rounds with  $n^\alpha$  memory

Idea: Choose  $n^\alpha$  pivots at random  
Sort pivots on one machine  
divide into subproblems of size  $\sim n^{1-\alpha}$  each  
recuse on subproblems in parallel  
 $O(\frac{1}{2})$  rounds

Sort edges in increasing order  
 $w(e_1) \leq w(e_2) \dots \leq w(e_m)$

Kruskal: examines edges in this order

Observation: Edge  $e_i$  is in MST iff its endpoints not in the same connected component in graph such  $\{e_1 \dots e_{i-1}\}$

Group edges into chunks of  $n$  edges  
Each chunk fits on one machine  
Process chunks simultaneously

For  $i \in \{1, \dots, \frac{m}{n}\}$

$E_i = \{e_{(i-1)n+1}, \dots, e_{in}\}$   $i^{\text{th}}$  chunk

$E_i' = \bigcup_{j=i}^l E_j$  union of  $E_i$  to  $E_l$

$F_i'$ : forest of connected components from  $E_i'$

$F_0'$ : all vertices isolated

Any edge  $\{u, v\} \in E_i$  is in MST iff

Components of  $u$  &  $v$  are different in

$F_{i-1} \cup \{ \text{edges preceding } \{u, v\} \text{ in } E_i \}$

Note: Single machine can hold  $E_i$  &  $F_{i-1}$  and make decisions for chunk  $E_i$

Need: Algo to compute connected components in  $O(1)$  rounds.

We already have such an algo!

Via Lo-sampling sketches

Can be implemented in  $O(1)$  rounds of MPC

With  $n \cdot \text{polylog}(n)$  memory — enough to store sketches of all vertices and run connectivity algo on single machine