# CS 4110 Programming Languages & Logics

Lecture 32 Shared-Memory Parallelism

21 November 2016

#### **IMP** with Parallel Composition

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```
a ::= x \mid n \mid a_1 + a_2
b ::= \text{true} \mid \text{false} \mid a_1 < a_2
c ::= \text{skip} \mid x := a \mid c_1; c_2 \mid \text{if } b \text{ then } c_1 \text{ else } c_2 \mid \text{ while } b \text{ do } c \mid c_1 \mid c_2
```

And add small-step operational semantics rules for  $c_1 \parallel c_2$  that interleave the execution of  $c_1$  and  $c_2$ :

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$$\langle \sigma, \mathbf{skip} \mid\mid \mathbf{skip} \rangle \to \langle \sigma, \mathbf{skip} \rangle$$

The rules allow either sub-command to take a step; two sub-commands can interleave read and write operations involving the same store.

#### Parallel Bank Account

What happens if we deposit into a bank account twice under parallel composition?

```
bal := 0; (bal := bal + 21.0 || bal := bal + 21.0)
```

#### Synchronization

Languages have synchronization constructs that control the interactions between threads.

Many languages have mutual exclusion, a.k.a. locking:

```
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\begin{aligned} & \textbf{lock } l; \\ & \textbf{bal := bal } + 21.0 \\ & \textbf{unlock } l \end{aligned}
```

A well-behaved alternative is transactional memory:

```
\begin{aligned} & \textbf{transaction} \ \big\{ \\ & \text{bal} := \text{bal} + 21.0 \\ \big\} \end{aligned}
```

## Reasoning About Shared Memory

This program reads and writes two shared variables from two different "threads":

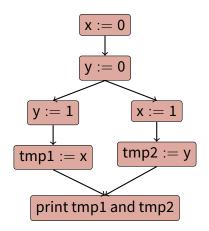
```
x := 0; y := 0;

(y := 1; tmp1 := x) ||

(x := 1; tmp2 := y)
```

What can tmp1 and tmp2 be afterward?

## **Ordering Operations**



The *happens before* relation is a partial order on events in a program execution.

See also Lamport, 1978: "Time, Clocks and the Ordering of Events in a Distributed System."

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Operation *a* happens before *b*, written  $a \rightarrow b$ , iff:

- a and b belong to the same thread and a comes before b in a single-threaded execution, or
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(Also add transitivity: if  $a \rightarrow b$  and  $b \rightarrow c$ , then  $a \rightarrow c$ .)

See also Lamport, 1978: "Time, Clocks and the Ordering of Events in a Distributed System."

In modern multithreaded programming, messages are sent and received at *synchronization* events:

- unlock  $l \rightarrow lock l$
- fork  $t \rightarrow$  first operation in thread t
- last operation in thread  $t \rightarrow$  join t

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Intuitively,  $\rightarrow_e$  is an *interleaving* that obeys  $\rightarrow$ .

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- $y := 1 \rightarrow tmp1 := x \rightarrow x := 1 \rightarrow tmp2 := y$  $\implies tmp1 \mapsto 0, tmp2 \mapsto 1$
- $\begin{array}{lll} \bullet \ \ y := 1 & \rightarrow & x := 1 & \rightarrow & tmp1 := x & \rightarrow & tmp2 := y \\ \Longrightarrow tmp1 \mapsto 1, tmp2 \mapsto 1 \end{array}$

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- $x := 1 \rightarrow y := 1 \rightarrow tmp2 := y \rightarrow tmp1 := x$  $\implies tmp1 \mapsto 1, tmp2 \mapsto 1$
- $\begin{array}{lll} \bullet & \mathsf{x} := \mathsf{1} & \to & \mathsf{y} := \mathsf{1} & \to & \mathsf{tmp1} := \mathsf{x} & \to & \mathsf{tmp2} := \mathsf{y} \\ \Longrightarrow \mathsf{tmp1} \mapsto \mathsf{1}, \mathsf{tmp2} \mapsto \mathsf{1} \end{array}$
- $x := 1 \rightarrow tmp2 := y \rightarrow y := 1 \rightarrow tmp1 := x$  $\implies tmp1 \mapsto 1, tmp2 \mapsto 0$

Enumerating SC executions gets old fast, but lets us produce the set of possible final stores,  $\sigma$ :

```
 \begin{aligned} & \{\mathsf{tmp1} \mapsto \mathsf{0}, \mathsf{tmp2} \mapsto \mathsf{1}\} \\ & \{\mathsf{tmp1} \mapsto \mathsf{1}, \mathsf{tmp2} \mapsto \mathsf{1}\} \\ & \{\mathsf{tmp1} \mapsto \mathsf{1}, \mathsf{tmp2} \mapsto \mathsf{0}\} \end{aligned}
```

So no sequentially consitent execution makes both tmp1 and tmp2 equal to zero.

## That Same Program, in C

```
volatile int x, y, tmp1, tmp2;
// Thread 0: write x and read y.
void *t0(void *arg) {
  x = 1:
  tmp1 = y;
  return 0;
// Thread 1, the opposite: write y and read x.
void *t1(void *arg) {
  y = 1;
  tmp2 = x;
  return 0;
```

## That Same Program, in C

```
void main() {
  x = y = tmp1 = tmp2 = 0;
  // Launch both threads.
  pthread t thread0, thread1;
  pthread create(&thread0, NULL, t0, NULL);
  pthread create(&thread1, NULL, t1, NULL);
  // Wait for both threads to finish.
  pthread join(thread0, NULL);
  pthread join(thread1, NULL);
  printf("%d_{\square}%d\n", tmp1, tmp2);
```

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There are many reasons and/or excuses:

- Per-processor caching lets each CPU read values that other processors can't see yet.
- Private write buffers are critical for good performance with coherent caches.
- Lots of "obvious" compiler optimizations violate sequential consistency.

See also Boehm, 2005: "Threads cannot be implemented as a library."

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Sequential consistency is the *strongest* memory model out there: it allows the fewest different executions.

Real machines and languages have weaker memory models:

$$SC \ge x86 \ge ARM \ge C/C++ \ge DRF0$$

#### **Data Races**

#### A data race occurs when:

- There are two events a and b that are unordered in the happens-before relation ( $a \rightarrow b$  and  $b \rightarrow a$ ),
- both events access the same shared variable, and
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Our little example has two data races: one on x and one on y.

#### Data Races & Memory Models

Languages have recently agreed on one critical property:

data race free ⇒ sequentially consistent

As long as you avoid data races, you get sequential consistency on *any* machine in Java, C, and C++.

(In jargon: the DRF implies SC theorem.)

#### Data Races & Memory Models

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Languages still disagree about what happens when you *do* have a race. In C and C++, races allow undefined behavior.

#### Race-Free Programming

\$ ./a.out

Data race detection is an active field of research.

\$ cc -g -fsanitize=thread simple race.c

#0 Thread2(void\*) simple race.cc:13

One called ThreadSanitizer is included with recent Clang and GCC compilers:

```
WARNING: ThreadSanitizer: data race (pid=26327)
Write of size 4 at 0x7f89554701d0 by thread T1:
    #0 Thread1(void*) simple_race.cc:8
Previous write of size 4 at 0x7f89554701d0 by thread T2:
```