STATE MACHINE			
name	Frame_Proc_FSM		
clock	CLK	posedge	
reset_signal	RST	posedge	
reset_state	Idle		Idle
default_state_is_x	1		Idio
stateout	FRM_STATE		
INPUTS			
CLK			
RST			NAME OF THE PROPERTY OF THE PR
VALID			VALID
ROM_ADDR[2:0]			/ INC_ROM = 1
OUTPUTS			
INC_ROM	0	comb	Preamble
RST_ROM	0	regdp	INC_ROM = 1
CRC_CALC	0	comb	CLR_CRC <= 1
CRC_VLD	0	comb	
CLR_CRC	0	regdp	
TX_ACK	0	regdp	DOM APPR CITY
STATES	·	. og up	$ROM\_ADDR == 3'd4$
INC ROM	0	output	
RST_ROM	0	output	
CRC_CALC	0	output	TX_Ack
CRC_VLD	0	output	CRC_CALC = 1
CLR_CRC	0	output	TX_ACK <= 1
TX ACK	0	output	
TRANSITIONS			
equation	1	def_type	
priority	1000	_ ,,	
CRC_VLD		output	
INC_ROM		output	
_		·	Data
			CRC_CALC = VALID
			\ !VALID
			CRC_VLD = 1
			CKC_VED = 1
			CRC_EOP
			\
			$\  \  \  \  \  \  \  \  \  \  \  \  \  $
			<b>\</b>
			Rst_ROM
			RST_ROM <= 1
			( )