

STATE MACHINE		
name	Pow_on_Rst	
clock	STUP_CLK	posedge
reset_signal	!EOS	negedge
reset_state	Qpll_rst	allzeros
implied_loopback	1	
INPUTS		
QPLL_LOCK		input
DAQ_MMCM_LOCK		input
por_done		input
EOS		input
STUP_CLK		input
OUTPUTS		
por_cnt	0	comb
por	1	comb
STATES		
por_cnt	0	output
por	1	output
TRANSITIONS		
equation	1	def_type

