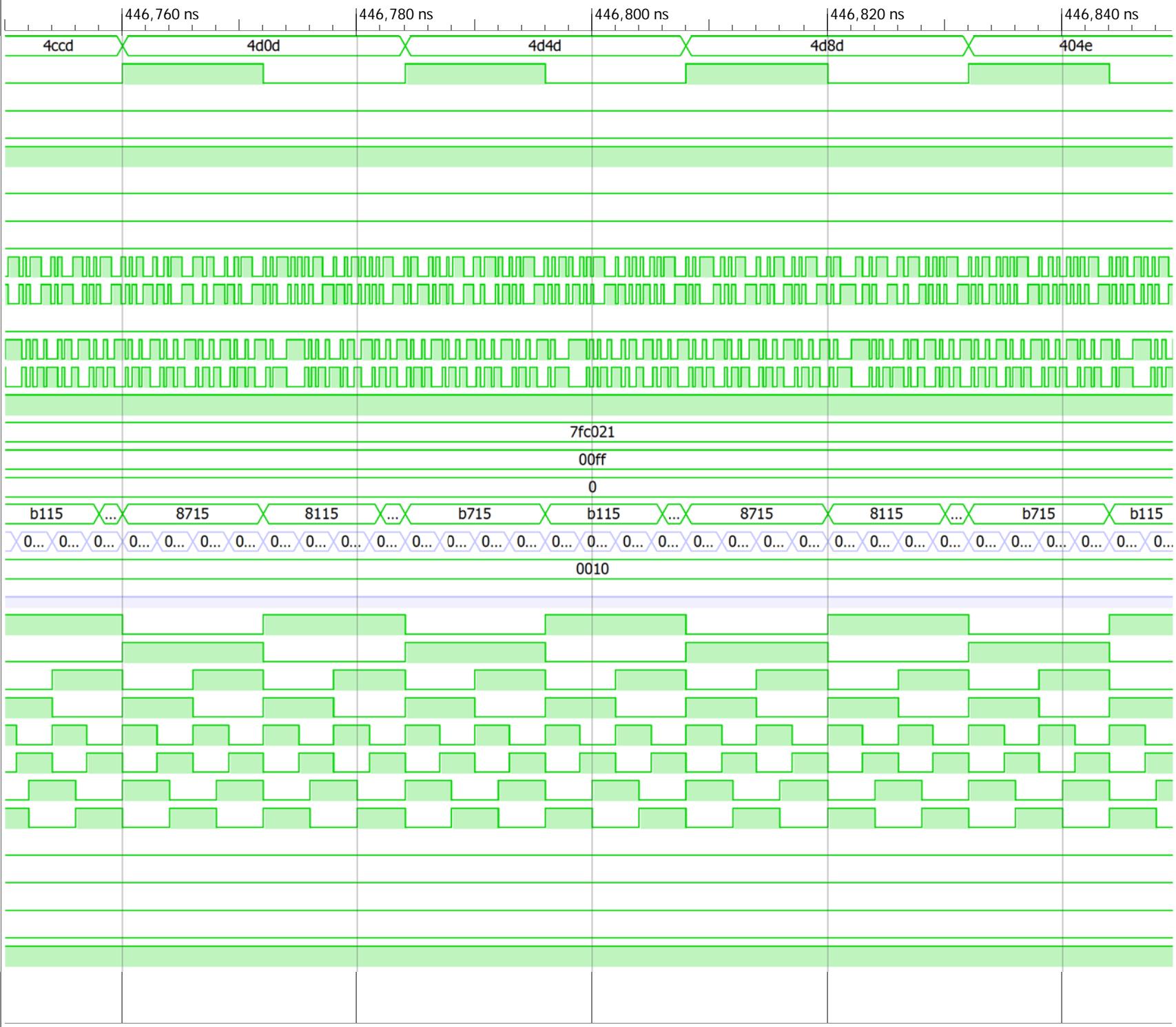
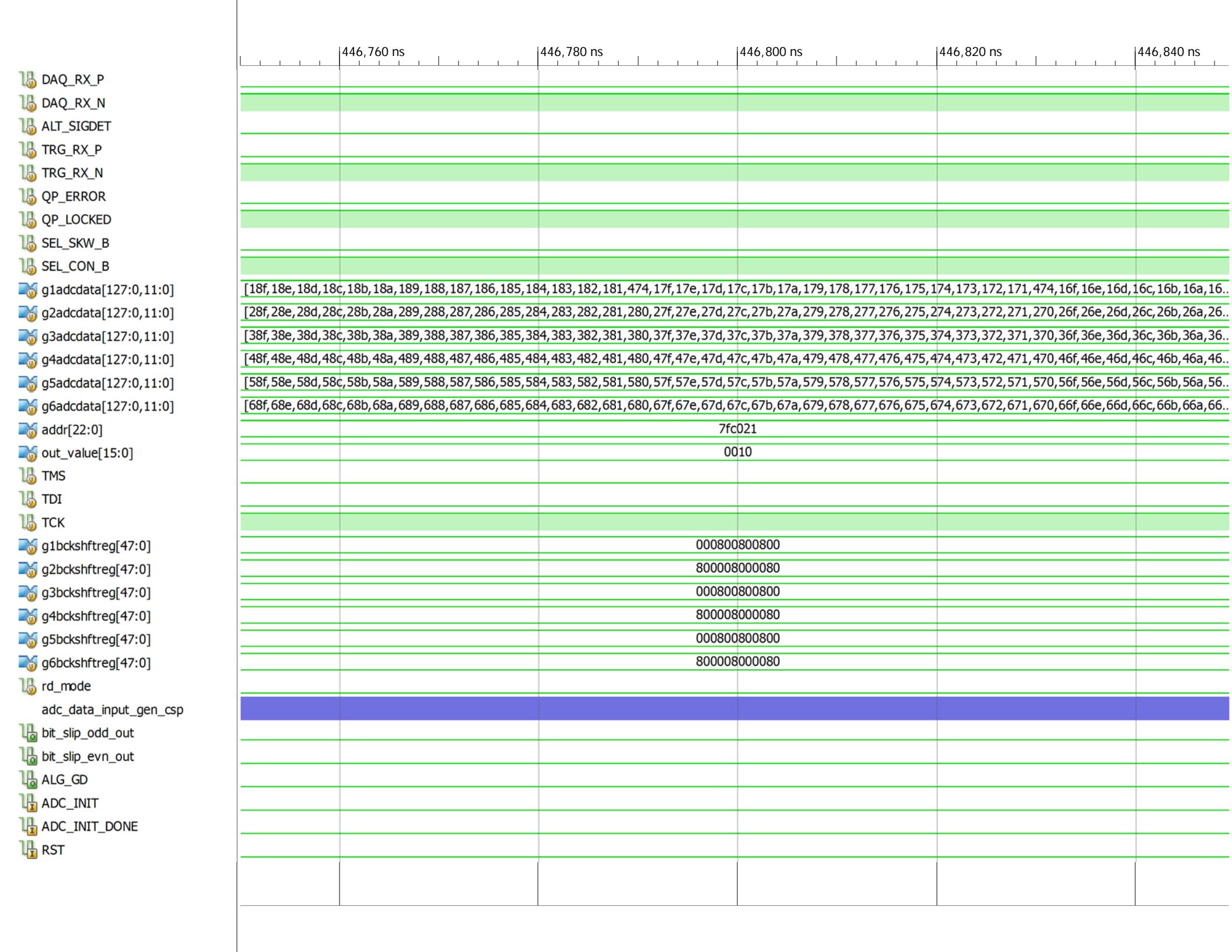
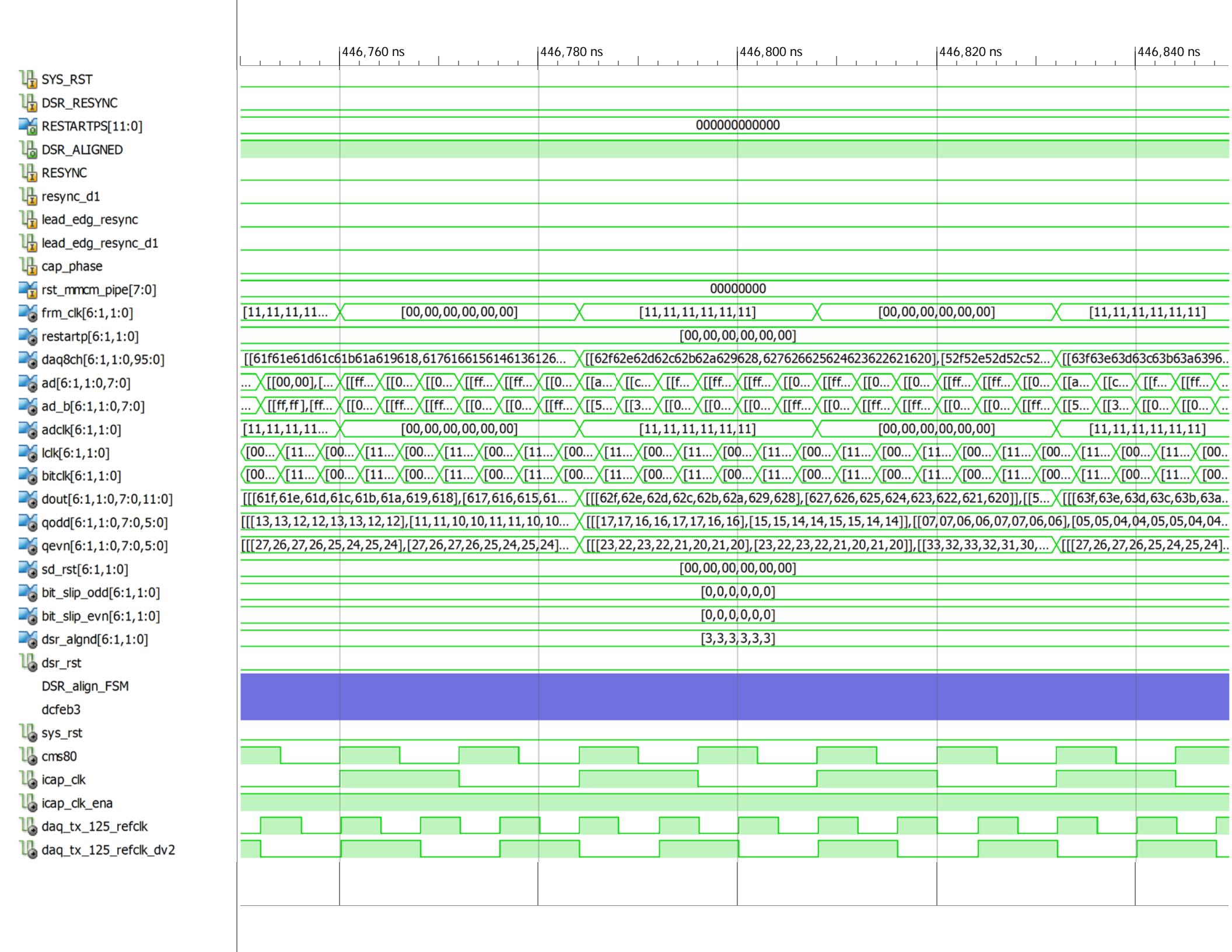


- DATAOUT[15:0]
- CHAN_LNK_CLK
- MB_FIFO_PUSH_B
- MOVLP
- OVLPMUX
- DATAAVAIL
- ENDWORD
- DAQ_TDIS
- DAQ_TX_P
- DAQ_TX_N
- TRG_TDIS
- TRG_TX_P
- TRG_TX_N
- QP_RST_B
- BPI_AD[22:0]
- CFG_DAT[15:0]
- TP_B24_[2:0]
- TP_B25_[15:0]
- TP_B35_[14:1]
- stored_data[15:0]
- TDO
- CMS_CLK_N
- CMS_CLK_P
- CMS80_N
- CMS80_P
- QPLL_CLK_AC_P
- QPLL_CLK_AC_N
- XO_CLK_AC_P
- XO_CLK_AC_N
- GC0N
- GC0P
- GC1N
- GC1P
- \SKW_EXTPLS-

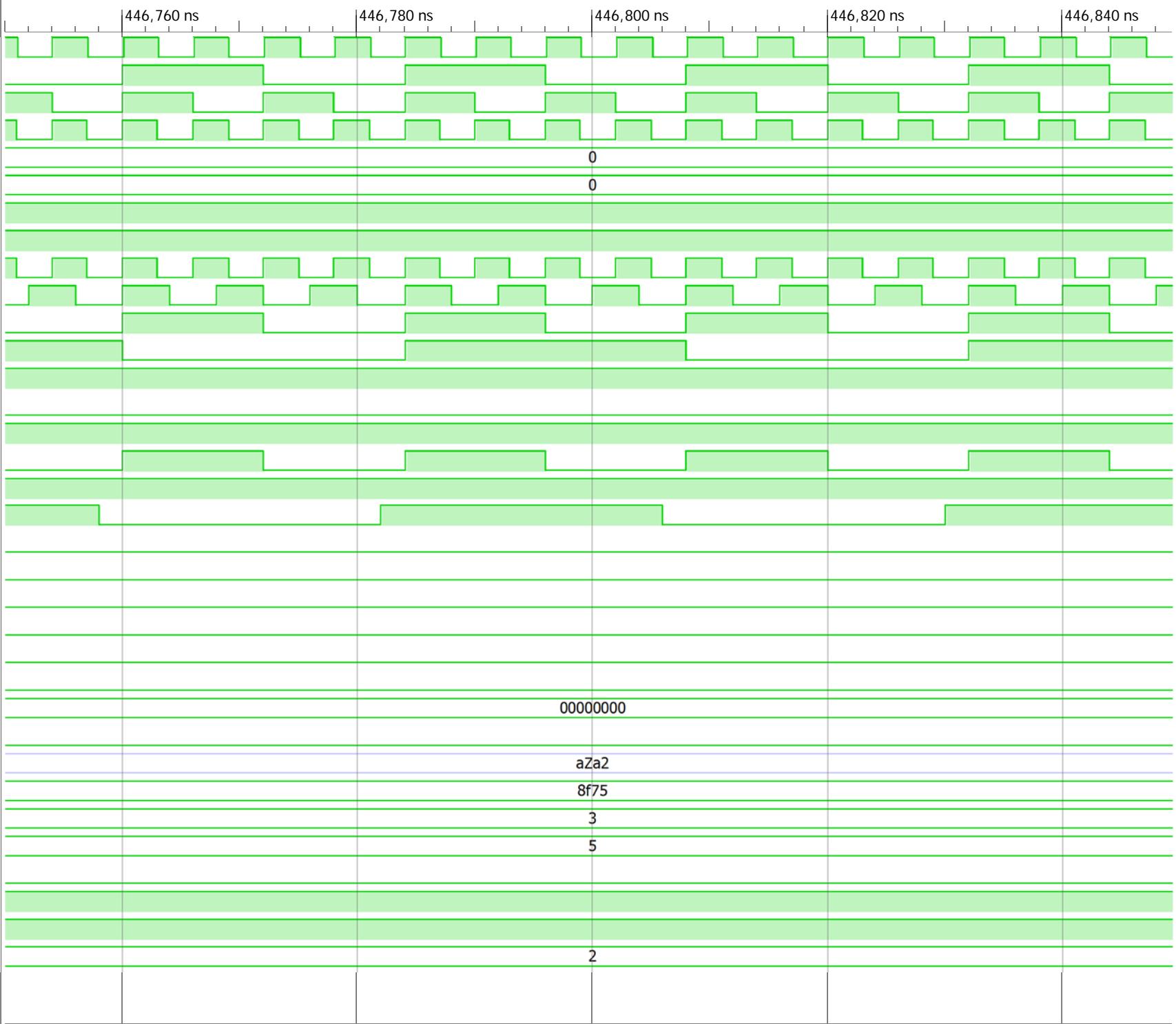




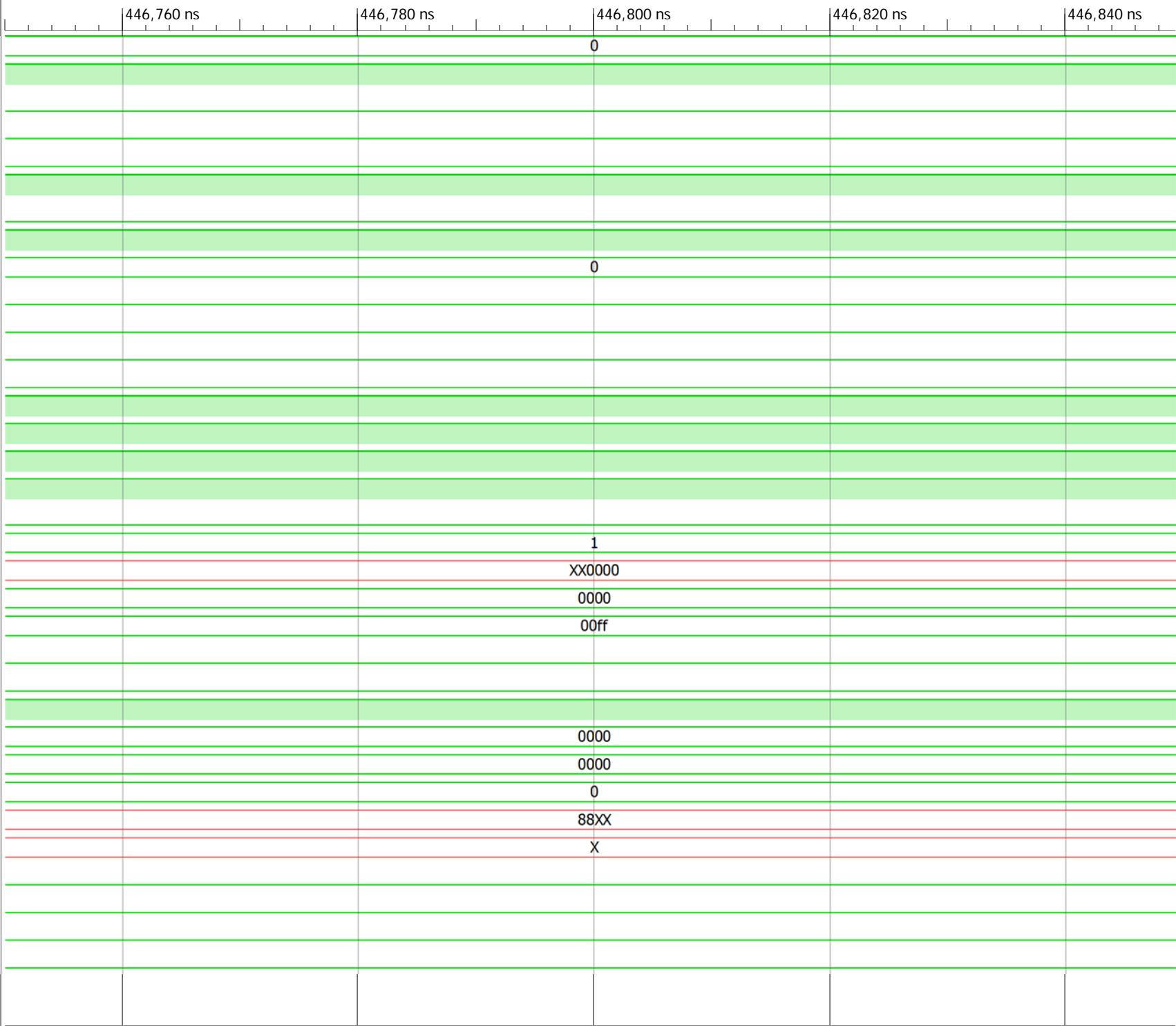




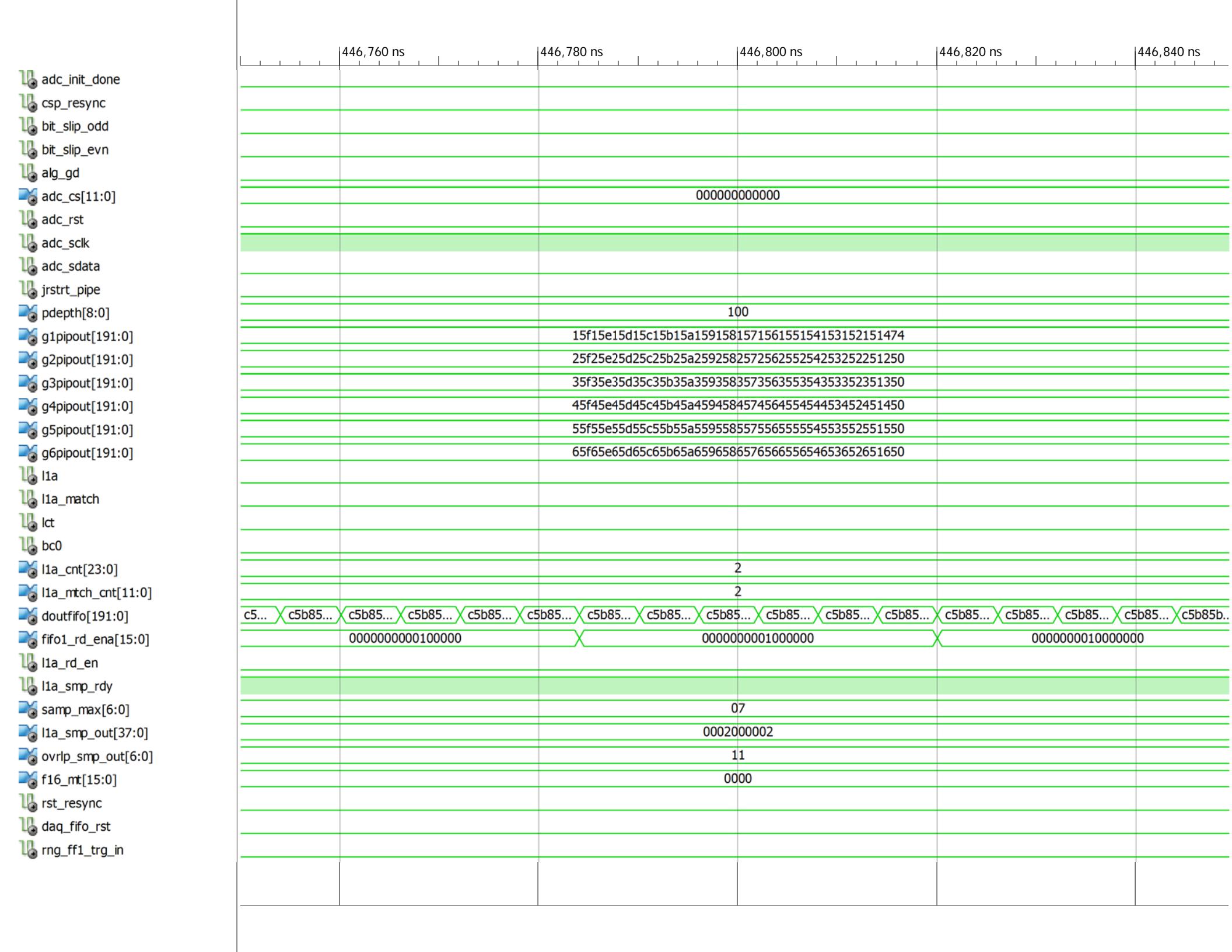
- trg_tx_160_refclk
- comp_clk
- comp_clk80
- comp_clk160
- cmp_clk_phase[4:0]
- samp_clk_phase[2:0]
- trg_tx_pll_lock
- trg_mmcm_lock
- clk160
- clk120
- clk40
- clk20
- clk1mhz
- mmcm_rst
- daq_mmcm_lock
- strtup_clk
- eos
- adc_clk
- dsr_resync
- resync
- resync_d1
- lead_edg_resync
- lead_edg_resync_d1
- cap_phase
- rst_mmcm_pipe[7:0]
- samp_clk_phs_chng
- dcfeb_status[15:0]
- startup_status[15:0]
- al_status[2:0]
- por_state[3:0]
- al_start
- run
- adc_rdy
- ttc_src[1:0]

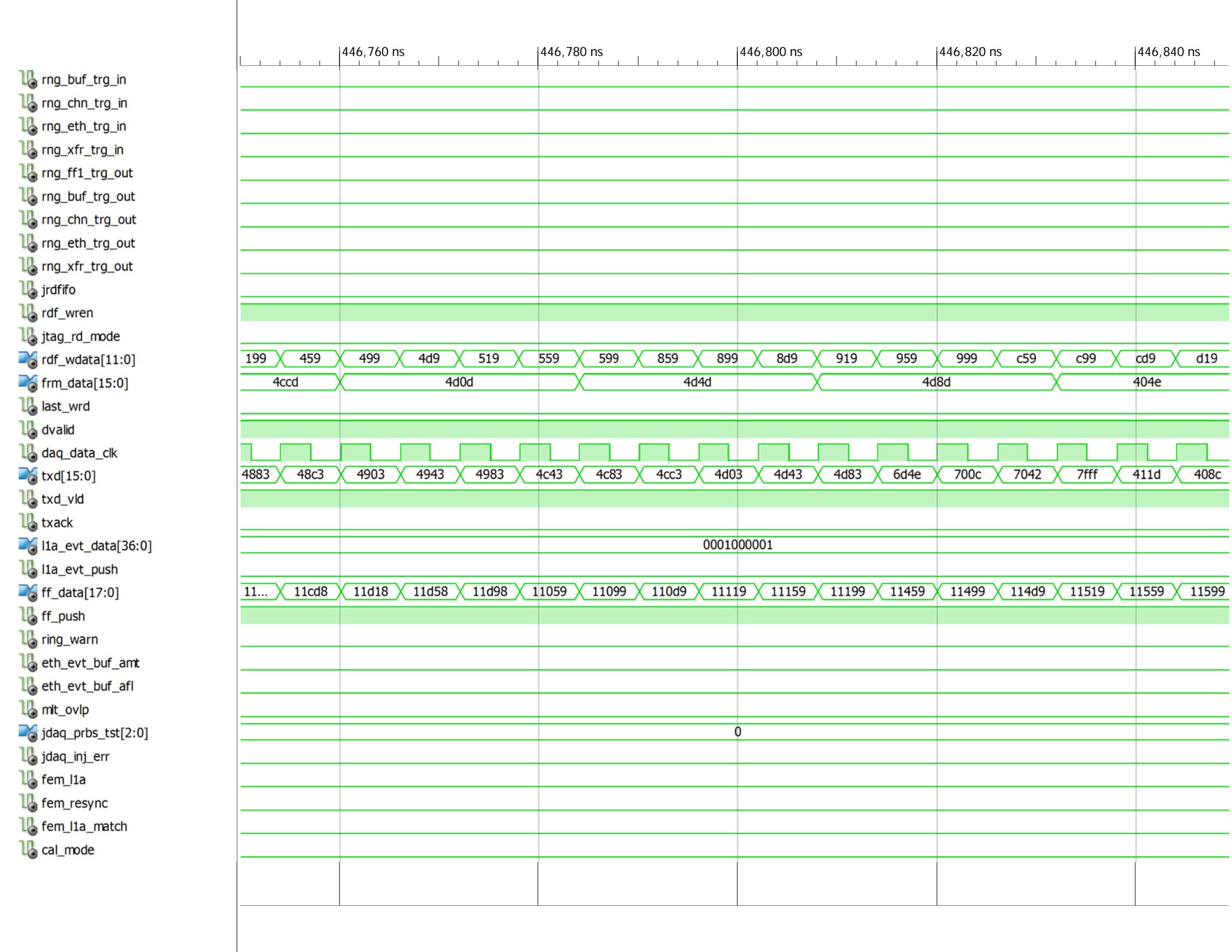


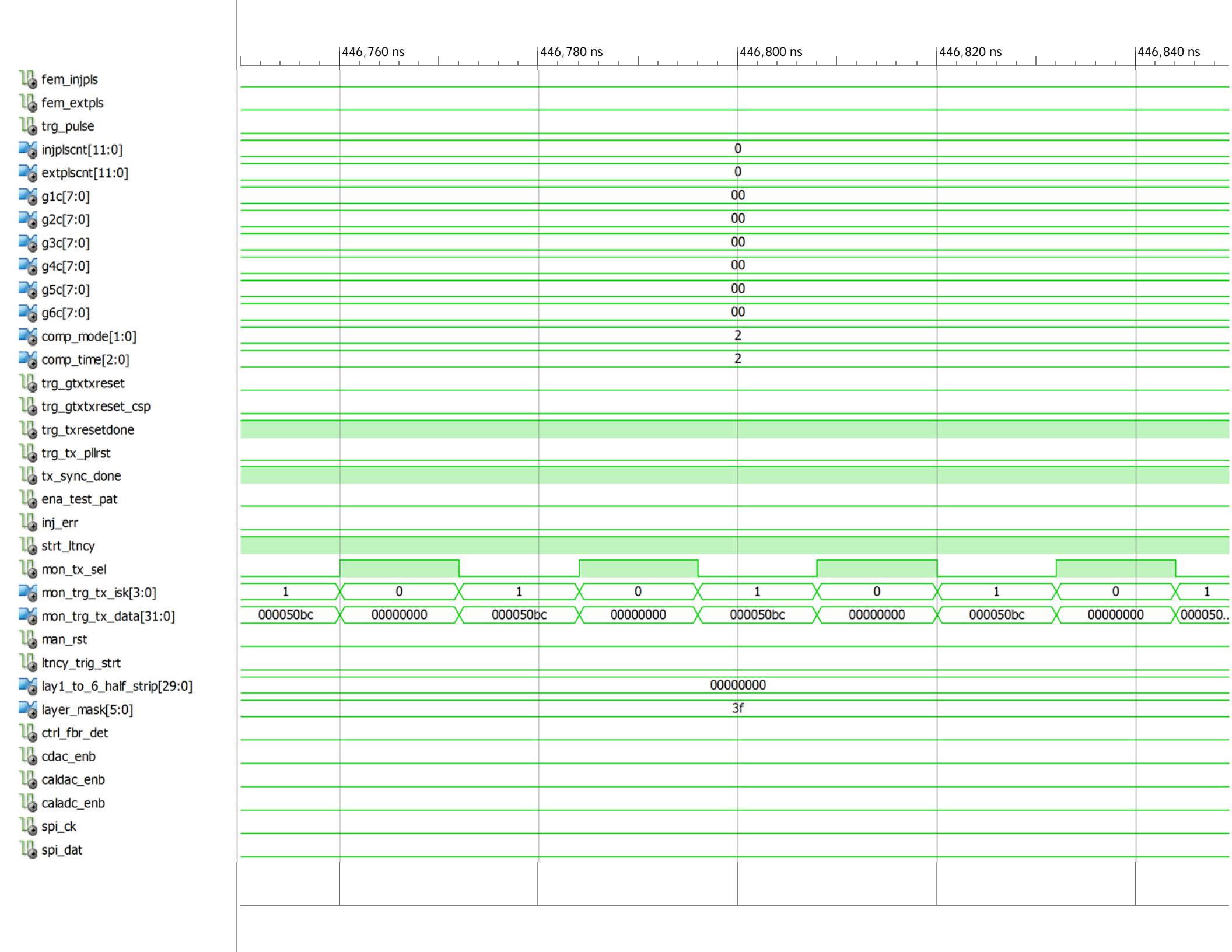
tmb_tx_mode[2:0]
qpll_lock
falling_edge_qpll
qpll_error
qpll_cnt_full
jdaq_rate
rate_1_25
rate_3_2
bc0cnt[11:0]
csp_man_ctrl
use_any_l1a
juse_any_l1a
csp_use_any_l1a
l1a_head
jl1a_head
csp_l1a_head
bpi_active
bpi_busy
bpi_op[1:0]
bpi_addr[22:0]
bpi_data_to[15:0]
bpi_data_from[15:0]
bpi_load_data
bpi_execute
bpi_seq_idle
bpi_rbk_fifo_data[15:0]
bpi_cmd_fifo_data[15:0]
bpi_rbk_wrd_cnt[10:0]
bpi_status[15:0]
bpi_timer[31:0]
bpi_jwe
bpi_jre
bpi_jrst
bpi_RST



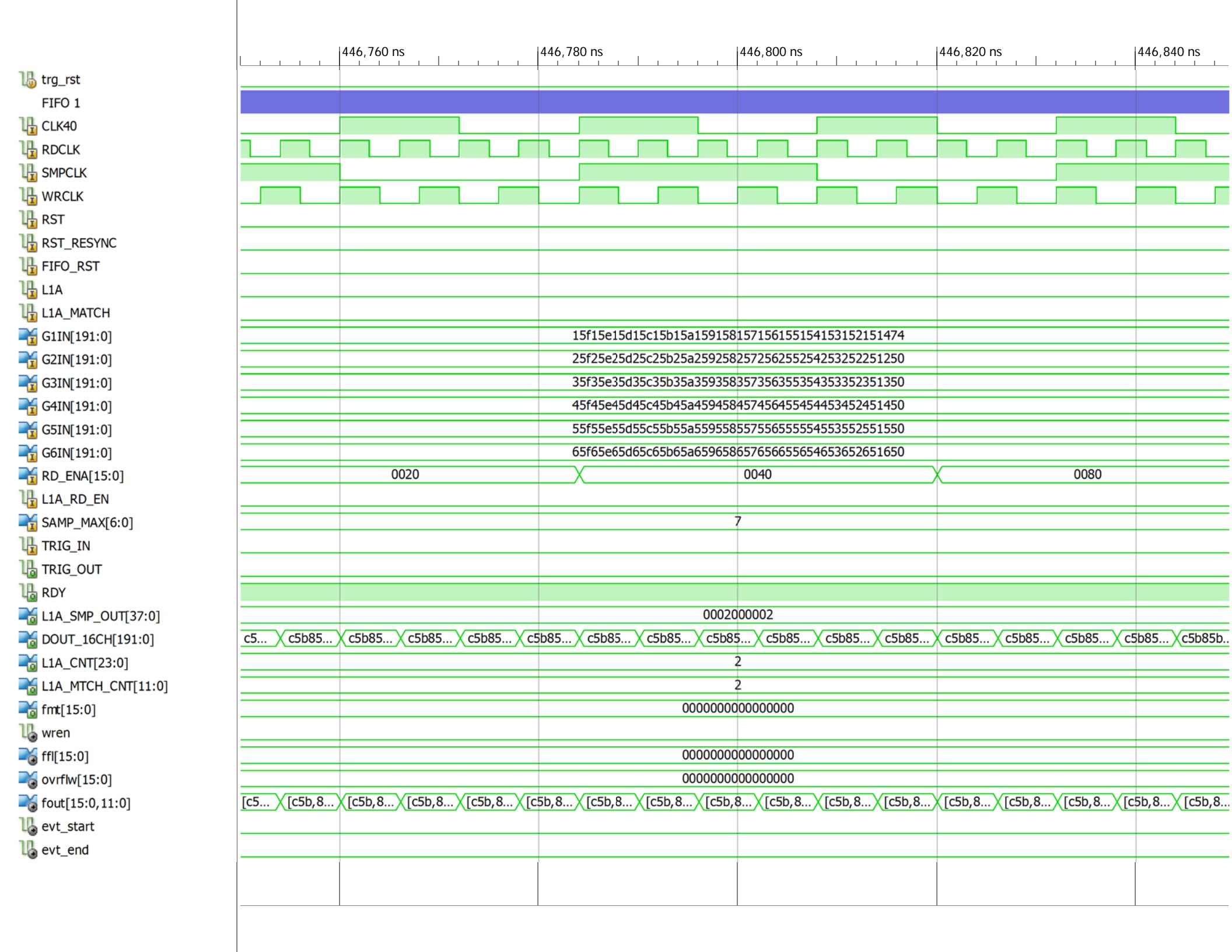


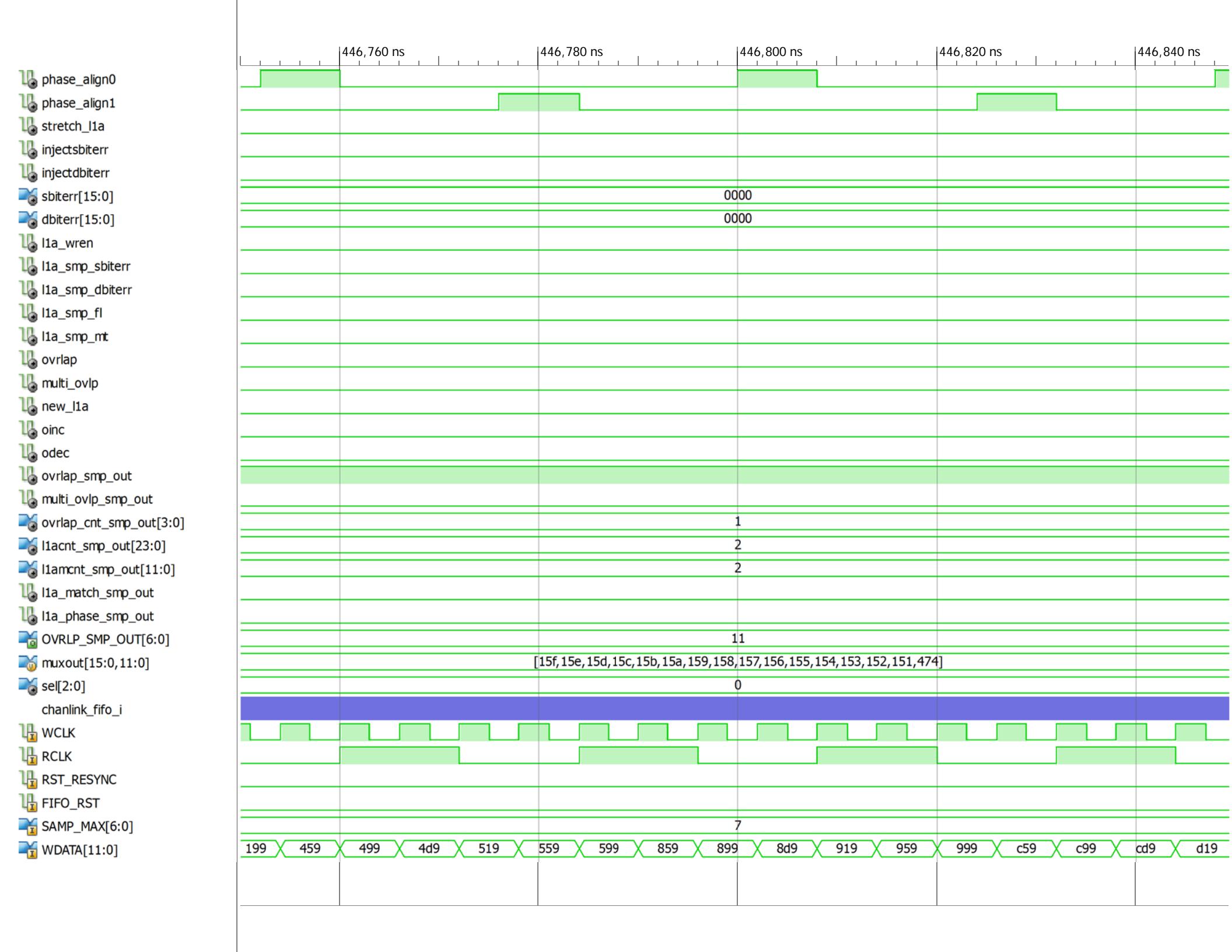








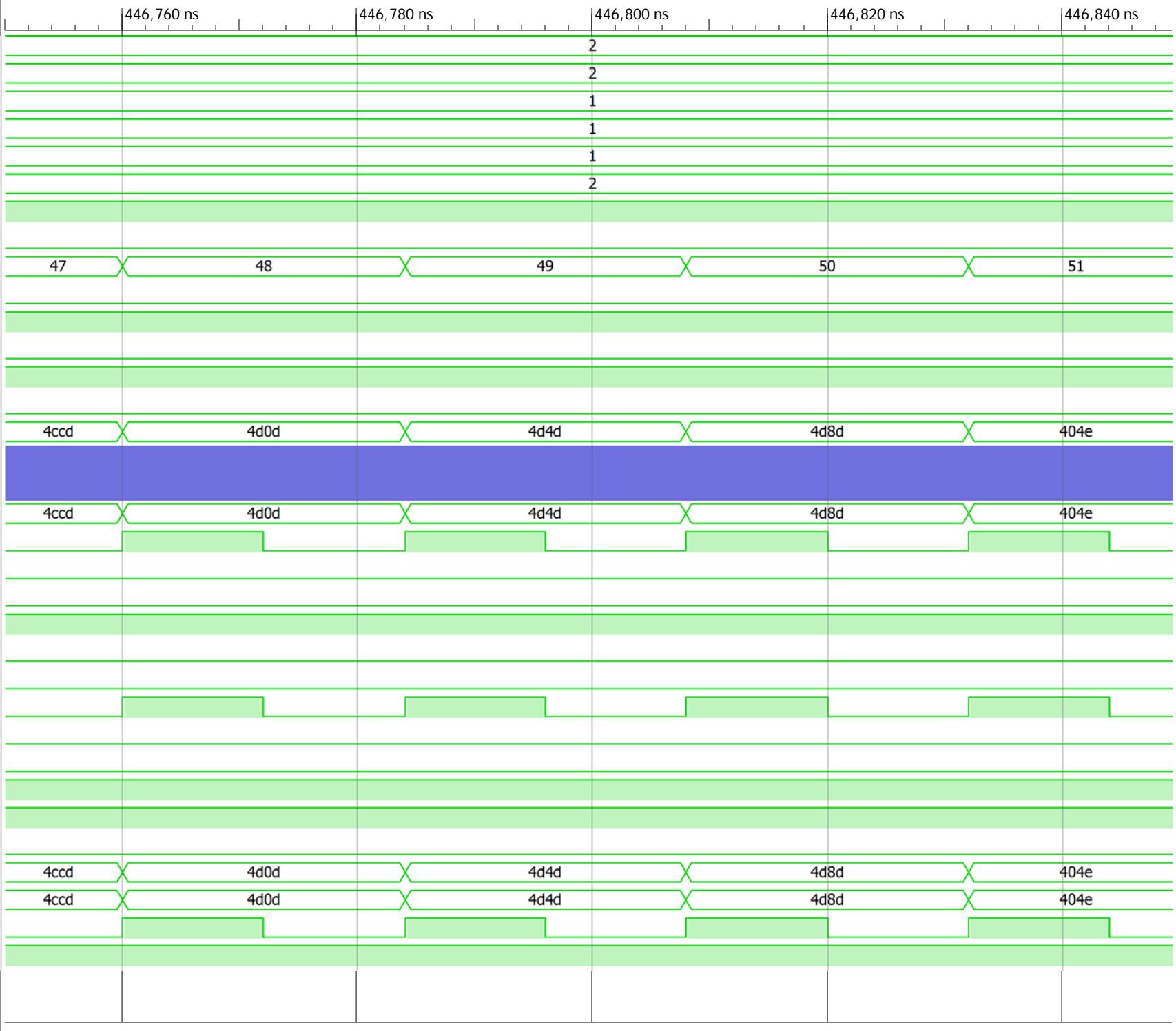


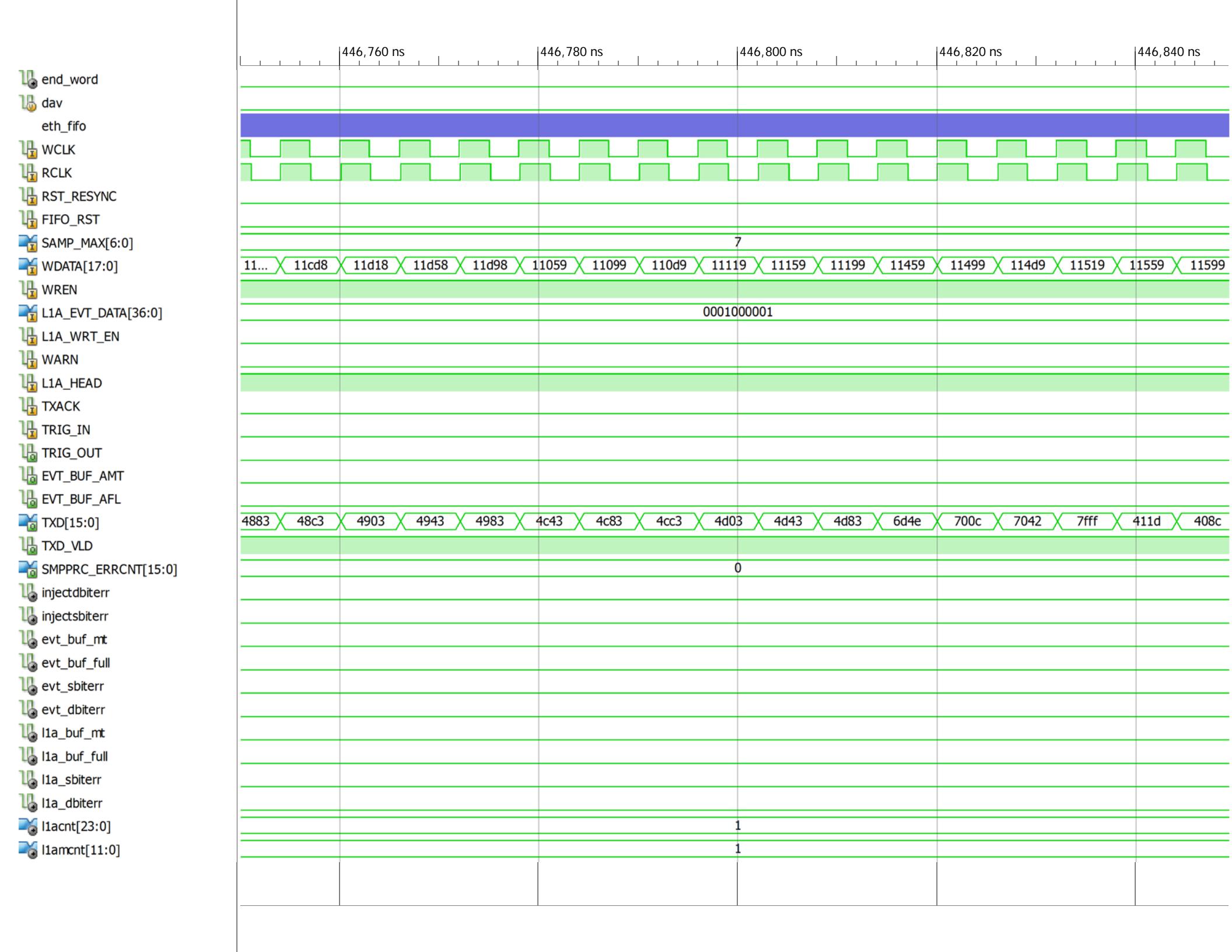


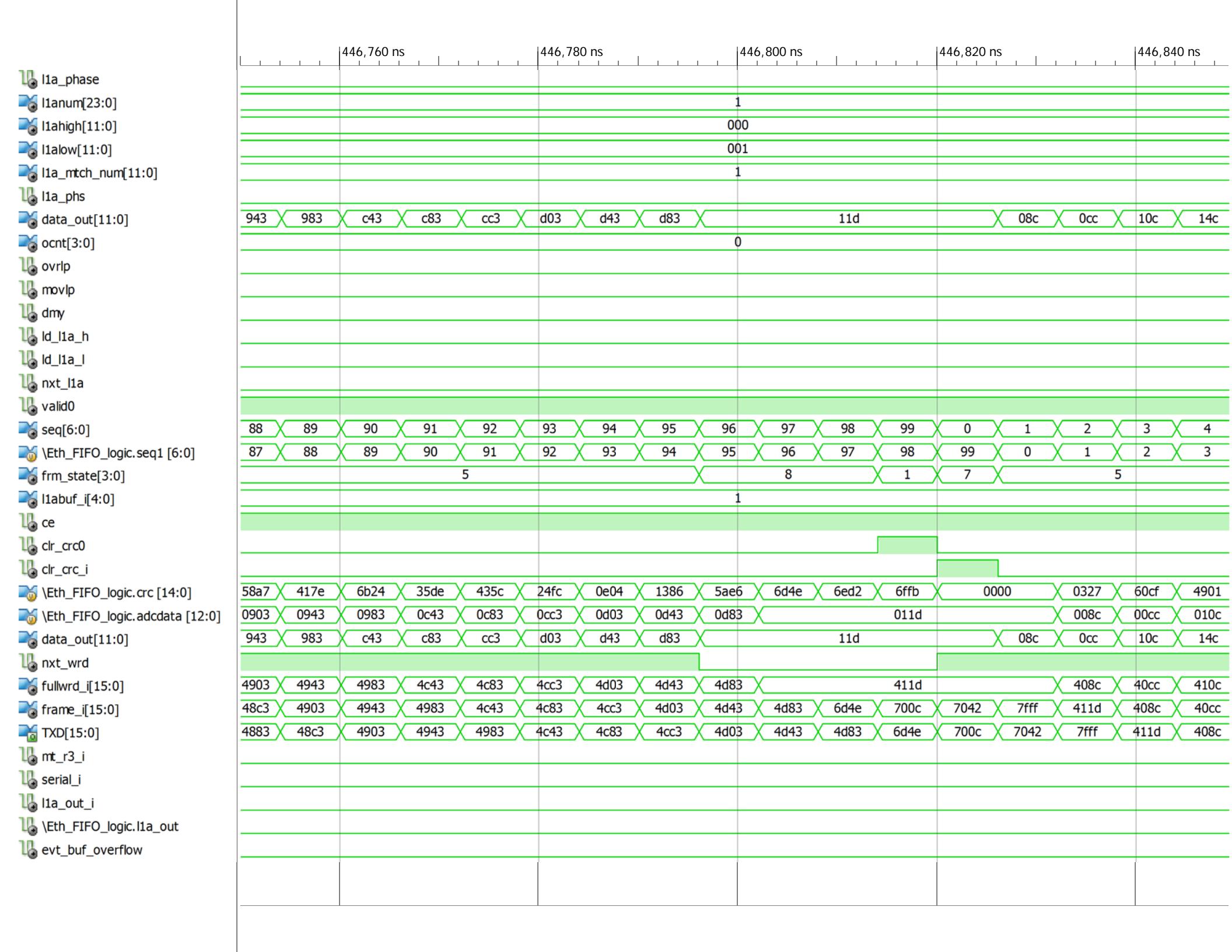
-  WREN
-  L1A_EVT_DATA[37:0]
-  OVRLP_EVT_DATA[6:0]
-  L1A_WRT_EN
-  WARN
-  TRIG_IN
-  TRIG_OUT
-  injectdbiterr
-  injectsbiterr
-  evt_buf_overflow
-  evt_buf_underflow
-  evt_buf_amt
-  evt_buf_afl
-  evt_buf_mt
-  evt_buf_full
-  evt_sbterr
-  evt_dbiterr
-  l1a_buf_mt
-  l1a_buf_full
-  l1a_sbterr
-  l1a_dbiterr
-  movlp
-  end_evt
-  ocnt[3:0]
-  data_out[11:0]
-  l1a_phase
-  l1a_match_smp
-  l1a_push
-  ovrlp_smp
-  multi_ovlp_smp
-  evt_end_smp
-  l1a_phs
-  nxt_l1a
-  nxt_wrd

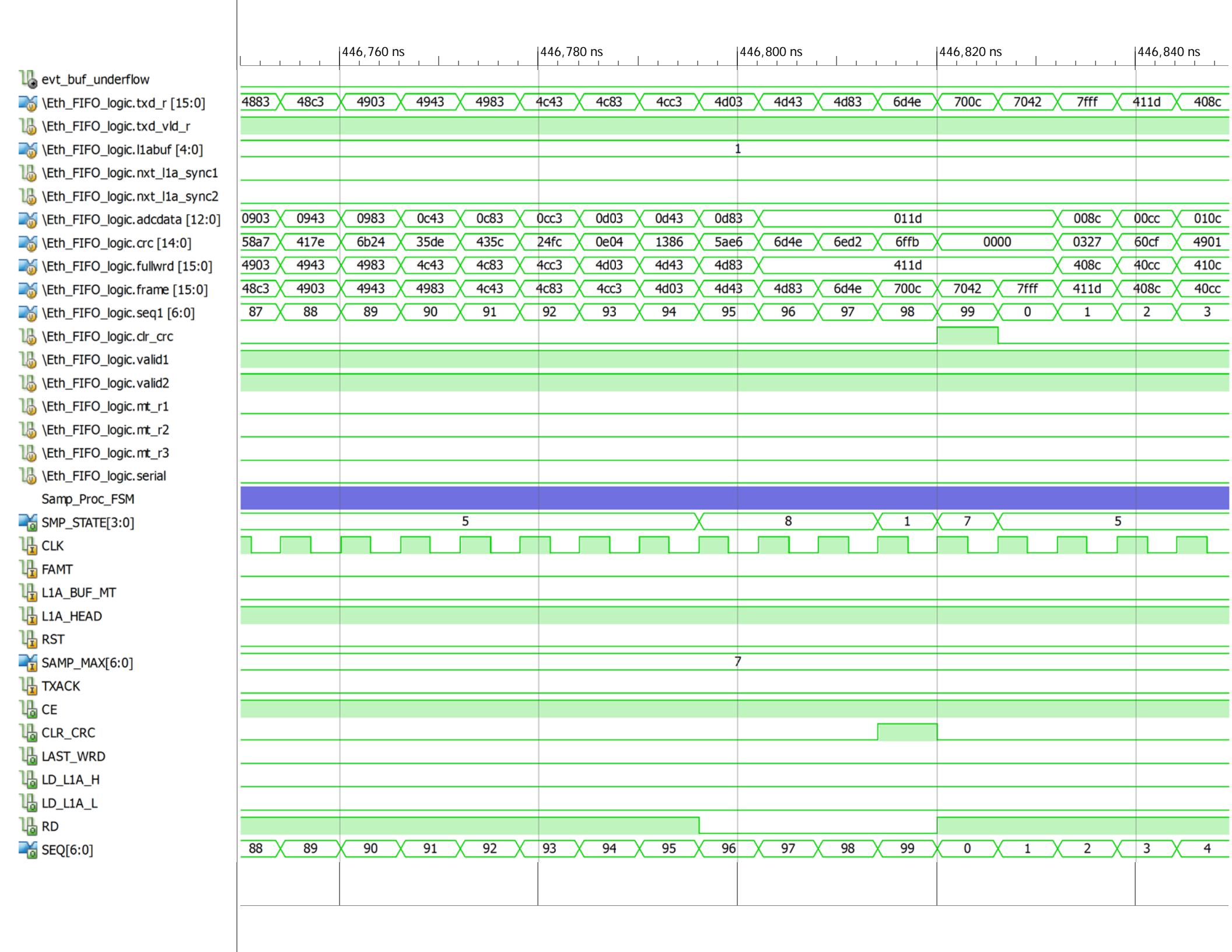


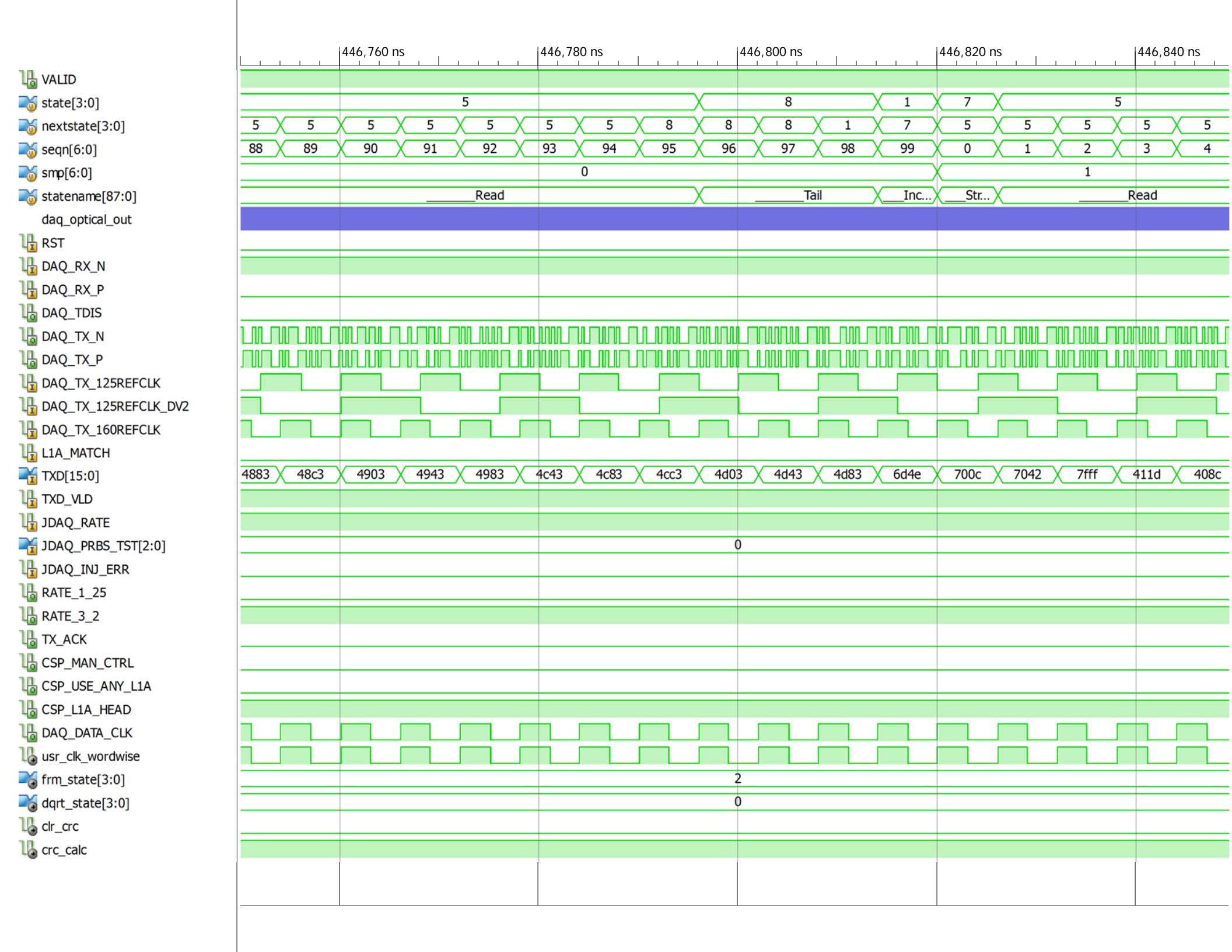
- l1acnt[23:0]
- l1amcnt[11:0]
- overlap_cnt[3:0]
- l1anum[23:0]
- l1a_mtch_num[11:0]
- frm_state[2:0]
- valid0
- clr_crc0
- seq[6:0]
- ovrlp
- OVLP_MUX
- LAST_WRD
- DVALID
- MLT_OVLP
- DOUT[15:0]
 - ChnLnk_Frame_FSM
 - ch_link1
- DATAOUT[15:0]
- CHAN_LNK_CLK
- MB_FIFO_PUSH_B
- MOVL_P
- OVLPMUX
- DATAAVAIL
- ENDWORD
- CLK
- L1A_MATCH
- LAST_WRD
- DVALID
- OVLP_MUX
- MLT_OVLP
- FRAME_DATA[15:0]
- chan_dat_out[15:0]
- clnk_clk
- mb_fifo_push



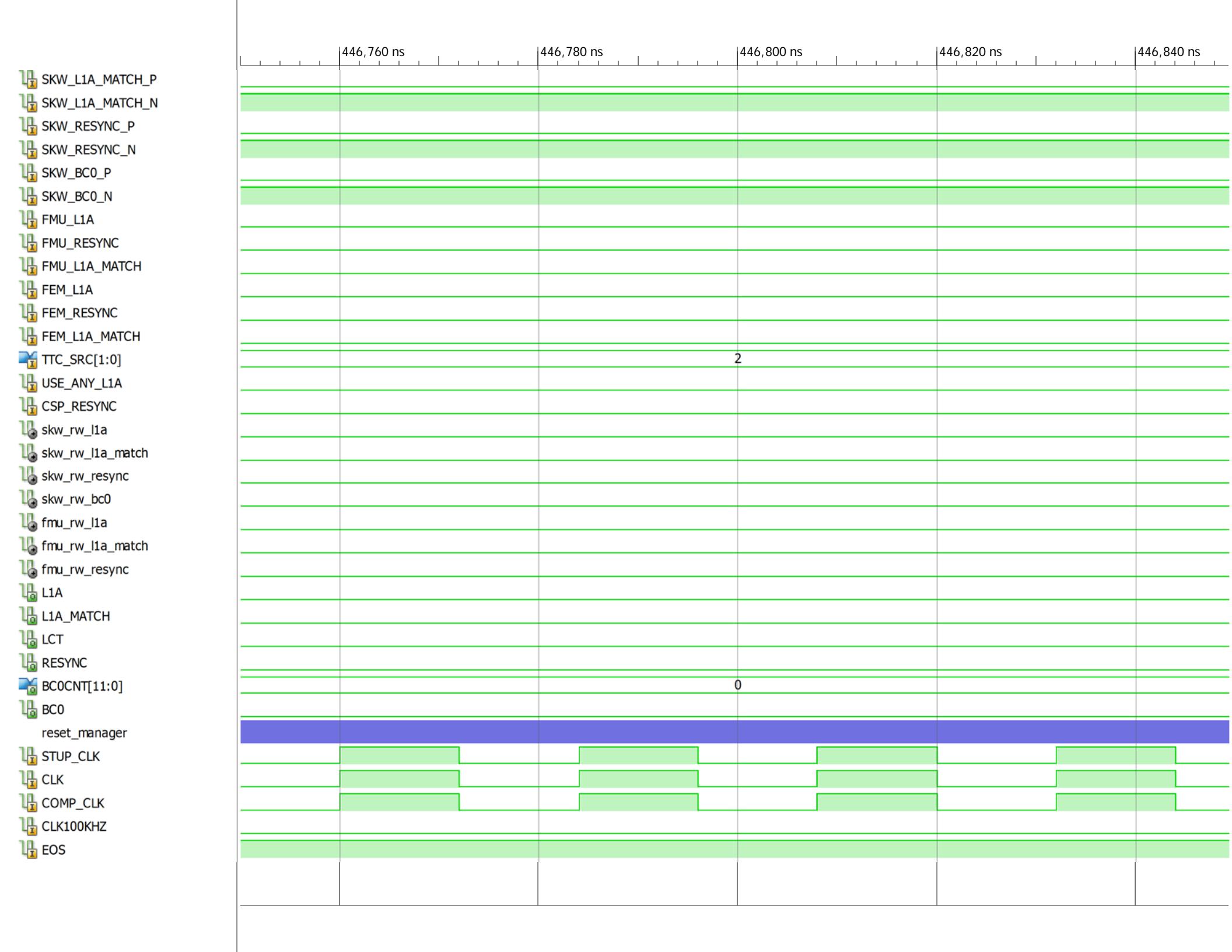












- JTAG_SYS_RST
- CSP_SYS_RST
- DAQ_MMCM_LOCK
- TRG_MMCM_LOCK
- CMP_PHS_CHANGE
- TRG_SYNC_DONE
- QP_ERROR
- QP_LOCKED
- AL_DONE
- ADC_INIT_DONE
- BPI_SEQ_IDLE
- ADC_INIT
- ADC_RDY
- TRG_GTXXRESET
- MMCM_RST
- SYS_MON_RST
- ADC_RST
- TRG_RST
- DSR_RST
- QPLL_LOCK
- QPLL_ERROR
- POR_STATE[3:0]
- adc_init_rst_i
- al_start_i
- por_i
- run_i
- restart_all
- AL_START
- SYS_RST
- RUN
- ADC_Init FSM

