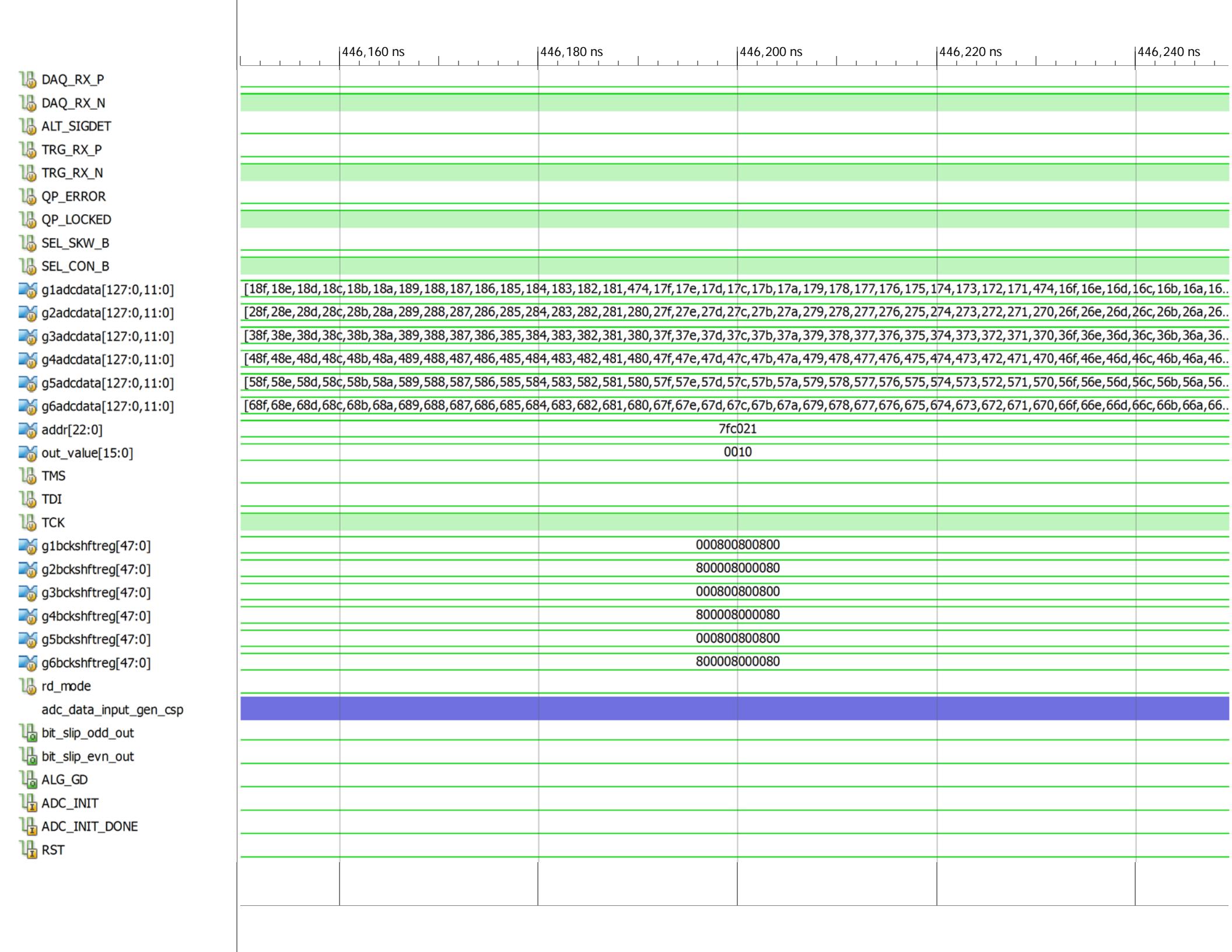
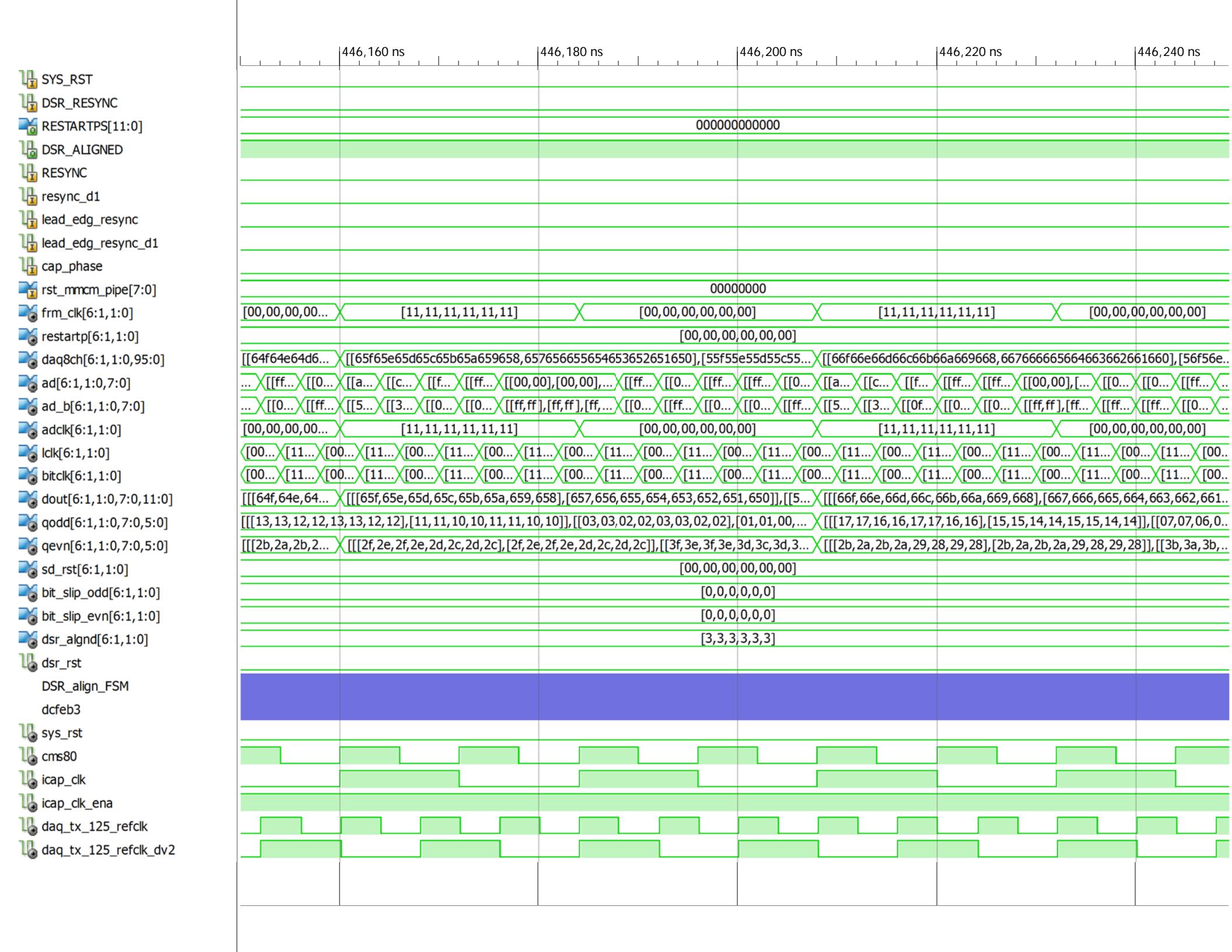


- DATAOUT[15:0]
- CHAN\_LNK\_CLK
- MB\_FIFO\_PUSH\_B
- MOVLP
- OVLPMUX
- DATAAVAIL
- ENDWORD
- DAQ\_TDIS
- DAQ\_TX\_P
- DAQ\_TX\_N
- TRG\_TDIS
- TRG\_TX\_P
- TRG\_TX\_N
- QP\_RST\_B
- BPI\_AD[22:0]
- CFG\_DAT[15:0]
- TP\_B24\_[2:0]
- TP\_B25\_[15:0]
- TP\_B35\_[14:1]
- stored\_data[15:0]
- TDO
- CMS\_CLK\_N
- CMS\_CLK\_P
- CMS80\_N
- CMS80\_P
- QPLL\_CLK\_AC\_P
- QPLL\_CLK\_AC\_N
- XO\_CLK\_AC\_P
- XO\_CLK\_AC\_N
- GC0N
- GC0P
- GC1N
- GC1P
- \SKW\_EXTPLS-

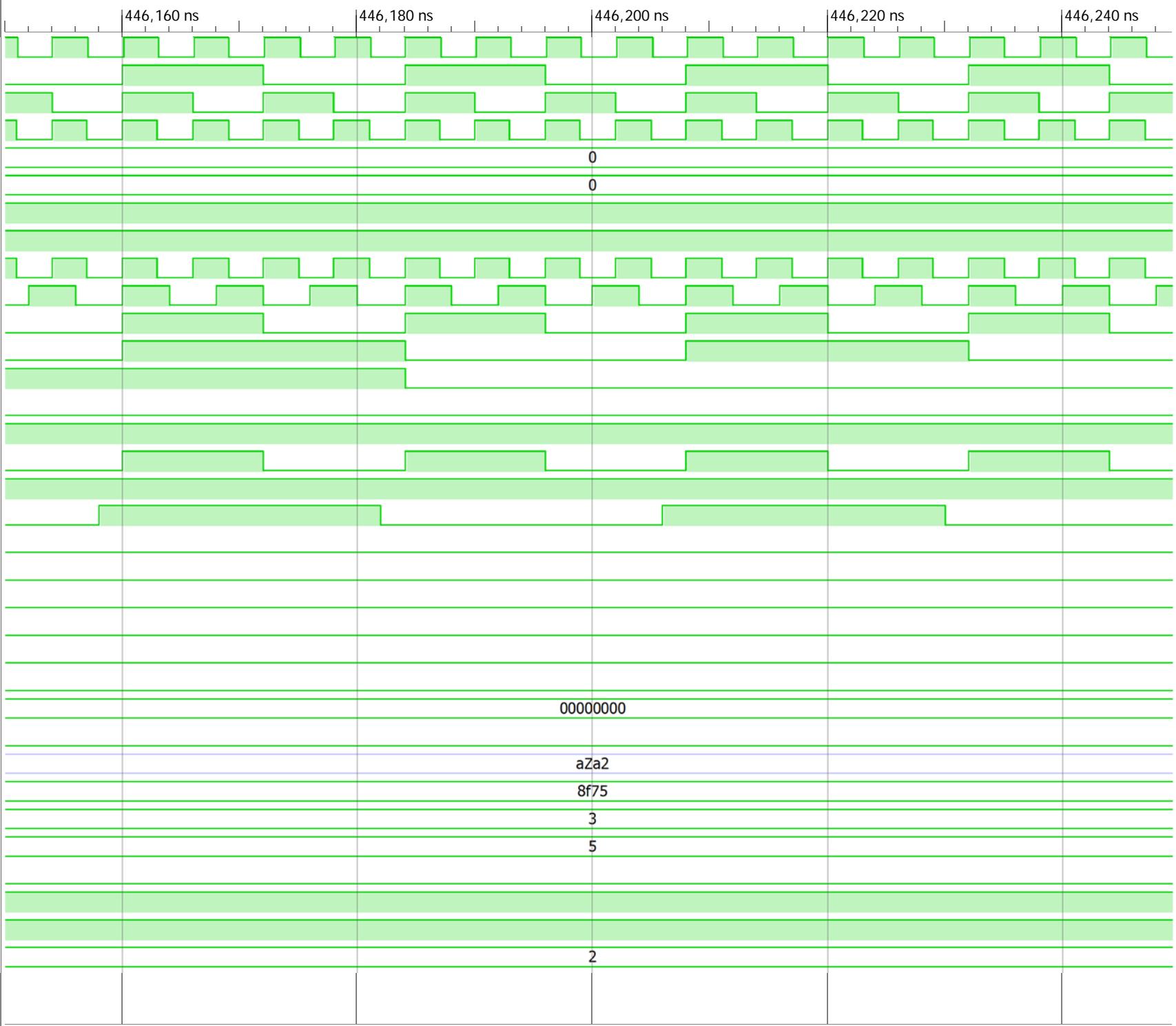




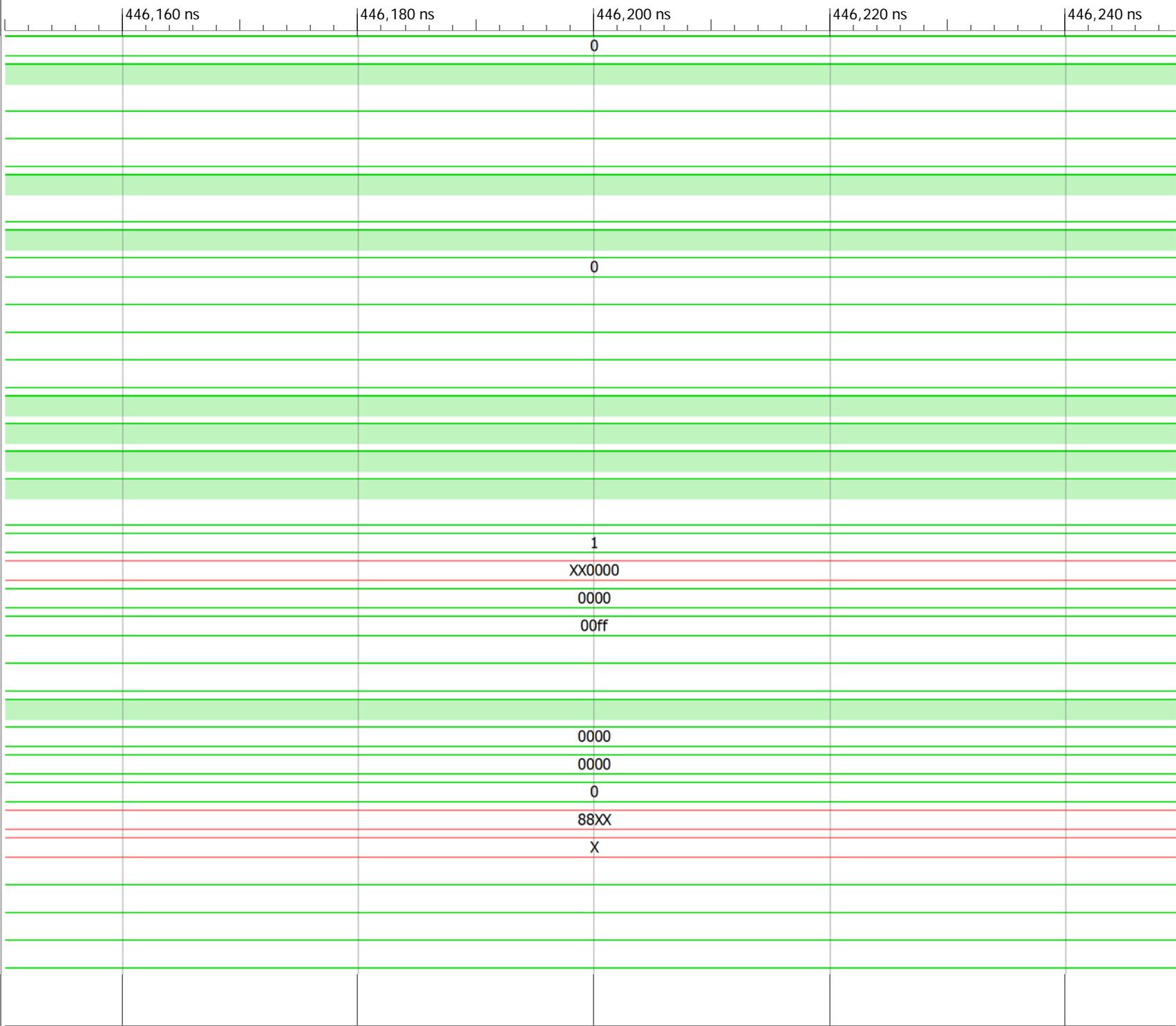


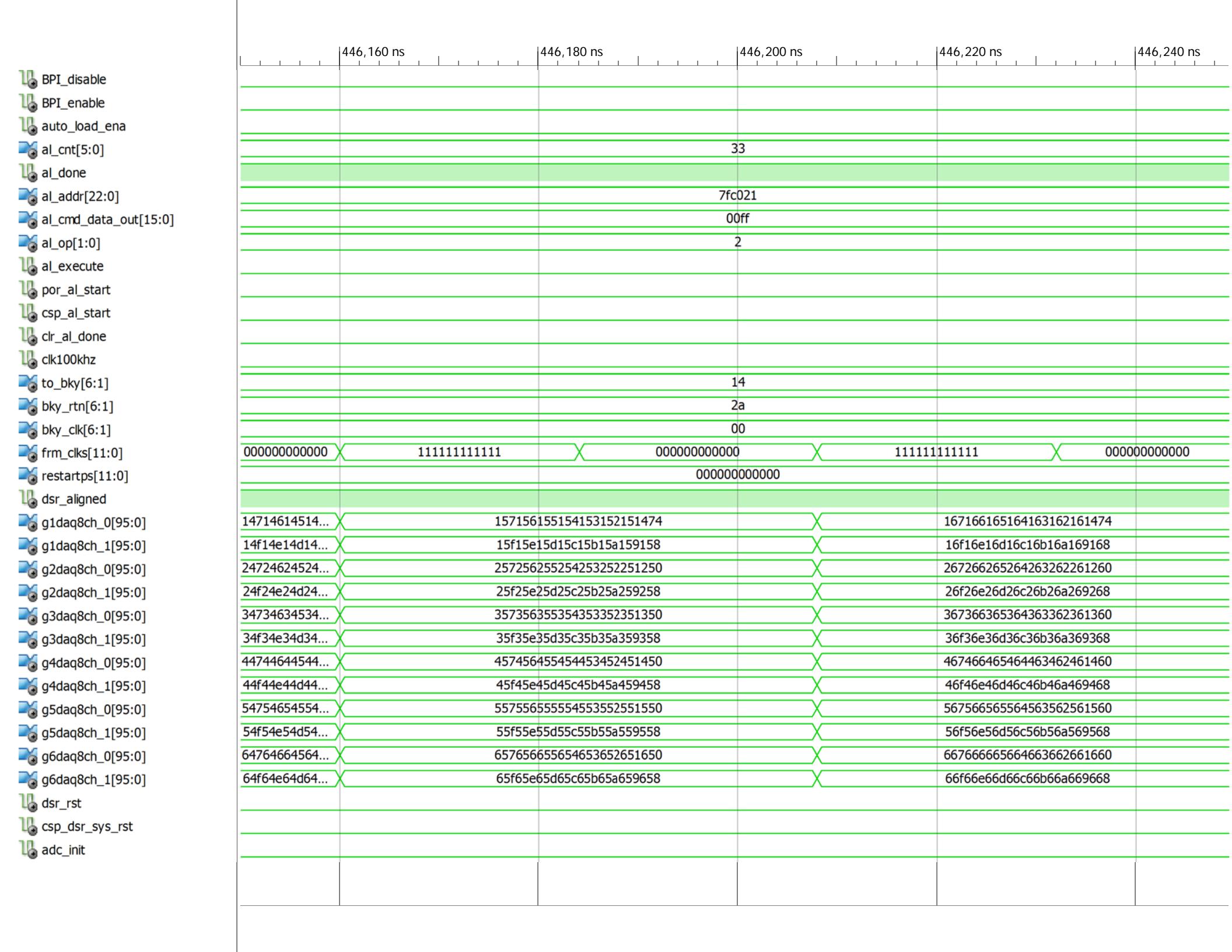


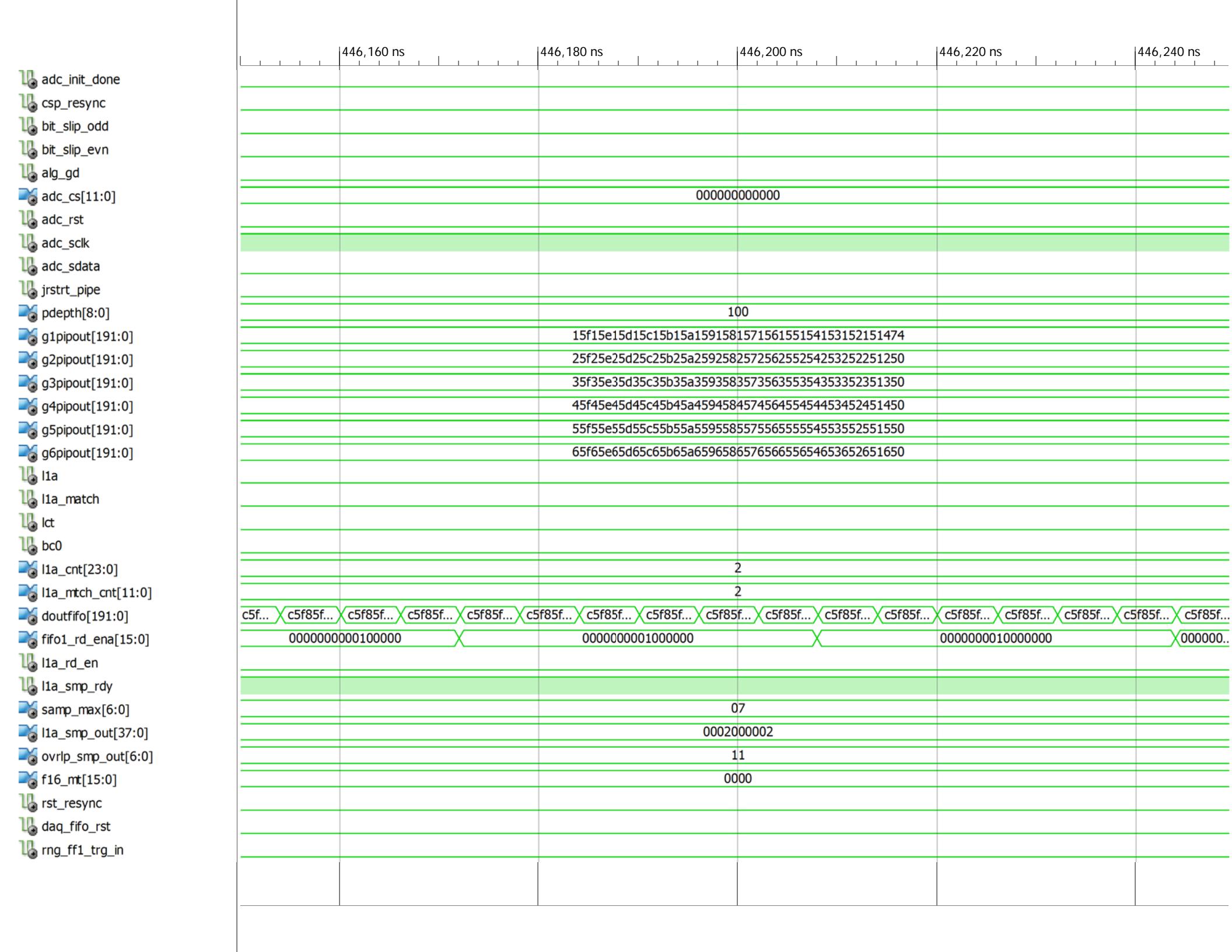
- trg\_tx\_160\_refclk
- comp\_clk
- comp\_clk80
- comp\_clk160
- cmp\_clk\_phase[4:0]
- samp\_clk\_phase[2:0]
- trg\_tx\_pll\_lock
- trg\_mmcm\_lock
- clk160
- clk120
- clk40
- clk20
- clk1mhz
- mmcm\_rst
- daq\_mmcm\_lock
- strtup\_clk
- eos
- adc\_clk
- dsr\_resync
- resync
- resync\_d1
- lead\_edg\_resync
- lead\_edg\_resync\_d1
- cap\_phase
- rst\_mmcm\_pipe[7:0]
- samp\_clk\_phs\_chng
- dcfeb\_status[15:0]
- startup\_status[15:0]
- al\_status[2:0]
- por\_state[3:0]
- al\_start
- run
- adc\_rdy
- ttc\_src[1:0]



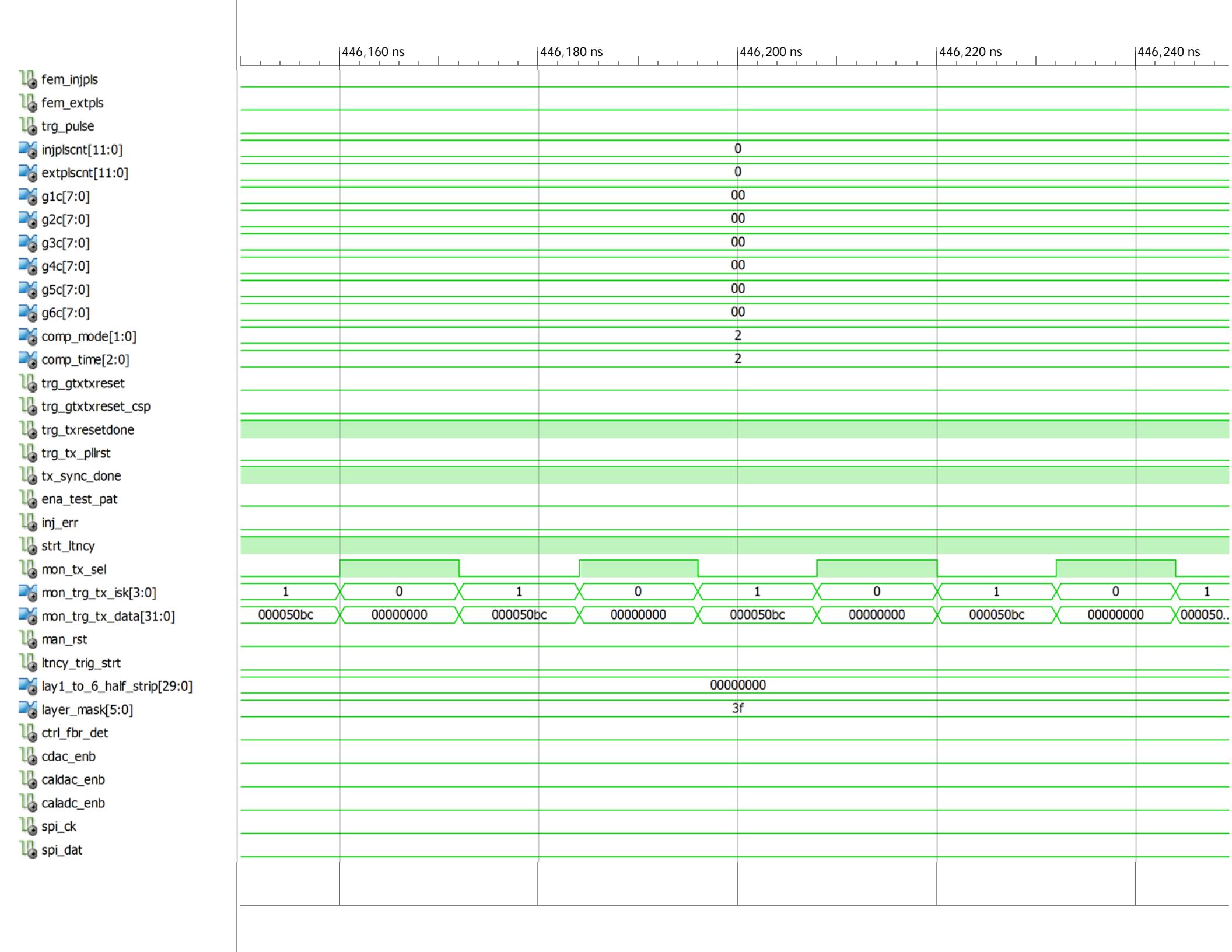
tmb\_tx\_mode[2:0]  
qpll\_lock  
falling\_edge\_qpll  
qpll\_error  
qpll\_cnt\_full  
jdaq\_rate  
rate\_1\_25  
rate\_3\_2  
bc0cnt[11:0]  
csp\_man\_ctrl  
use\_any\_l1a  
juse\_any\_l1a  
csp\_use\_any\_l1a  
l1a\_head  
jl1a\_head  
csp\_l1a\_head  
bpi\_active  
bpi\_busy  
bpi\_op[1:0]  
bpi\_addr[22:0]  
bpi\_data\_to[15:0]  
bpi\_data\_from[15:0]  
bpi\_load\_data  
bpi\_execute  
bpi\_seq\_idle  
bpi\_rbk\_fifo\_data[15:0]  
bpi\_cmd\_fifo\_data[15:0]  
bpi\_rbk\_wrd\_cnt[10:0]  
bpi\_status[15:0]  
bpi\_timer[31:0]  
bpi\_jwe  
bpi\_jre  
bpi\_jrst  
bpi\_RST

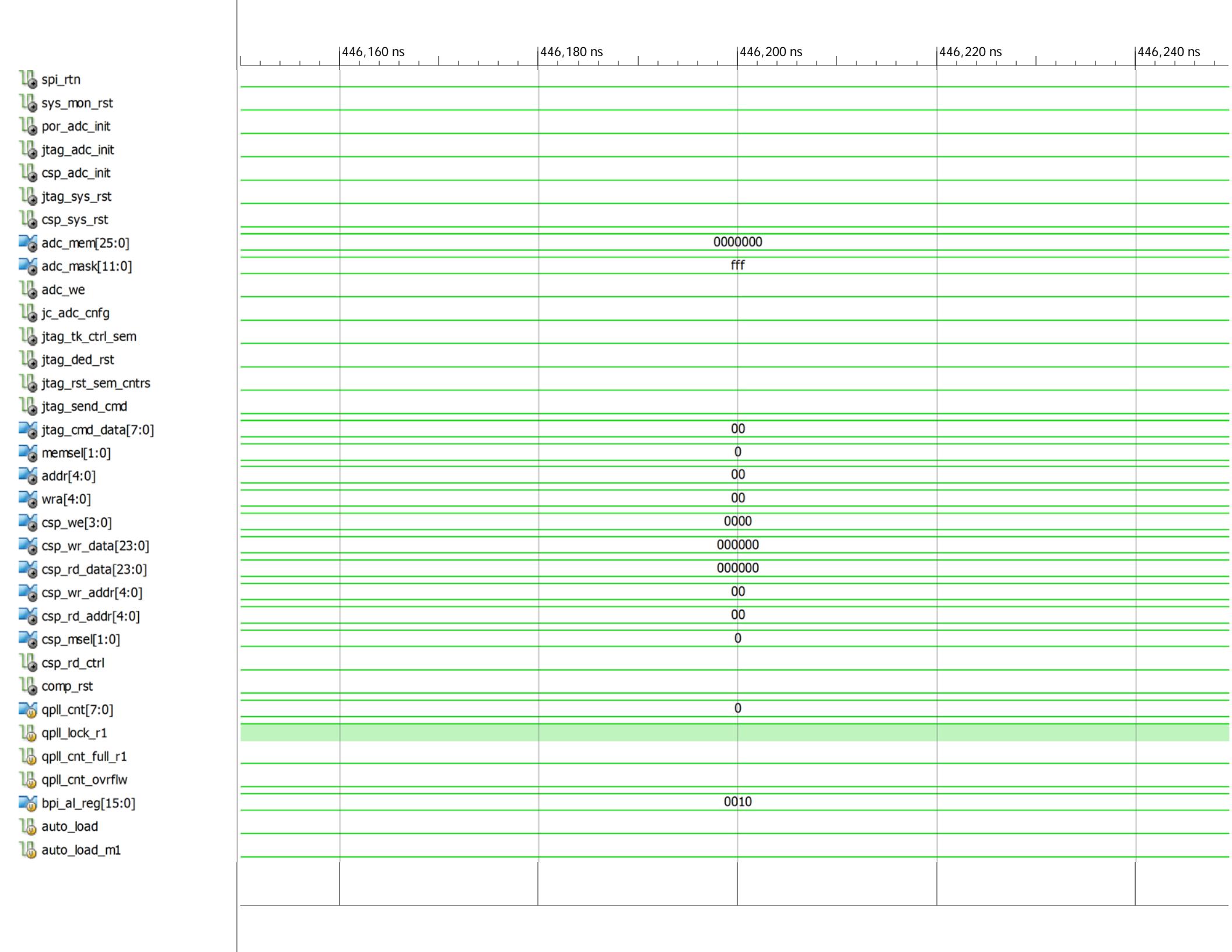




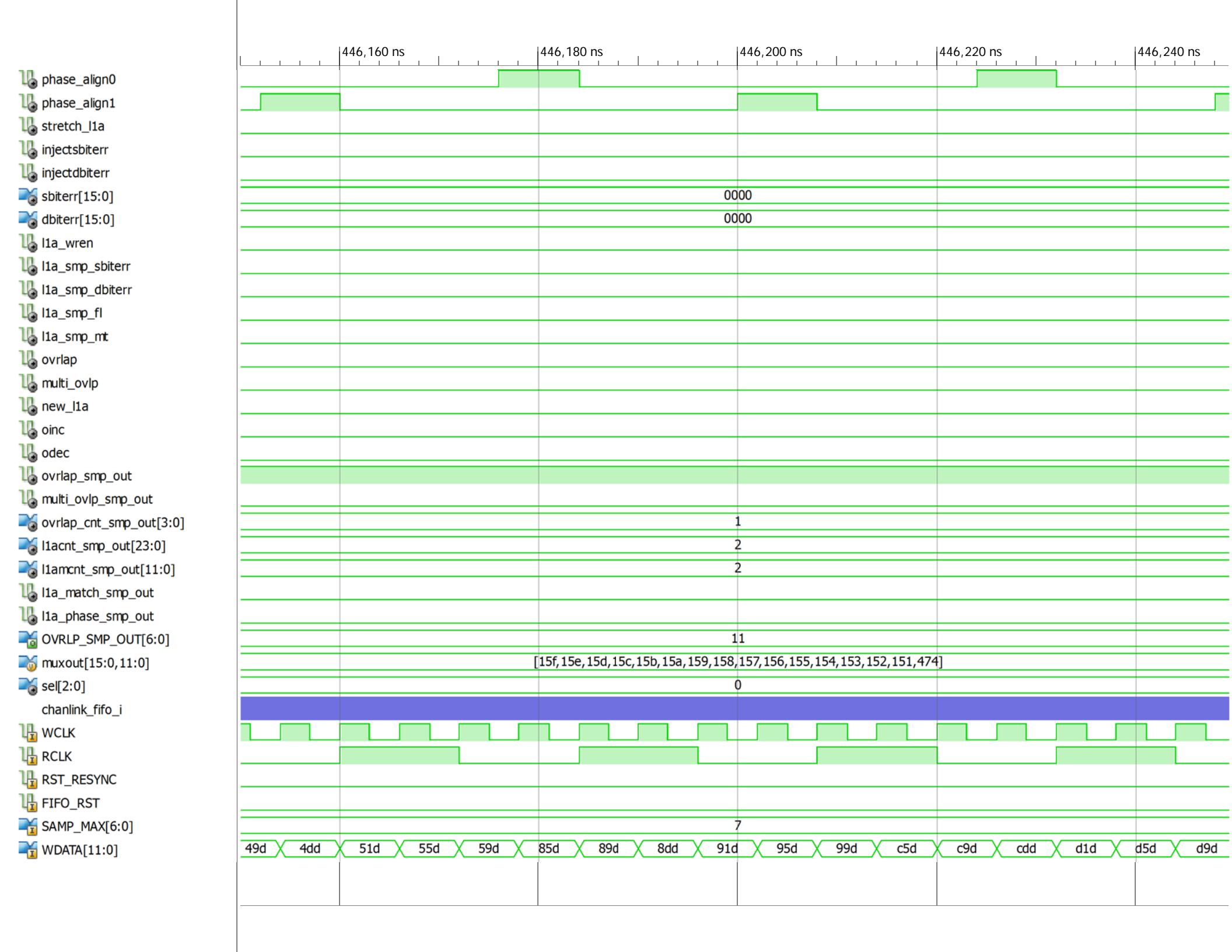








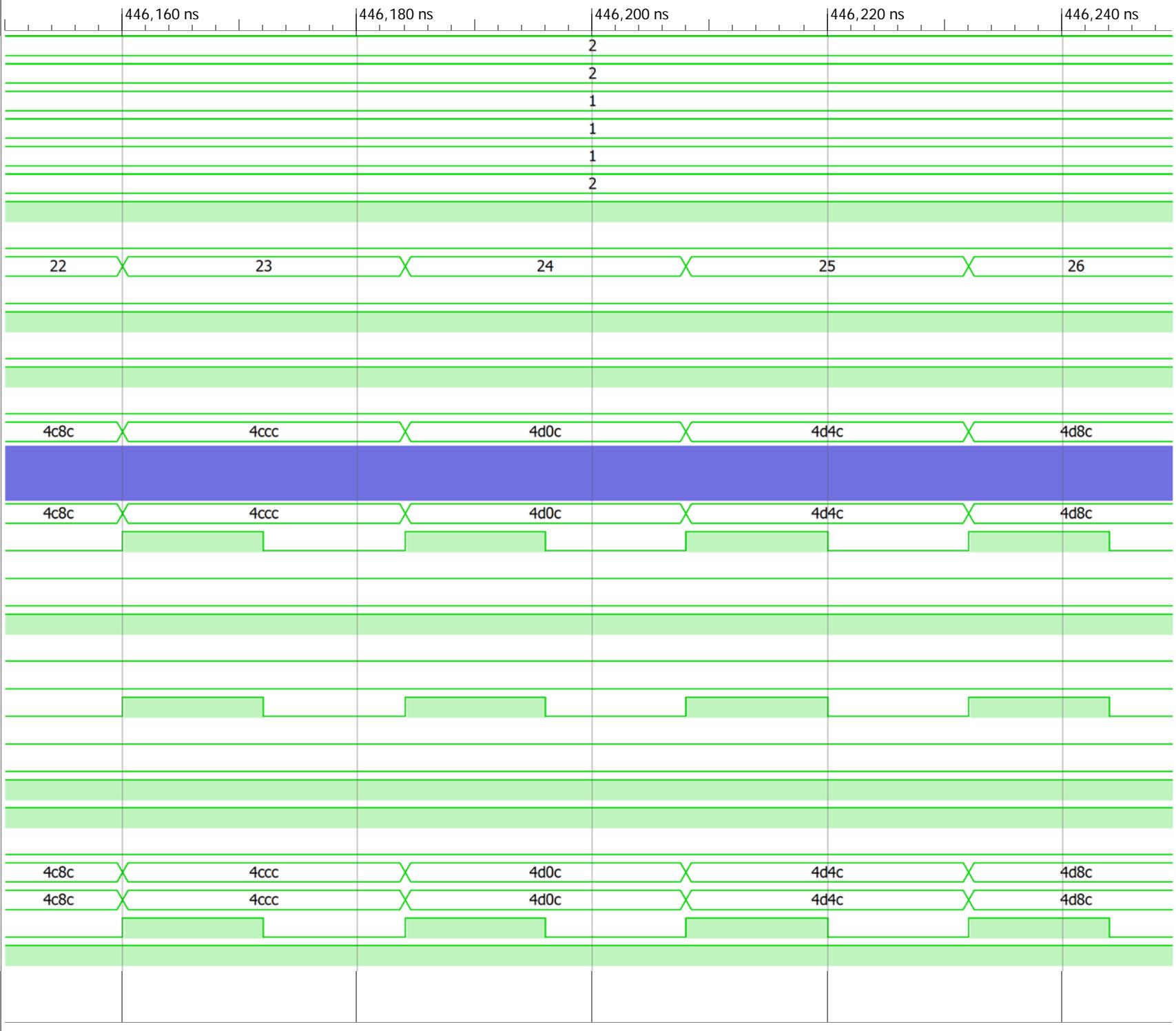


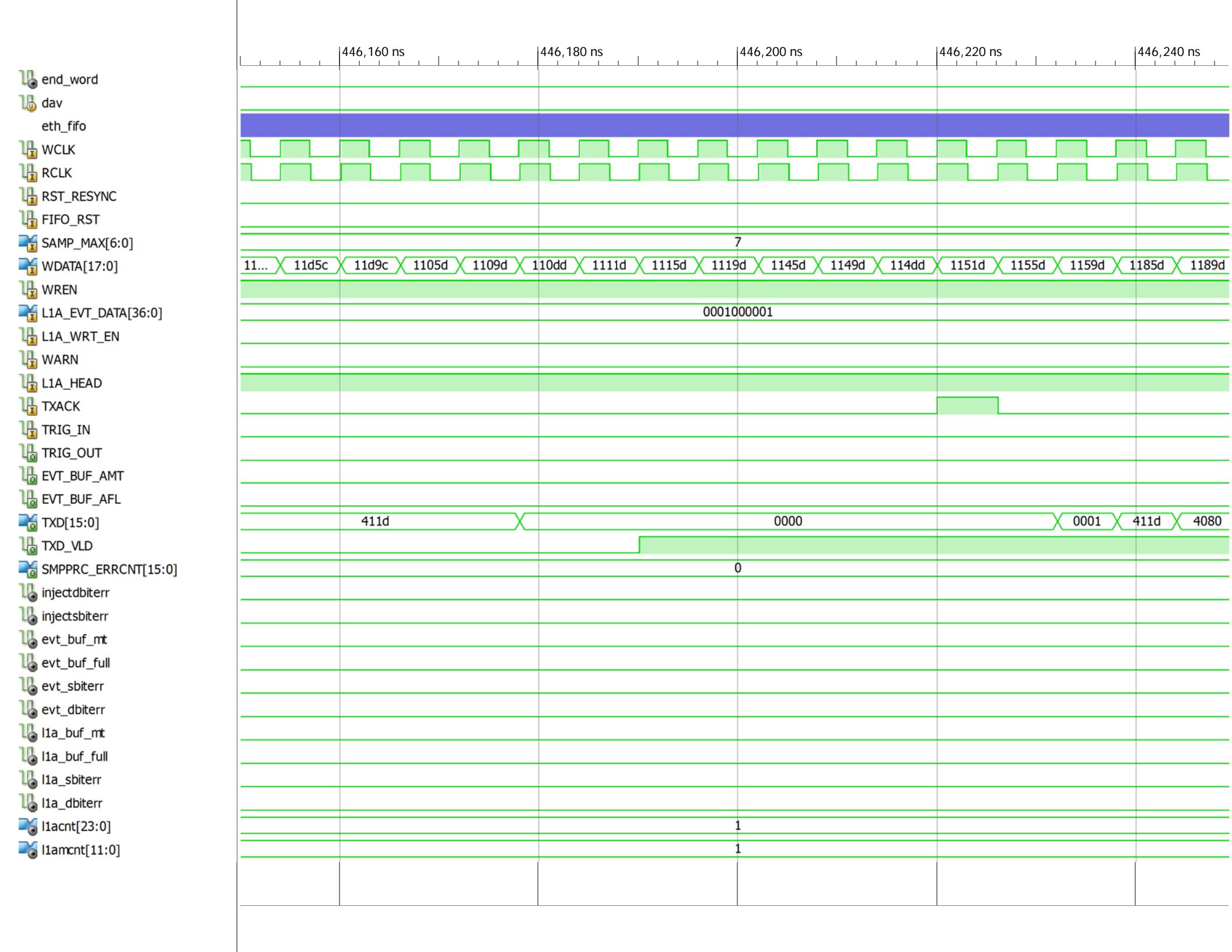


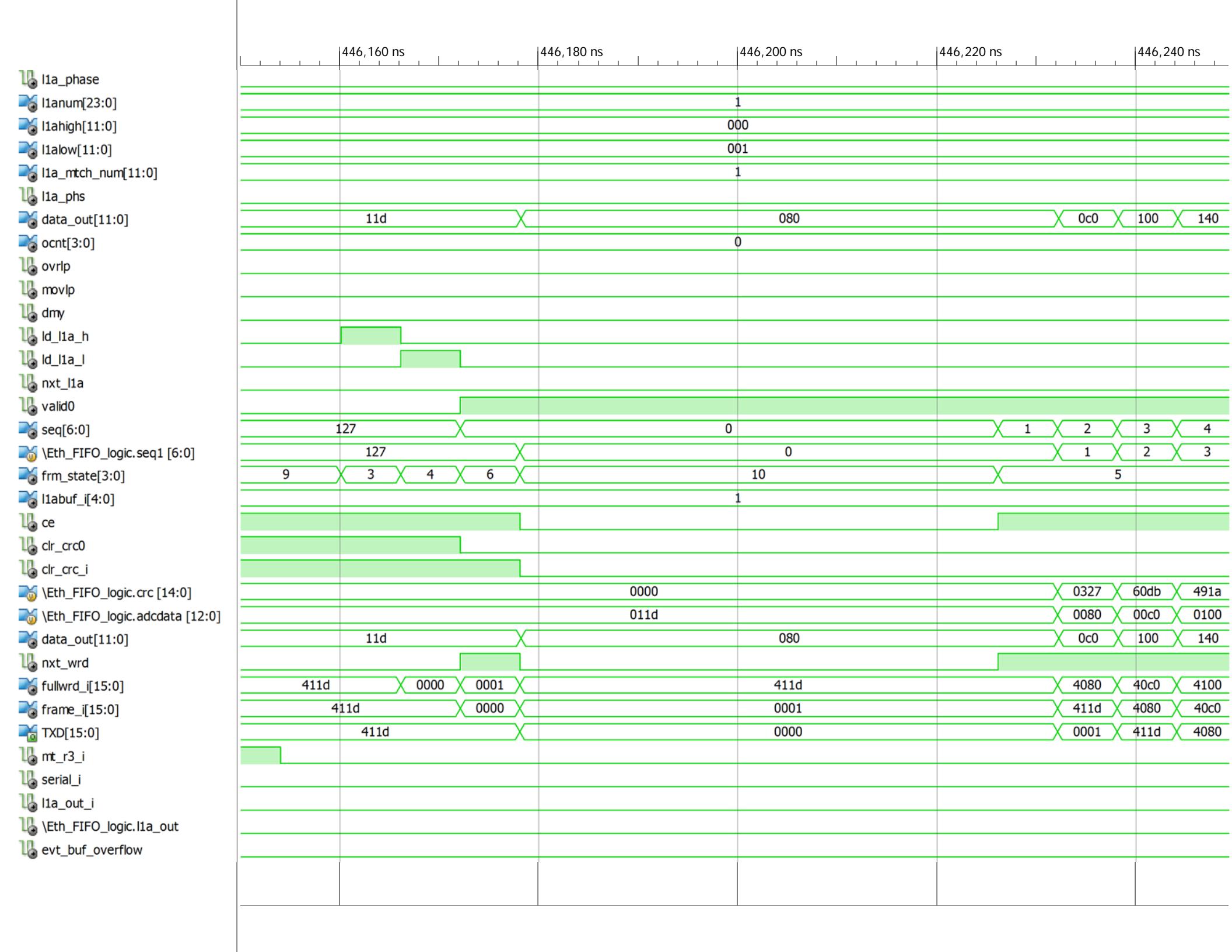
-  WREN
-  L1A\_EVT\_DATA[37:0]
-  OVRLP\_EVT\_DATA[6:0]
-  L1A\_WRT\_EN
-  WARN
-  TRIG\_IN
-  TRIG\_OUT
-  injectdbiterr
-  injectsbiterr
-  evt\_buf\_overflow
-  evt\_buf\_underflow
-  evt\_buf\_amt
-  evt\_buf\_afl
-  evt\_buf\_mt
-  evt\_buf\_full
-  evt\_sbterr
-  evt\_dbiterr
-  l1a\_buf\_mt
-  l1a\_buf\_full
-  l1a\_sbterr
-  l1a\_dbiterr
-  movlp
-  end\_evt
-  ocnt[3:0]
-  data\_out[11:0]
-  l1a\_phase
-  l1a\_match\_smp
-  l1a\_push
-  ovrlp\_smp
-  multi\_ovlp\_smp
-  evt\_end\_smp
-  l1a\_phs
-  nxt\_l1a
-  nxt\_wrd

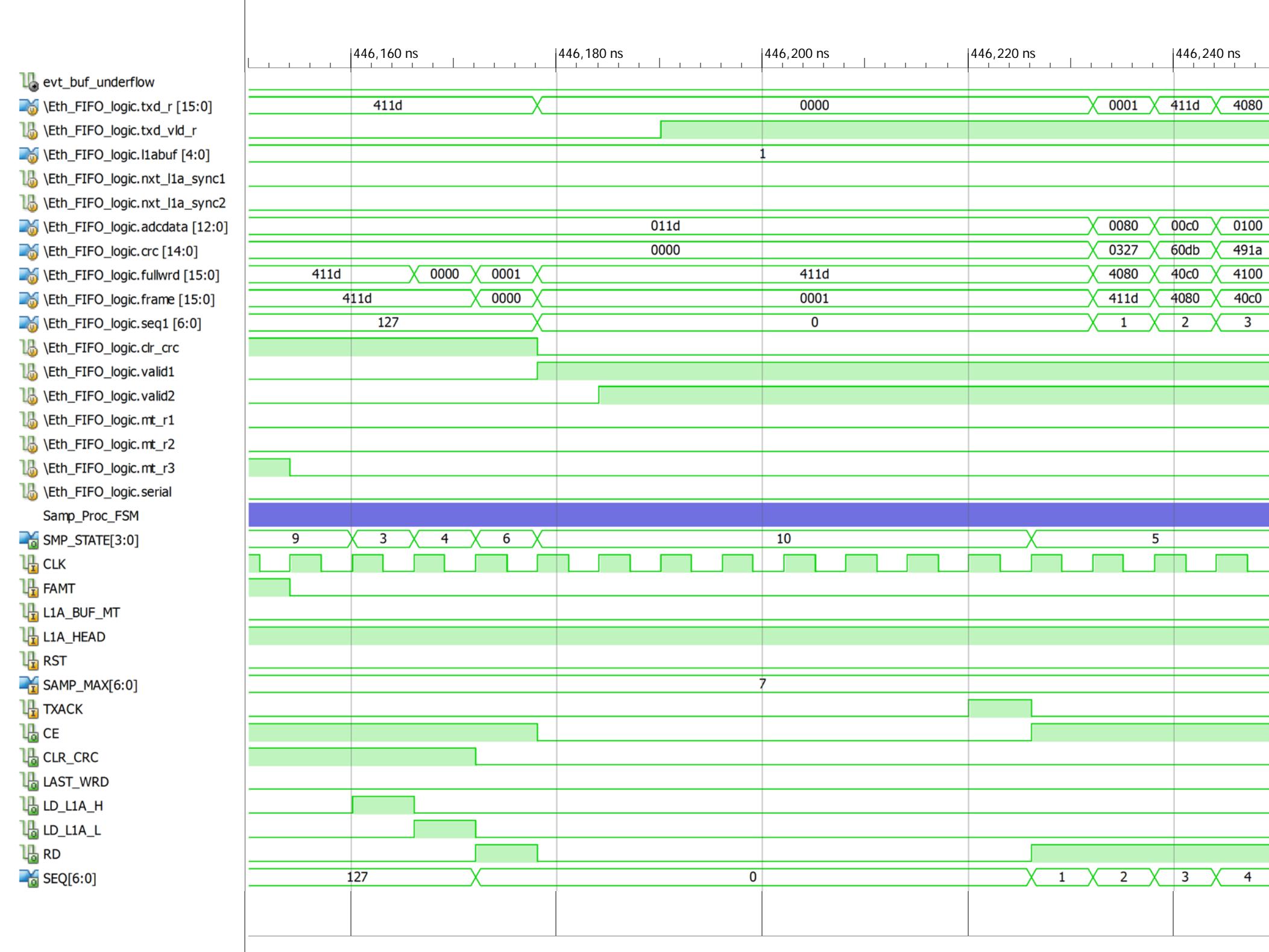


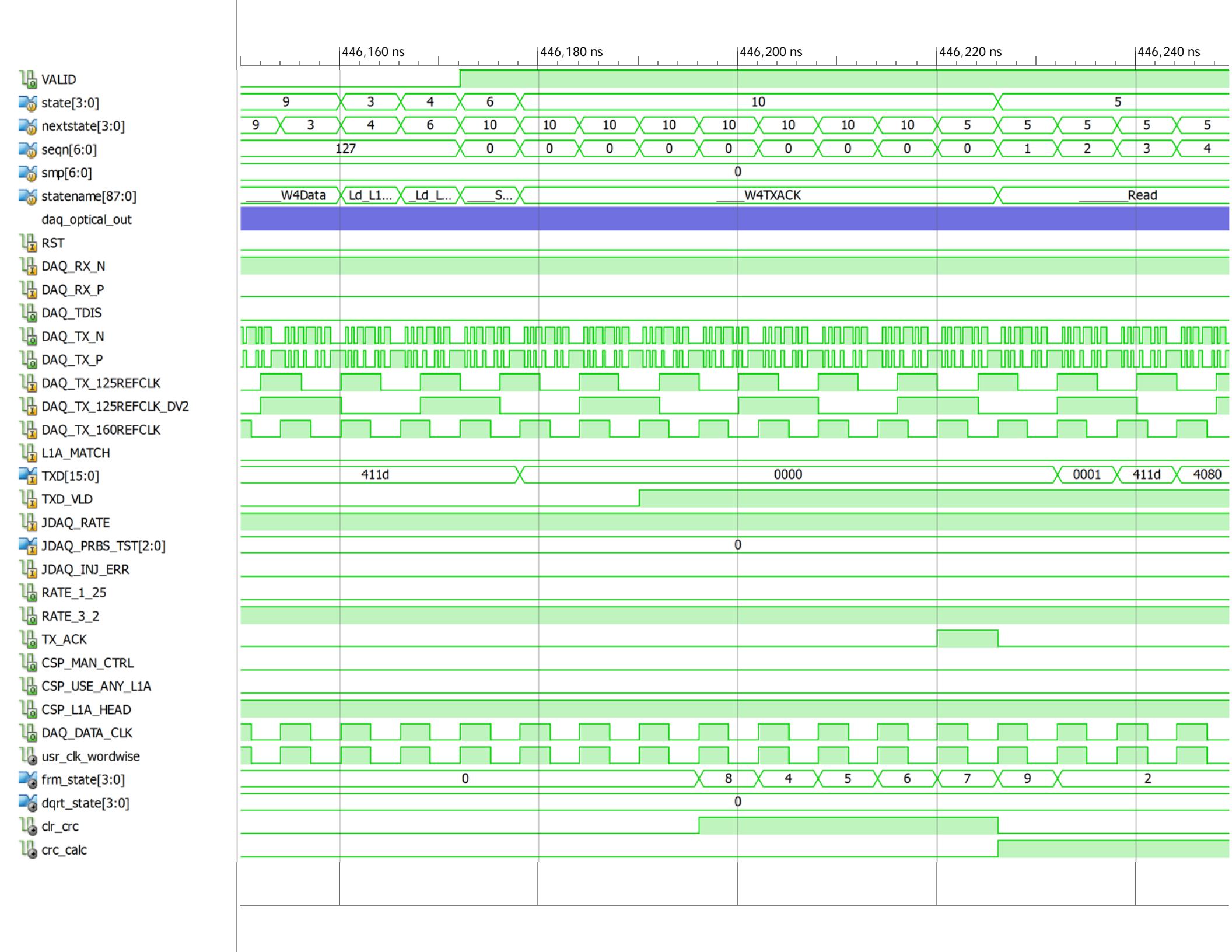
- l1acnt[23:0]
- l1amcnt[11:0]
- overlap\_cnt[3:0]
- l1anum[23:0]
- l1a\_mtch\_num[11:0]
- frm\_state[2:0]
- valid0
- clr\_crc0
- seq[6:0]
- ovrlp
- OVLP\_MUX
- LAST\_WRD
- DVALID
- MLT\_OVLP
- DOUT[15:0]
  - ChnLnk\_Frame\_FSM
  - ch\_link1
- DATAOUT[15:0]
- CHAN\_LNK\_CLK
- MB\_FIFO\_PUSH\_B
- MOVL
- OVLPMUX
- DATAAVAIL
- ENDWORD
- CLK
- L1A\_MATCH
- LAST\_WRD
- DVALID
- OVLP\_MUX
- MLT\_OVLP
- FRAME\_DATA[15:0]
- chan\_dat\_out[15:0]
- clnk\_clk
- mb\_fifo\_push

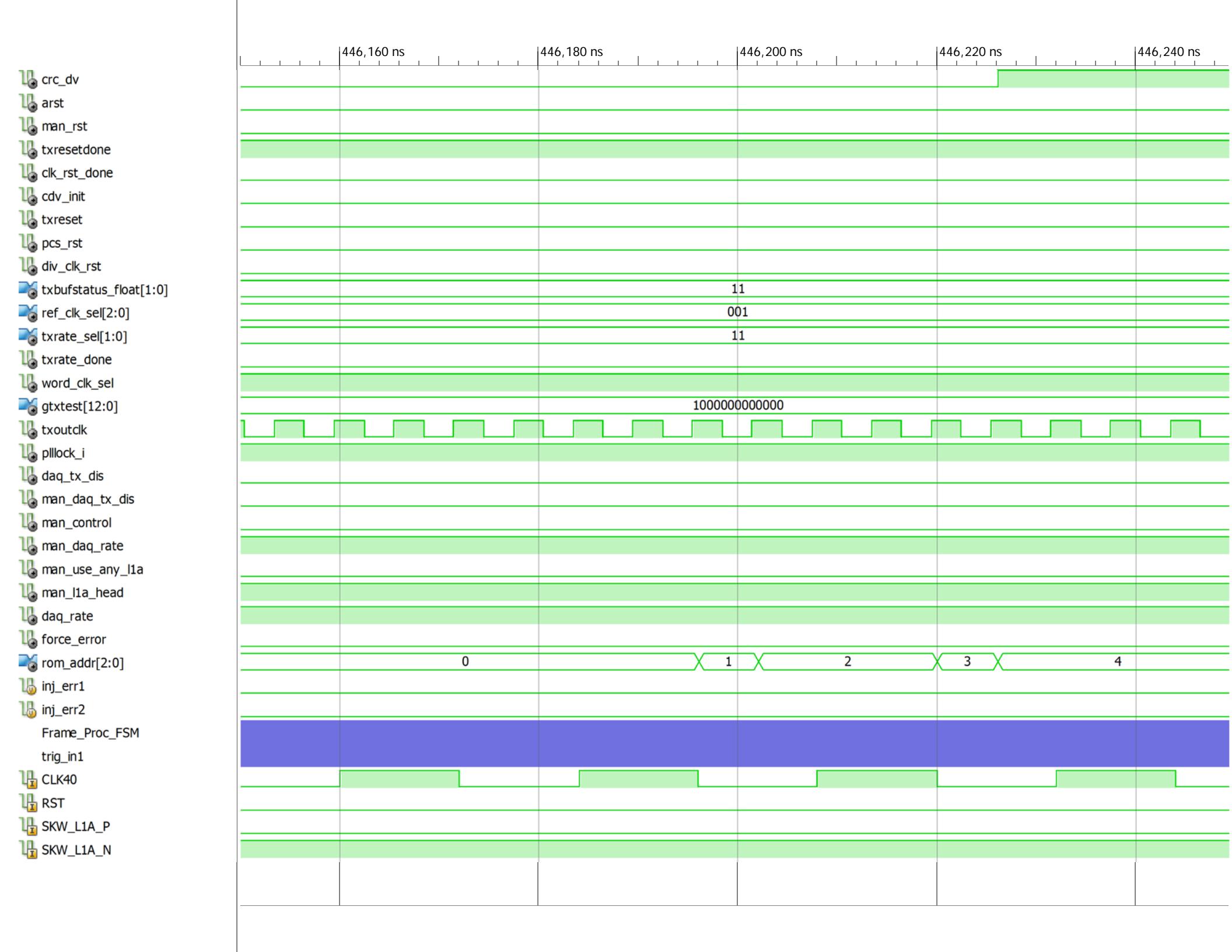


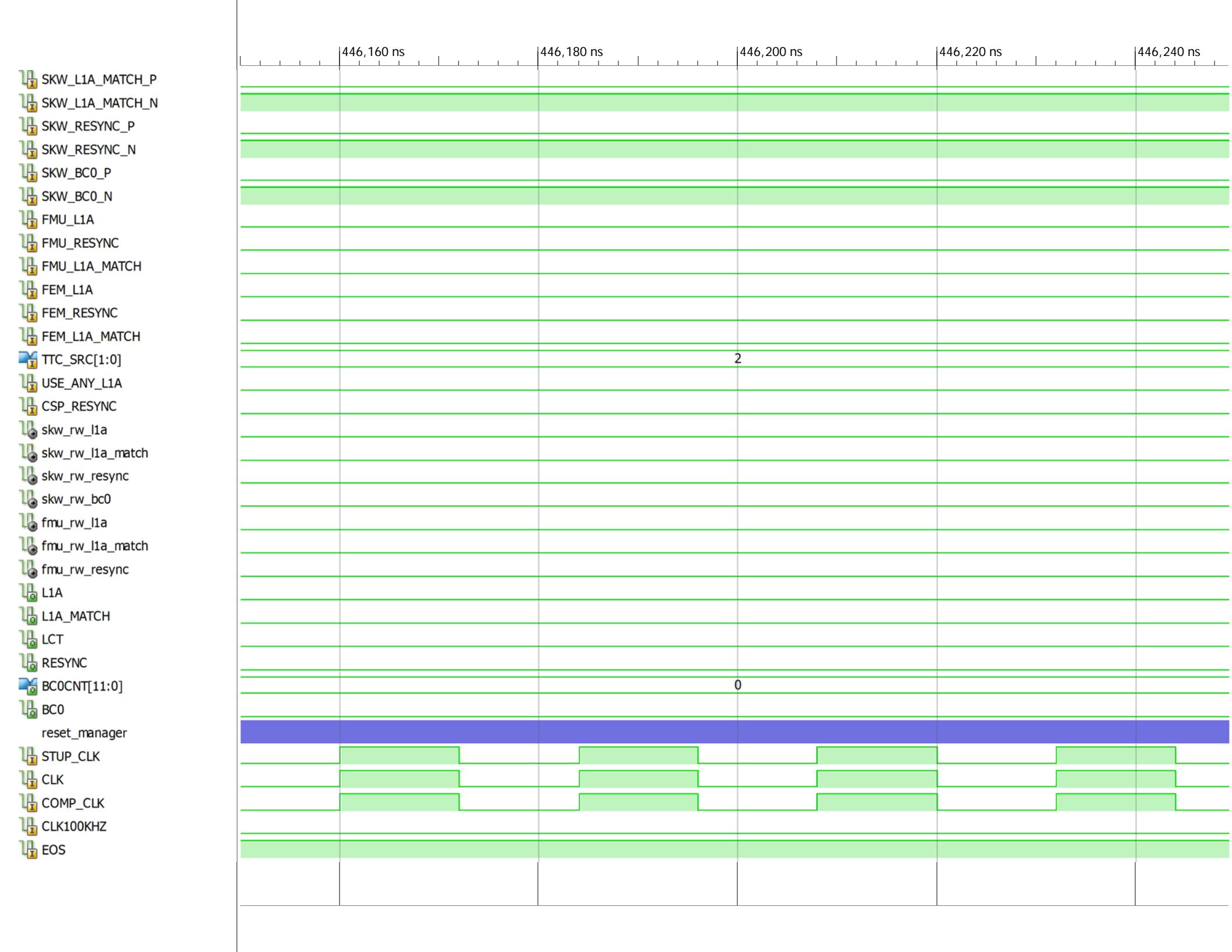












- JTAG\_SYS\_RST
- CSP\_SYS\_RST
- DAQ\_MMCM\_LOCK
- TRG\_MMCM\_LOCK
- CMP\_PHS\_CHANGE
- TRG\_SYNC\_DONE
- QP\_ERROR
- QP\_LOCKED
- AL\_DONE
- ADC\_INIT\_DONE
- BPI\_SEQ\_IDLE
- ADC\_INIT
- ADC\_RDY
- TRG\_GTXXRESET
- MMCM\_RST
- SYS\_MON\_RST
- ADC\_RST
- TRG\_RST
- DSR\_RST
- QPLL\_LOCK
- QPLL\_ERROR
- POR\_STATE[3:0]
- adc\_init\_rst\_i
- al\_start\_i
- por\_i
- run\_i
- restart\_all
- AL\_START
- SYS\_RST
- RUN
- ADC\_Init FSM

