

STATE MACHINE		
name	BPI_intrf_FSM	
clock	CLK	posedge
reset_signal	RST	posedge
reset_state	Standby	
default_state_is_x	1	
INPUTS		
CLK		
RST		
READ		
WRITE		
EXECUTE		
OUTPUTS		
BUSY	1	regdp
E	0	regdp
G	0	regdp
L	0	regdp
W	0	regdp
LOAD	0	regdp
CAP	0	regdp
STATES		
BUSY	1	output
E	0	output
G	0	output
L	0	output
W	0	output
LOAD	0	output
CAP	0	output
TRANSITIONS		
equation	1	def_type
priority	1000	

