STATE MACHINE

name	BB_adc_FSM	
clock	CLK	posedge
reset_signal	RST	posedge
reset_state	Idle	
default_state_is_x	1	

INPUTS

- CLK
- RST
- STROBE
- READBB
- ADCBUSY

OUTPUTS

BBCONV	0	regdp
DATAREADY	0	regdp
hold[2:0]	0	regdp
scnd		flag

STATES

BBCONV	0	output
DATAREADY	0	output
hold[2:0]	0	output
scnd		output

TRANSITIONS

equation	1	def_type
priority	1000