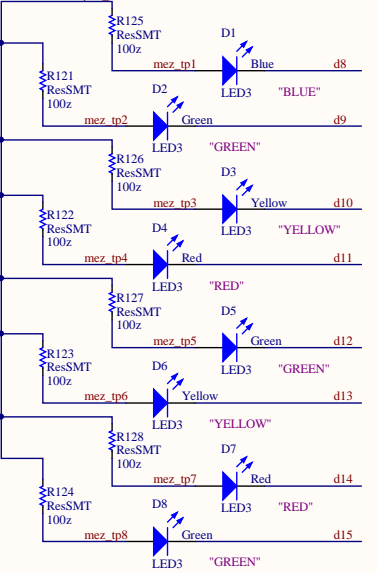
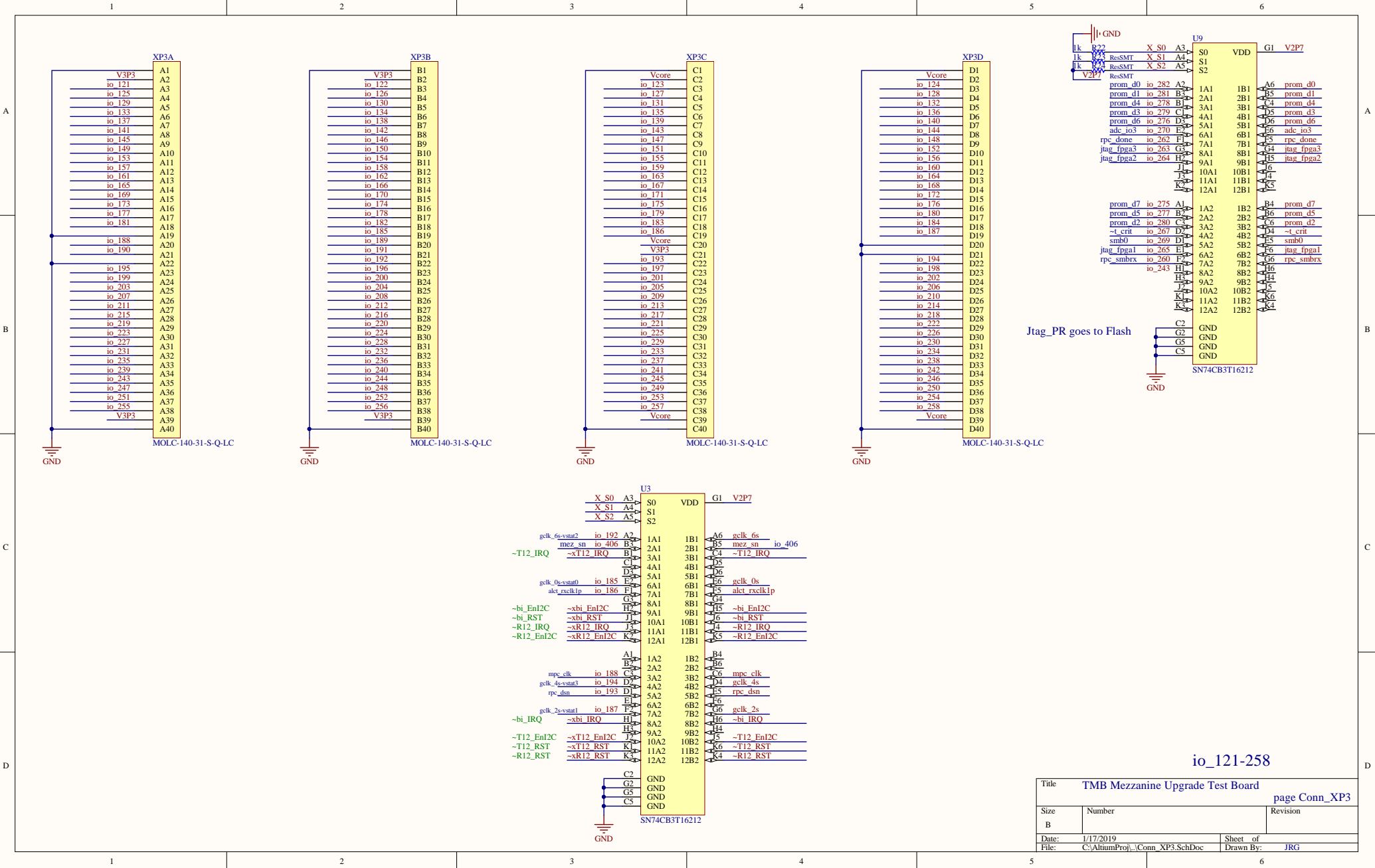


LED-Testpoints, they share the PROM data bus

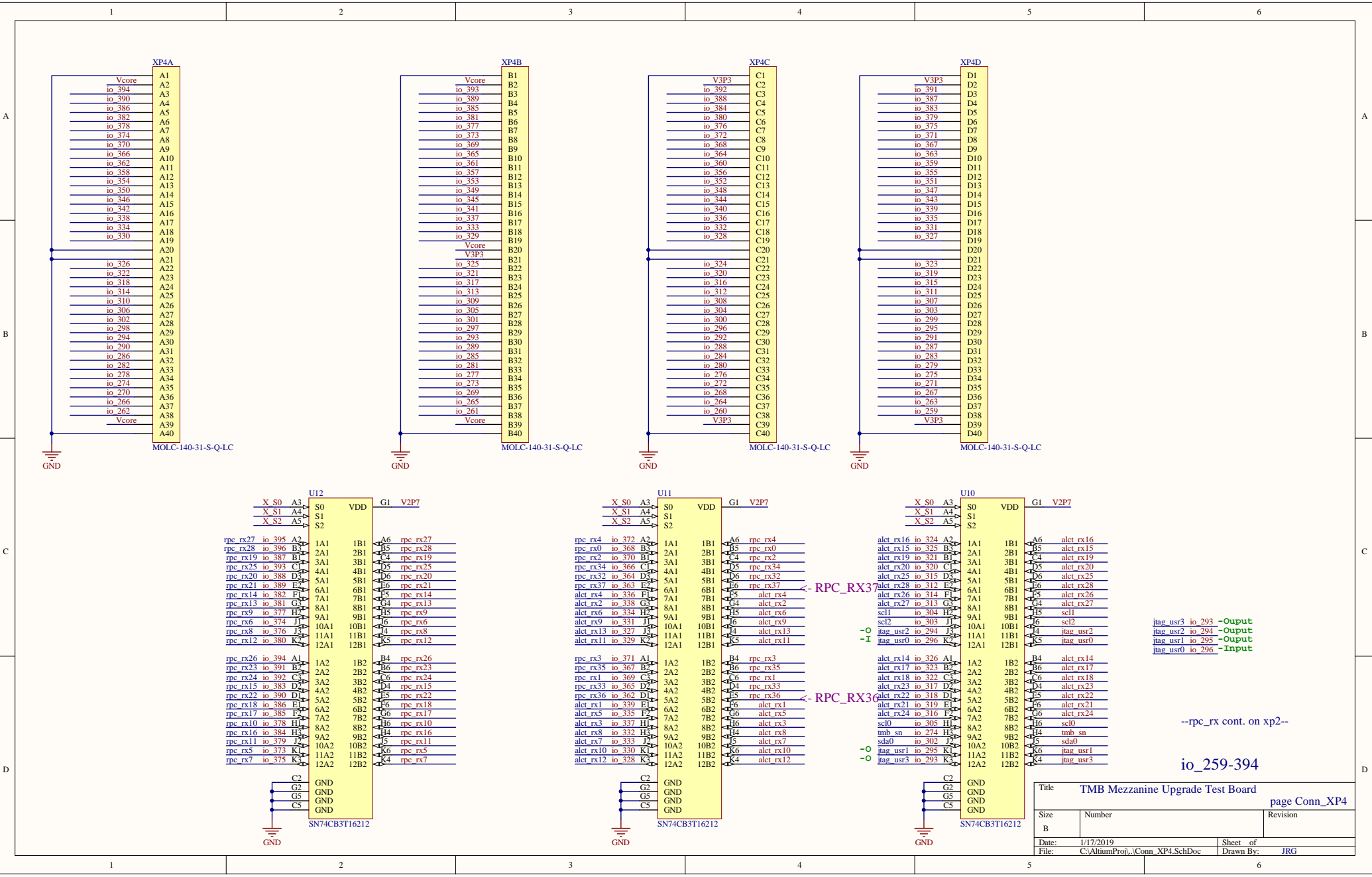


--ceb_rx cont. from xp1--
--rpc_rx cont. on xp4--
io_395-484, 501-516, 600-603, 700-708, 800-807

Title TMB Mezzanine Upgrade Test Board			page Conn_XP2
Size B	Number	Revision	
Date: 1/17/2019	File: C:\AltiumProj\Conn_XP2.SchDoc	Sheet of 1	Drawn By: JRG



Title			TMB Mezzanine Upgrade Test Board			page Conn_XP3		
Size		Number				Revision		
B								
Date:		1/17/2019				Sheet of		
File:		C:\AltiumProj\Conn_XP3.SchDoc				Drawn By:		JRG



--rpc_rx cont. on xp2--

io_259-394

Title				TMB Mezzanine Upgrade Test Board		page Conn_XP4	
Size		Number		Revision			
B							
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File:		C:\AltiumProj\Conn_XP4.SchDoc		Drawn By:		JRG	

U6A

BANK 12

IO_L0P_12 AD25 ~ccb rx11
IO_L0N_12 AD26 ~ccb rx12
IO_L1P_12 AE27 ~ccb rx10
IO_L1N_12 AD27 ~ccb rx5
IO_L2P_12 AH33 ~ccb rx48
IO_L2N_12 AH32 ~ccb rx50
IO_L2N_12 AE28 ~ccb tx17 io 39
IO_L3P_12 AE29 ~ccb tx0 io 2
IO_L3N_12 AJ34 ~ccb rx45
IO_L4P_12 AH34 ~ccb rx44
IO_L4N_VREF_12 AF28 ~ccb tx16 io 18
IO_L5P_12 AF29 ~ccb tx4 io 6
IO_L5N_12 AL34 ~ccb rx26
IO_L6P_12 AK34 ~ccb rx21
IO_L6N_12 AH29 ~ccb rx29
IO_L7P_12 AH30 ~ccb rx19
IO_L7N_12 AN33 ~ccb rx32
IO_L8P_SRCC_12 AN34 ~ccb rx31
IO_L8N_SRCC_12 AG27 ~ccb rx3
IO_L9P_MRCC_12 AG28 ~ccb rx0
IO_L9N_MRCC_12 AF30 ~ccb tx8 io 10
IO_L10P_MRCC_12 AG30 ~ccb tx5 io 7
IO_L10N_MRCC_12 AF26 ~ccb rx9
IO_L11P_SRCC_12 AE26 ~ccb rx13
IO_L11N_SRCC_12 AJ31 vrn 12
IO_L12P_VRN_12 AJ32 vrp 12
IO_L12N_VRP_12 AJ29 ~ccb rx28
IO_L13P_12 AJ30 ~ccb rx27
IO_L13N_12 AK33 ~ccb rx30
IO_L14P_12 AK32 ~ccb rx25
IO_L14N_VREF_12 AL31 ~ccb rx23
IO_L15P_12 AK31 ~ccb rx17
IO_L15N_12 AM33 ~ccb rx30
IO_L16P_12 AL33 ~ccb rx22
IO_L16N_12 AN32 ~ccb rx2
IO_L17P_12 AM32 ~ccb rx1
IO_L17N_12 AP32 ~ccb rx6
IO_L18P_12 AP33 ~ccb rx33
IO_L18N_12 AL30 ~ccb rx24
IO_L19P_12 AM31 ~ccb rx7
IO_L19N_12

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U6B

BANK 13

IO_L0P_13 AA34 dmb tx24 io 456
IO_L0N_13 AA33 dmb tx25 io 455
IO_L1P_13 AA30 ~ccb rx40
IO_L1N_13 AB31 ~ccb rx46
IO_L2P_13 AD34 dmb tx12 io 468
IO_L2N_13 AC34 dmb tx21 io 459
IO_L2N_13 AB30 ~ccb tx21 io 496
IO_L3P_13 AB31 ~ccb tx18 io 493
IO_L3N_13 AC33 dmb tx16 io 464
IO_L4P_13 AB33 dmb tx20 io 460
IO_L4N_VREF_13 AE31 ~ccb tx2 io 4
IO_L5P_13 AD31 ~ccb tx25 io 500
IO_L5N_13 AA25 ~ccb rx49
IO_L6P_13 A26 ~ccb rx42
IO_L6N_13 AA28 ~ccb rx36
IO_L7P_13 AA29 ~ccb rx38
IO_L7N_13 AE34 dmb tx13 io 467
IO_L8P_SRCC_13 AF34 dmb tx4 io 476
IO_L8N_SRCC_13 AD30 ~ccb tx24 io 499
IO_L9P_MRCC_13 AC30 ~ccb tx10 io 12
IO_L9N_MRCC_13 AB33 dmb tx8 io 472
IO_L10P_MRCC_13 AF33 dmb tx5 io 475
IO_L10N_MRCC_13 AD29 ~ccb tx12 io 14
IO_L11P_SRCC_13 AC29 ~ccb tx9 io 11
IO_L11N_SRCC_13 AB32 vrn 13
IO_L12P_VRN_13 AC32 vrp 13
IO_L12N_VRP_13 AB28 ~ccb rx18
IO_L13P_13 AC28 ~ccb tx23 io 498
IO_L13N_13 AD32 dmb tx17 io 469
IO_L14P_13 AB32 dmb tx9 io 471
IO_L14N_VREF_13 AB27 ~ccb rx35
IO_L15P_13 AC27 ~ccb tx13 io 15
IO_L15N_13 AG33 dmb tx3 io 512
IO_L16P_13 AG32 ~ccb rx39
IO_L16N_13 AA26 ~ccb rx34
IO_L17P_13 AB26 ~ccb tx19 io 494
IO_L17N_13 AG31 ~ccb rx43
IO_L18P_13 AF31 ~ccb tx1 io 3
IO_L18N_13 AB25 ~ccb tx20 io 495
IO_L19P_13 AC25 ~ccb rx4
IO_L19N_13

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U6C

BANK 14

IO_L0P_14 U25 SET_SW7
IO_L0N_14 U25 TestLED9
IO_L1P_14 U28 TestLED8
IO_L1N_14 U29 TestLED1
IO_L2P_14 R33 dmb tx48 io 426
IO_L2N_14 R34 dmb tx45 io 429
IO_L2N_14 U30 TestLED6
IO_L3P_14 U31 dmb tx43 io 437
IO_L3N_14 R33 dmb tx44 io 430
IO_L4P_14 U34 dmb rx3
IO_L4N_VREF_14 U26 SET_SW8
IO_L5P_14 U27 TestLED10
IO_L5N_14 U33 dmb rx0
IO_L6P_14 U32 dmb rx2
IO_L6N_14 U28 TestLED7
IO_L7P_14 U29 RESET
IO_L7N_14 U31 dmb rx5
IO_L8P_SRCC_14 U30 Rst QPLL
IO_L8N_SRCC_14 U30 TP INIT gp io5
IO_L9P_MRCC_14 U30 TP WR gp io6
IO_L9N_MRCC_14 U34 dmb rx4
IO_L10P_MRCC_14 U34 dmb tx33 io 448
IO_L10N_MRCC_14 U28 rpe dsn
IO_L11P_SRCC_14 U27 T12 Selk
IO_L11N_SRCC_14 U32 vrn 14
IO_L12P_VRN_14 U33 vrp 14
IO_L12N_VRP_14 U32 dmb tx29 io 451
IO_L13P_14 U31 dmb tx26 io 513
IO_L13N_14 U32 dmb tx28 io 452
IO_L14P_14 U34 dmb tx33 io 447
IO_L14N_VREF_14 U29 TP CS gp io7
IO_L15P_14 U29 ~ccb rx41
IO_L15N_14 U31 dmb rx1
IO_L16P_14 U32 dmb tx40 io 440 < was DMB_TX39
IO_L16N_14 U28 ~ccb rx47
IO_L17P_14 U27 ~ccb rx37
IO_L17N_14 U25 me2 done io 403
IO_L18P_14 U25 vss1 gclx 2s
IO_L18N_14 U27 ~R12 IRQ < from translator < was F_Selk
IO_L19P_14 U26 R12 Selk
IO_L19N_14

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U6D

BANK 15

IO_L0P_15 M31 io 406 me2 sn < from translator
IO_L0N_15 L31 ~mpc tx24 io 509
IO_L1P_15 U25 ~mpc tx1 io 411
IO_L1N_15 U25 ~mpc tx29 io 407
IO_L2P_15 U32 ~mpc tx18 io 503
IO_L2N_15 U26 ~mpc tx5 io 415
IO_L3P_15 U27 ~mpc tx2 io 412
IO_L3N_15 U31 ~mpc tx7 io 417
IO_L4P_15 U30 ~mpc tx15 io 425
IO_L4N_VREF_15 U27 ~mpc tx26 io 511
IO_L5P_15 U27 ~mpc tx6 io 416
IO_L5N_15 U33 ~mpc tx21 io 506
IO_L6P_15 U32 ~mpc tx23 io 508
IO_L6N_15 U28 ~mpc tx20 io 505
IO_L7P_15 U28 ~mpc tx30 io 408
IO_L7N_15 U32 ~mpc tx0 io 410
IO_L8P_SRCC_15 U32 ~T12 En12C < from translator
IO_L8N_SRCC_15 U28 ~mpc tx10 io 420
IO_L9P_MRCC_15 U29 ~mpc tx28 io 405
IO_L9N_MRCC_15 U33 ~mpc tx8 io 418
IO_L10P_MRCC_15 U33 ~mpc tx25 io 510
IO_L10N_MRCC_15 U29 ~mpc tx27 io 404
IO_L11P_SRCC_15 U30 ~mpc tx22 io 507
IO_L11N_SRCC_15 U25 ~mpc tx13 io 423
IO_L12P_VRN_15 U26 ~mpc tx9 io 419
IO_L12N_VRN_15 U28 ~mpc tx11 io 421
IO_L13P_15 U27 TestLED5
IO_L13N_15 U31 TestLED2
IO_L14P_15 U32 ~R12 RST < from translator < was free IO pin
IO_L14N_VREF_15 U26 ~mpc tx14 io 424
IO_L15P_15 U26 TestLED3
IO_L15N_15 U34 vrn 15
IO_L16P_15 U34 vrp 15
IO_L16N_15 U30 ~R12 En12C < from translator
IO_L17P_15 U30 ~mpc tx31 io 409
IO_L17N_15 U34 ~mpc tx4 io 414
IO_L18P_15 U34 ~mpc tx12 io 422
IO_L18N_15 U29 ~mpc tx3 io 413
IO_L19P_15 U29 TestLED4
IO_L19N_15

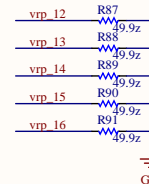
XC6VLX195T-1FF1156C

U6E

BANK 16

IO_L0P_16 C32 rpe rx17
IO_L0N_16 B32 rpe rx6
IO_L1P_16 U26 alet tx14 io 352
IO_L1N_16 U27 alet tx8 io 358
IO_L2P_16 U32 rpe rx25
IO_L2N_16 U33 rpe rx20
IO_L3P_16 U30 rpe rx7
IO_L3N_16 U30 rpe rx16
IO_L4P_16 U33 rpe rx10
IO_L4N_VREF_16 U33 rpe rx11
IO_L5P_16 U31 rpe rx15
IO_L5N_16 U30 rpe rx5
IO_L6P_16 U33 rpe rx13
IO_L6N_16 U34 rpe rx9
IO_L7P_16 U28 alet tx5 io 361
IO_L7N_16 U29 alet tx6 io 360
IO_L8P_SRCC_16 U34 rpe rx22
IO_L8N_SRCC_16 U34 rpe rx18
IO_L9P_MRCC_16 U26 alet tx19 io 349
IO_L9N_MRCC_16 U27 alet tx16 io 350
IO_L10P_MRCC_16 U33 rpe rx23
IO_L10N_MRCC_16 U31 rpe rx19
IO_L11P_SRCC_16 U31 rpe rx12
IO_L11N_SRCC_16 U34 vrn 16
IO_L12P_VRN_16 U34 vrp 16
IO_L12N_VRP_16 U30 rpe rx8
IO_L13P_16 U29 alet tx7 io 359
IO_L13N_16 U34 rpe rx28
IO_L14P_16 U31 rpe rx14
IO_L14N_VREF_16 U32 rpe rx21
IO_L15P_16 U33 ~mpc tx19 io 504
IO_L15N_16 U34 ~mpc tx17 io 502
IO_L16P_16 U32 rpe rx26
IO_L16N_16 U25 rpe rx0
IO_L17P_16 U26 alet tx12 io 354
IO_L18P_16 U31 rpe tx1 io 401
IO_L18N_16 U32 rpe tx0 io 402
IO_L19P_16
IO_L19N_16

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Title TMB Mezzanine Upgrade Test Board		
Number		Revision page FPGA_1
Date: 1/17/2019	Sheet of	
File: C:\AltiumProj\1\FPGA_1.SchDoc	Drawn By: JRG	

U6F
BANK 22
IO_L10P_22 AE21 vme_reply1 io 111
IO_L0N_22 AD21 ~vme cmd1
IO_L11P_22 AM18 ~vme cmd4
IO_L1N_22 AL18 ~vme geo4
IO_L2P_22 AG22 ~vme geo2
IO_L2N_22 AH22 ~vme cmd0
IO_L3P_22 AP19 vme a17
IO_L3N_22 AN18 vme a13
IO_L4P_22 AK22 vme a18
IO_L4N_VREF_22 AJ22 ~vme cmd2
IO_L5P_22 AN19 vme a14
IO_L5N_22 AN20 vme a9
IO_L6P_22 AC20 alct_hand oe io 124
IO_L6N_22 AD20 vme_reply4 io 114
IO_L7P_22 AM20 ~vme cmd3
IO_L7N_22 AL20 vme_reply3 io 113
IO_L8P_SRCC_22 AF19 vme_reply2 io 112
IO_L8N_SRCC_22 AE19 vme_reply6 io 116
IO_L9P_MRCC_22 AP20 vme a15
IO_L9N_MRCC_22 AK19 ~vme geo5
IO_L10P_MRCC_22 AL19 ~vme geo3
IO_L10N_MRCC_22 AF20 vme_reply0 io 110
IO_L11P_SRCC_22 AP21 ~vme cmd7
IO_L11N_SRCC_22 AJ20 vrn 22
IO_L12P_VRN_22 AH20 vrp 22
IO_L12N_VRP_22 AM21 vme a20
IO_L13P_22 AL21 ~vme cmd5
IO_L13N_22 AC19 ~mb_hand oe io 125
IO_L14P_22 AD19 vme_reply5 io 115
IO_L14N_VREF_22 AM23 vme a2
IO_L15P_22 AL23 vme a23
IO_L16P_22 AK21 ~vme geo0
IO_L16N_22 AJ21 vme a19
IO_L17P_22 AM22 ~vme cmd9
IO_L17N_22 AN22 vme a16
IO_L18P_22 AG20 ~vme geo6
IO_L18N_22 AG21 ~vme geo1
IO_L19P_22 AP22 vme a7
IO_L19N_22 AN23 vme a11

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U6I
BANK 25
IO_L0P_25 D25 rpc tx3 io 340
IO_L0N_25 D26 alct_clock_enio 348
IO_L11P_25 C24 alct rx14
IO_L1N_25 C25 alct rx15
IO_L2P_25 E26 alct tx23 io 343
IO_L2N_25 E26 alct loop io 342
IO_L3P_25 B25 alct rx17
IO_L3N_25 A25 alct rx16
IO_L4P_25 D27 alct tx06 io 347
IO_L4N_VREF_25 B26 alct rx3
IO_L5P_25 A26 alct rx6
IO_L6P_25 G26 alct tx21 io 345
IO_L6N_25 G27 alct tx22 io 344
IO_L7P_25 E27 alct rx5
IO_L7N_25 D24 alct rx7
IO_L8P_SRCC_25 E24 alct rx8
IO_L8N_SRCC_25 C28 rpc rx37 <- pre-2013 was wrong, RPC_RX36
IO_L9P_MRCC_25 B28 alct rx4
IO_L10P_MRCC_25 C29 rpc rx32
IO_L10N_MRCC_25 D29 alct tx11 io 355
IO_L11P_SRCC_25 G25 alct rx06 io 341
IO_L11N_SRCC_25 G25 rpc rx4
IO_L12P_VRN_25 H27 alct tx9 io 357
IO_L12N_VRP_25 G28 alct tx13 io 353
IO_L13P_25 A28 alct rx1
IO_L13N_25 A29 rpc rx36 <- pre-2013 was wrong, RPC_RX37
IO_L14P_25 F28 alct tx10 io 356
IO_L14N_VREF_25 G28 alct tx15 io 351
IO_L15P_25 B30 rpc rx1
IO_L15N_25 B30 rpc rx3
IO_L16P_VRN_25 E29 vrn 25
IO_L16N_VRP_25 E29 vrp 25
IO_L17P_25 C30 rpc rx35
IO_L17N_25 D30 rpc rx34
IO_L18P_GC_25 H28 GCLK3-2P_DCC:clk
IO_L18N_GC_25 H29 R12_Sdat
IO_L19P_GC_25 B31 io 197 ~mb_clk05p <- "stoppable" LHC_Clk
IO_L19N_GC_25 A31 T12_Sdat

XC6VLX195T-1FF1156C

final clock connections

U6G
BANK 23
IO_L10P_23 AH27 vme d15
IO_L0N_23 AH28 vme d3
IO_L11P_23 AN30 vme d0
IO_L1N_23 AM30 ~ccb rx14
IO_L2P_23 AG25 vme a10
IO_L2N_23 AG26 vme a00
IO_L3P_23 AP30 ~ccb rx8
IO_L3N_23 AP31 vme d4
IO_L4P_23 AL29 vme d11
IO_L4N_VREF_23 AK29 vme d9
IO_L5P_23 AN29 vme d5
IO_L5N_23 AP29 ~ccb rx15
IO_L6P_23 AL28 vme d7
IO_L6N_23 AK28 vme d13
IO_L7P_23 AN28 vme d10
IO_L7N_23 AM28 ~ccb rx16
IO_L8P_SRCC_23 AH25 vme a5
IO_L8N_SRCC_23 AJ25 vme d2
IO_L9P_MRCC_23 AN27 vme am2
IO_L9N_MRCC_23 AM27 vme d8
IO_L10P_MRCC_23 AK27 vme d12
IO_L10N_MRCC_23 AJ27 vme d1
IO_L11P_SRCC_23 AH23 ~vme cmd6
IO_L11N_SRCC_23 AH24 vme a21
IO_L12P_VRN_23 AK26 vrn 23
IO_L12N_VRP_23 AJ26 vrp 23
IO_L13P_23 AL26 vme am1
IO_L13N_23 AL26 vme d14
IO_L14P_23 AJ24 vme a22
IO_L14N_VREF_23 AK24 ~vme cmd10
IO_L15P_23 AP27 vme d6
IO_L15N_23 AM25 vme a8
IO_L16P_23 AM25 vme am5
IO_L16N_23 AL25 vme am3
IO_L17P_23 AN24 vme a12
IO_L17N_23 AK23 ~vme cmd8
IO_L18P_23 AL24 vme am4
IO_L18N_23 AP25 vme a4
IO_L19P_23 AP24 vme a6
IO_L19N_23

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final clock connections

U6H
BANK 24
IO_L10P_GC_24 L23 GCLK2-0P_ALCTrx:clk
IO_L0N_GC_24 M22 OPLL_Lock
IO_L11P_GC_24 C24 GCLK1-6P_tmb_clk1
IO_L1N_GC_24 C23 ~T12_IRQ <- from translator <- was free IO pin
IO_L2P_D15_24 D23 D15
IO_L2N_D14_24 D24 D14
IO_L3P_D13_24 D23 D13
IO_L3N_D12_24 D23 D12
IO_L4P_D11_24 D23 D11
IO_L4N_VREF_D10_24 D24 D10
IO_L5P_D9_24 D23 D9
IO_L5N_D8_24 D23 D8
IO_L6P_D7_24 D24 D7
IO_L6N_D6_24 D25 D5
IO_L7P_D5_24 D24 D4
IO_L7N_D4_24 D24 D4
IO_L8P_SRCC_24 D24 rpc rx30
IO_L8N_SRCC_24 D23 rpc rx29
IO_L9P_MRCC_24 D25 rpc rx33
IO_L9N_MRCC_24 D24 rpc rx2
IO_L10P_MRCC_24 D23 rpc rx31
IO_L10N_MRCC_24 D23 alct rxclk1p
IO_L11P_SRCC_24 D24 ~ccb tx6 io 8
IO_L11N_SRCC_24 AE24 ~ccb tx22 io 497
IO_L12P_D3_24 D24 D3
IO_L12N_D2_FS2_24 D24 D2
IO_L13P_D1_FS1_24 AF25 D1
IO_L13N_D0_FS0_24 D24 D0
IO_L14P_FCS_B_24 D24 FCS <- Firmware must drive HIGH!
IO_L14N_VREF_FOE_B_MOSI_24 A24 FOE io 16 ~ccb tx14
IO_L15P_FWE_B_24 AF23 FWE io 1 ~ccb tx26
IO_L15N_RS1_24 AG23 dmb tx11 a23io 469 <- Output!
IO_L16P_RS0_24 AG23 dmb tx19 a2io 461 <- Output!
IO_L16N_CS0_B_24 AB23 ~ccb tx11 io 13 <- Output!
IO_L17P_VRN_24 AE23 vrn 24
IO_L17N_VRP_24 AE22 vrp 24
IO_L18P_24 C23 ~ccb tx3 io 5 <- L Output to Prom
IO_L18N_24 C24 ~T12_RST <- from translator
IO_L19P_24 AC22 ~ccb tx7 io 9
IO_L19N_24 AD22 ~ccb tx15 io 17

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FOEFWE not connected, can use as a placeholder for UCLA code I/O

U6J
BANK 26
IO_L0P_26 C20 alct rx21
IO_L0N_26 D20 jtag usr3
IO_L11P_26 A23 alct rx20
IO_L1N_26 C21 alct rx18
IO_L2P_26 D21 io 309 gtl loop
IO_L2N_26 D22 alct rx23
IO_L3P_26 B23 alct rx25
IO_L3N_26 B23 alct rx11
IO_L4P_26 D20 step1 io 300
IO_L4N_VREF_26 D21 rpc loop io 306
IO_L5P_26 B21 alct rx26
IO_L5N_26 B22 alct rx22
IO_L6P_26 B22 alct rx9
IO_L6N_26 E23 alct rx10
IO_L7P_26 A20 alct rx27
IO_L7N_26 A21 alct rx28
IO_L8P_SRCC_26 F19 jtag usr2
IO_L8N_SRCC_26 F20 tmb sn
IO_L9P_MRCC_26 B20 alct rx24
IO_L9N_MRCC_26 F19 scl1
IO_L10P_MRCC_26 D20 io 311 OPLL_Err
IO_L10N_MRCC_26 D20 io 310 ~ccb status oe
IO_L11P_SRCC_26 H19 sel usr3 io 289
IO_L11N_SRCC_26 H20 step0 io 301
IO_L12P_VRN_26 D21 vrn 26
IO_L12N_VRP_26 D21 vrp 26
IO_L13P_26 H19 sel usr0 io 292
IO_L13N_26 H19 step2 io 299
IO_L14P_26 H22 alct rx19
IO_L14N_VREF_26 D22 io 308 dmb loop
IO_L15P_26 A18 scl2
IO_L15N_26 A19 scl0
IO_L16N_26 C21 sda0
IO_L16N_26 D22 ~dmb oe io 307
IO_L17P_26 F18 step4 io 297
IO_L17N_26 D20 step3 io 298
IO_L18P_26 D21 jtag usr0
IO_L18N_26 C22 alct rx13
IO_L19P_26 D22 alct rx12
IO_L19N_26

~gtl_oe
~gtl_oe Low, for SEU safety
~gtl_oe Low, for SEU safety
keeps COE High, for SEU safety
Open at CERN, keeps Loop Low, for SEU safety
red R/A slider: spdt-co
Only close Loop for Prod.Tests w/special TMB (short it)

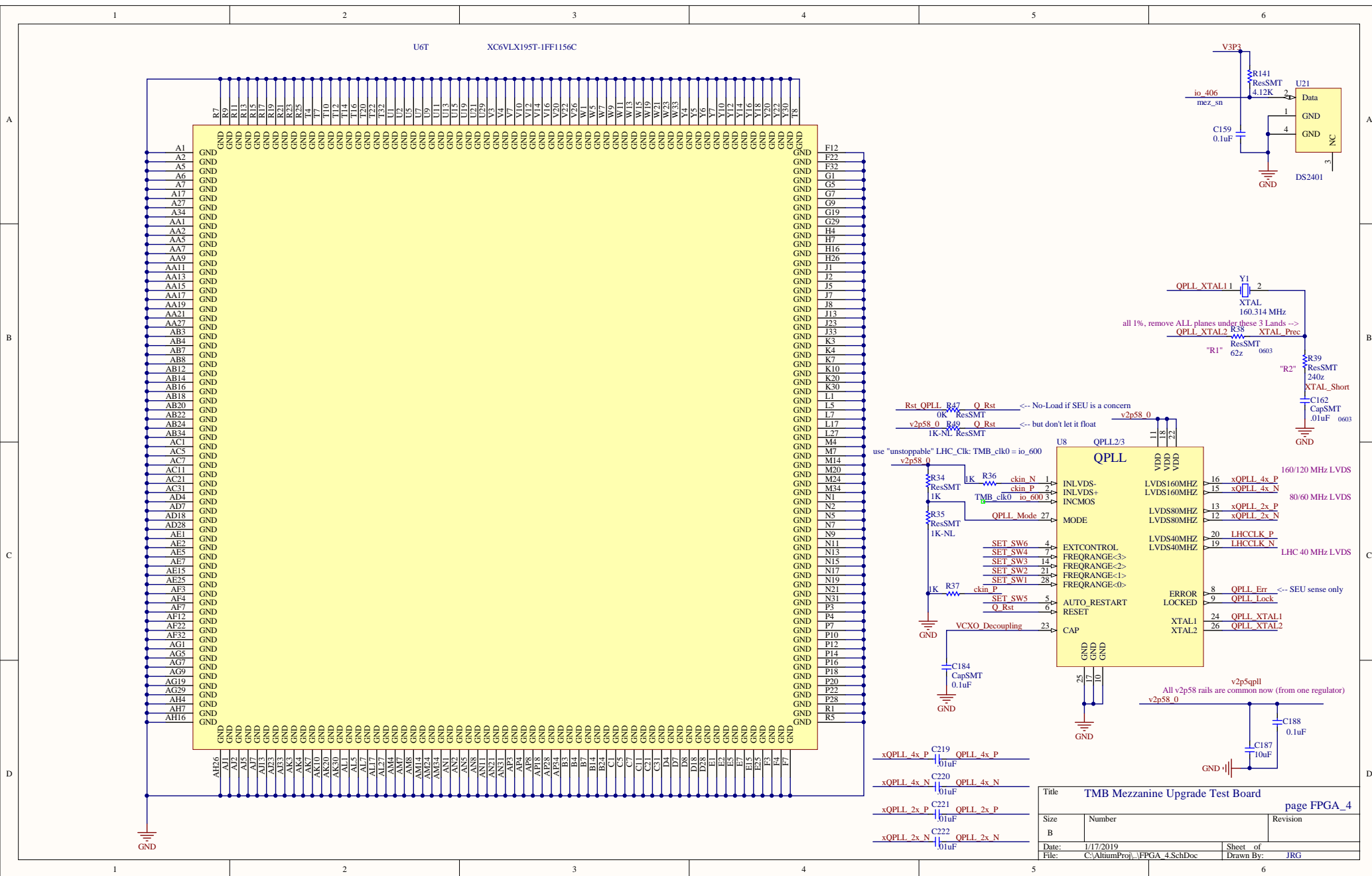
vrp 22 R97 49.9z
vrp 23 R98 49.9z
vrp 24 R99 49.9z
vrp 25 R100 49.9z
vrp 26 R101 49.9z
GND
vrn 22 R92 v2pS8.0
vrn 23 R93 49.9z
vrn 24 R94 v2pS8.0
vrn 25 R95 49.9z
vrn 26 R96 49.9z

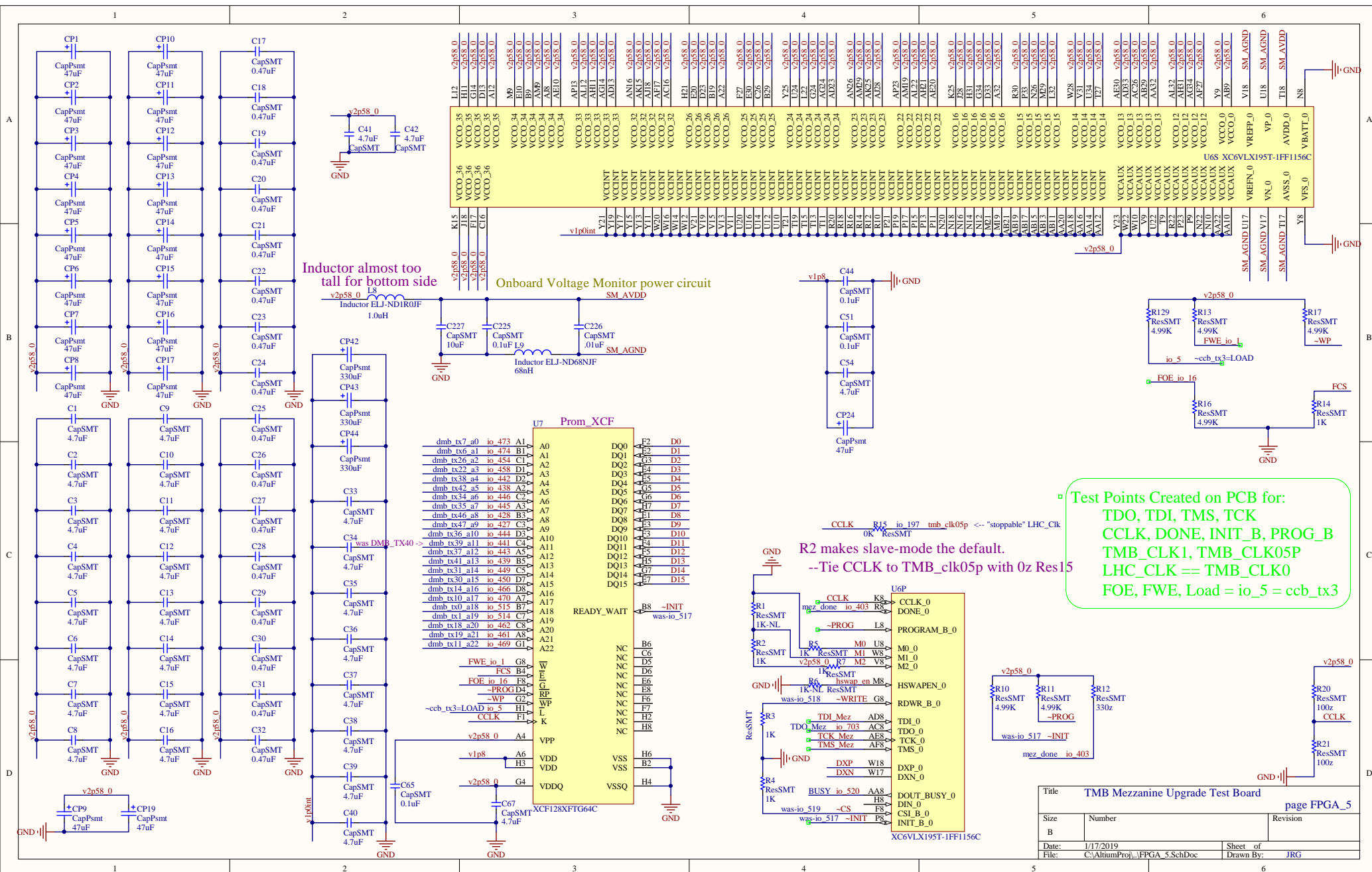
XC6VLX195T-1FF1156C

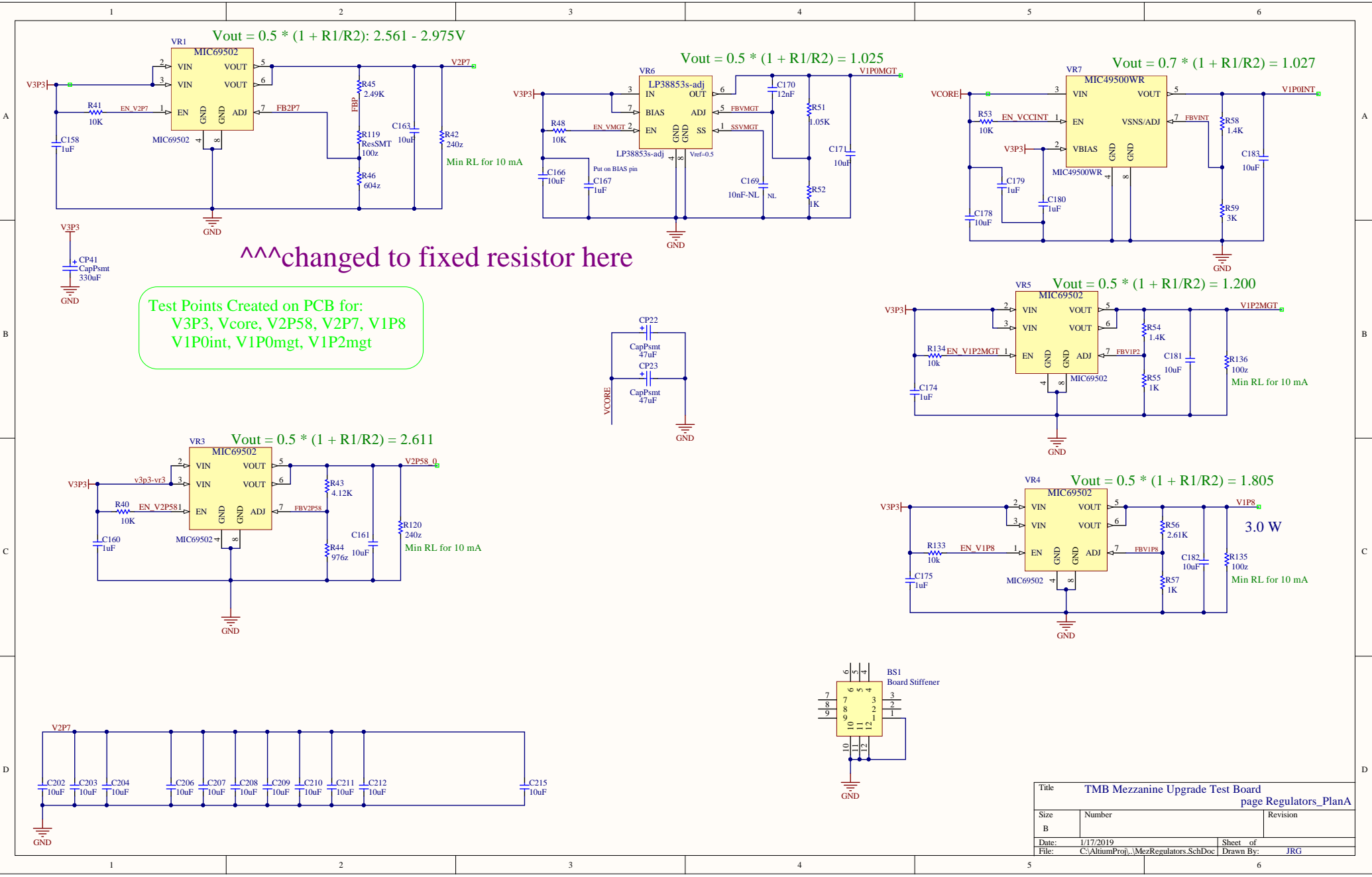
Title TMB Mezzanine Upgrade Test Board			page FPGA_2
Size B	Number	Revision	
Date: 1/17/2019	Sheet of		Drawn By: JRG
File: C:\AltiumProj\...FPGA_2.SchDoc			



Title TMB Mezzanine Upgrade Test Board		
Number		page FPGA_3
Revision		
Date: 1/17/2019	Sheet of	
File: C:\AltiumProj\...FPGA_3.schdoc	Drawn By: JRG	



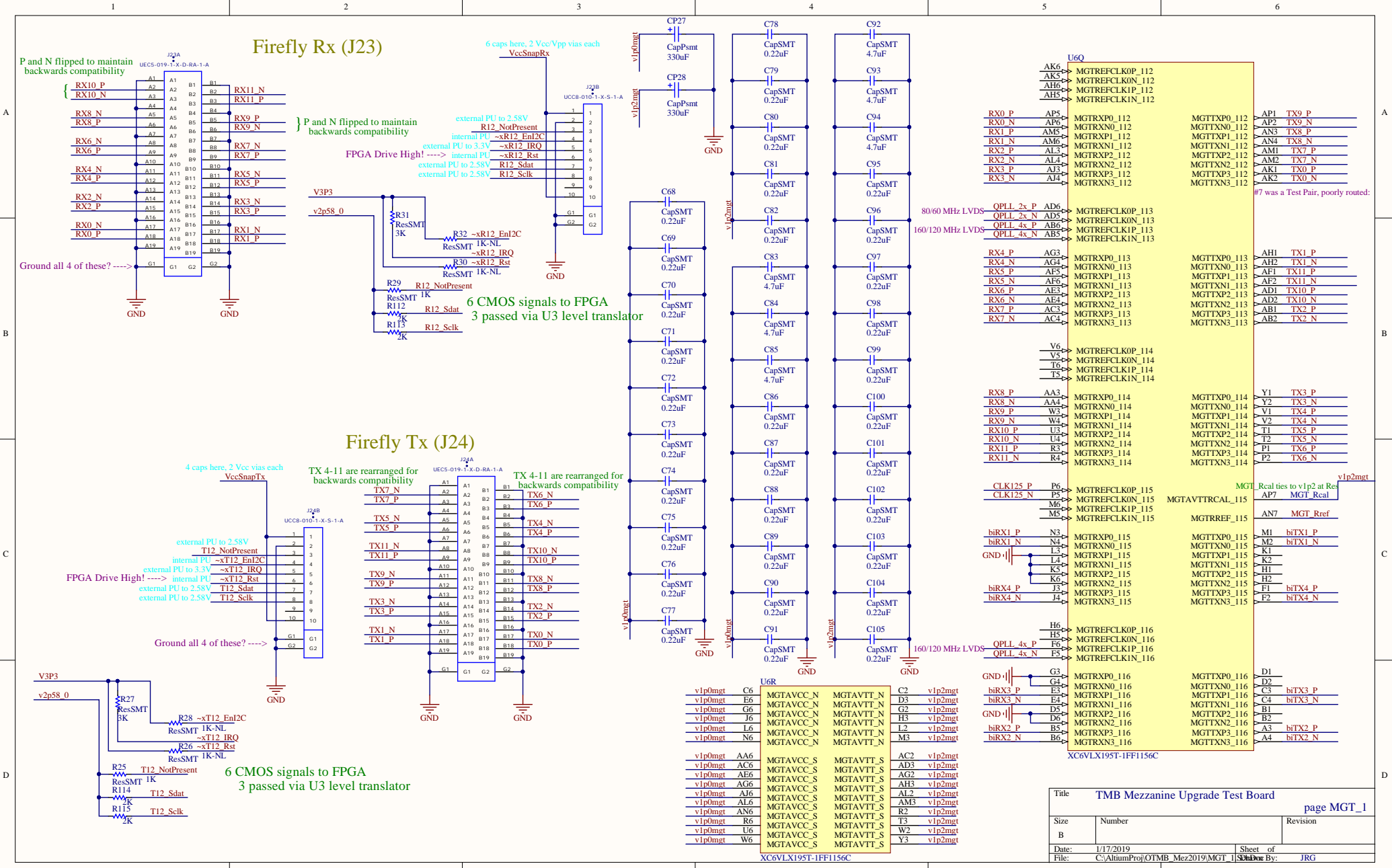




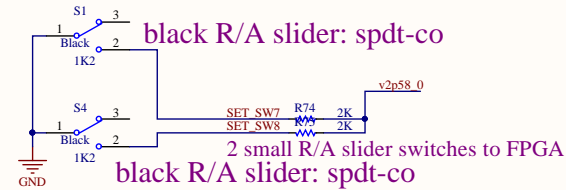
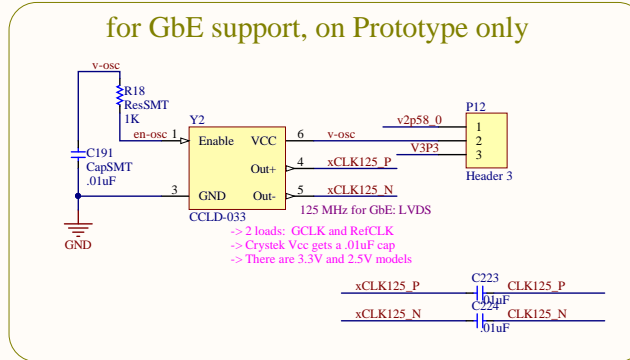
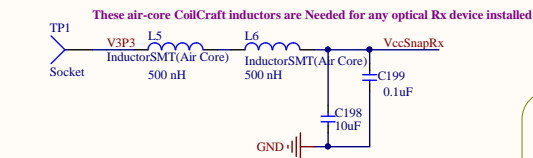
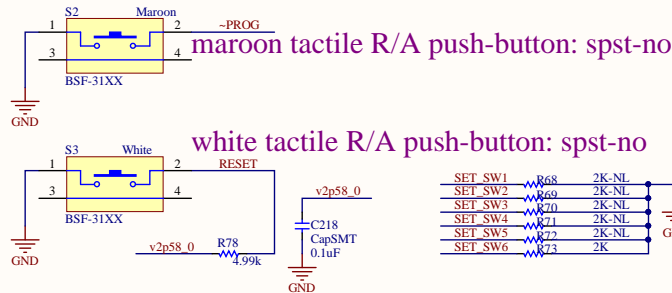
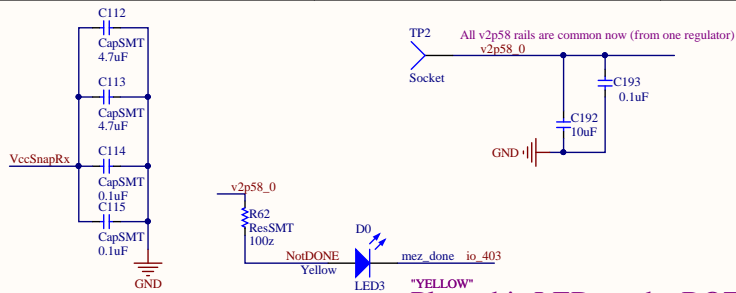
Test Points Created on PCB for:
V3P3, Vcore, V2P58, V2P7, V1P8
V1P0int, V1P0mgt, V1P2mgt

^^^changed to fixed resistor here

Title			TMB Mezzanine Upgrade Test Board
page			Regulators_PlanA
Size	Number	Revision	
B			
Date:	1/17/2019	Sheet of	
File:	C:\AltiumProj\MezRegulators.SchDoc	Drawn By:	JRG



Title		TMB Mezzanine Upgrade Test Board		page MGT_1	
Size B	Number			Revision	
Date:	1/17/2019	Sheet of			
File:	C:\AlumPro\OTMB_Mez2019\MGT_1	Drawn By:	JRG		



"TestLED" IOs actually go to Test Points now

TestLED1	TestLED2
TestLED3	TestLED4
TestLED5	TestLED6
TestLED7	TestLED8
TestLED9	TestLED10

