Network Theorem

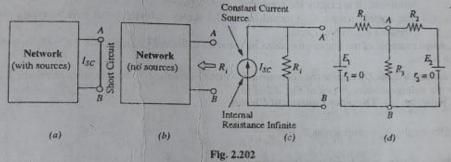
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(62.25, Nopion's Theorem

This theorem is an alternative to the Thevenin's theorem. In fact, it is the dual of Thevenin's theorem. Whereas Thevenin's theorem reduces a two-terminal active network of linear resistances and generators to an equivalent constant-voltage source and series resistance, Norton's theorem replaces the network by an equivalent constant-current source and a parallel resistance.

This theorem may be stated as follows :-

(i) Any two-terming active network containing voltage sources and resistances when viewed from its output terminals, is equivalent to a constant-current source and a parallel resistance. The constant current is equal to the current which would flow in a short-circuit placed across the terminals and parallel resistance is the resistance of the network when viewed from these open-circuited terminals after all voltage and current sources have been removed and replaced by their internal resistances.



Explanation

As seen from Fig. 2.202 (a), a short is placed across the terminals A and B of the network with all its energy sources present. The short-circuit current Isc gives the value of constant-current source. For finding R_0 all sources have been removed as shown in Fig. 2.202 (b). The resistance of the

network when looked into from terminals A and B gives R.

The Norton's equivalent circuit is shown in Fig. 2.202 (c). It consists of an ideal constant-current source of infinite internal resistance (Art. 2.16) having a resistance of R, connected in parallel with it. Solved Examples 2.96, 2.97 and 2.98 etc. illustrate this procedure.

(ii) Another useful generalized form of this theorem is as follows:

The voltage between any two points in a network is equal to Iso, R, where Iso is the short-circuit current between the two points and R, is the resistance of the network as viewed from these points with all voltage sources being replaced by their internal resistances (if any) and current sources replaced by open-circuits.

Suppose, it is required to find the voltage across resistance R₃ and hence current through it [Fig. 2.202 (d)]. If short-circuit is placed between A and B, then current in it due to battery of e.m.f. E, is

 E_1/R_1 and due to the other battery is E_2/R_2 .

$$I_{SC} = \frac{E_1}{R_1} + \frac{E_2}{R_2} = E_1 G_1 + E_2 G_2$$

where G_1 and G_2 are branch conductances.

Now, the internal resistance of the network as viewed from A and B simply consists of three resistances R_1 , R_2 and R_3 connected in parallel between A and B. Please note that here load resistance R3 has not been removed. In the first method given above, it has to be removed.

$$\frac{1}{R_{i}} = \frac{1}{R_{1}} + \frac{1}{R_{2}} + \frac{1}{R_{3}} = G_{1} + G_{2} + G_{3} \qquad \Rightarrow \frac{R_{i}R_{v}}{R_{1} + R_{v}}$$

$$R_{i} = \frac{1}{G_{1} + G_{2} + G_{3}} \quad \therefore \quad V_{AB} = I_{SC}.R_{i} = \frac{E_{1}G_{1} + E_{2}G_{2}}{G_{1} + G_{3} + G_{3}}$$

Current through R_2 is $I_3 = V_{AB}/R_3$. Solved example No. 2.96 illustrates this approach.

2.26. How To Nortonize a Given Circuit?

This procedure is based on the first statement of the theorem given above. (1) Remove the resistance (if any) across the two given terminals and put a short-circuit across them.

- (3) Remove all voltage sources but retain their internal resistances, if any. Similarly, remove
 - all current sources and replace them by open-circuits i.e. by infinite resistance.
- (4) Next, find the resistance R_i (also called R_N) of the network as looked into from the given
- (5) The current source (I_{SC}) joined in parallel across R_i between the two terminals gives

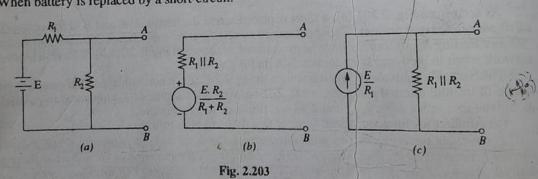
As an example of the above procedure, please refer to Solved Example No. 2.87, 88, 90 and 91 given below.

Example 2.96. Determine the Thevenin and Norton equivalent circuits between terminals A and B for the voltage divider circuit of Fig. 2.203 (a).

Solution. (a) Thevenin Equivalent Circuit

Obviosuly,
$$V_{th} = \text{drop across } R_2 = E \frac{R_2}{R_1 + R_2}$$

When battery is replaced by a short-circuit.



 $R_i = R_1 \parallel R_2 = R_1 R_2 / (R_1 + R_2)$

Hence, Thevenin equivalent circuit is as shown in Fig. 2.203 (b).

(b) Norton Equivalent Circuit

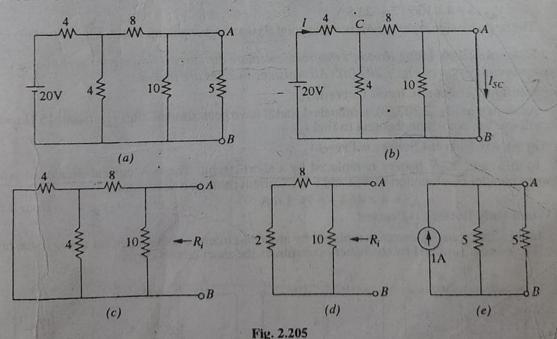
A short placed across terminals A and B will short out R_2 as well. Hence, $I_{sc} = E/R_1$. The Norton equivalent resistance is exactly the same as Thevenin resistance except that it is connected in parallel with the current source as shown in Fig. 2.203 (c).

Example 2.98. Apply Norton's theorem to calculate current flowing through $5 - \Omega$ resistor of Fig. 2.205 (a)

Solution. (i) Remove $5 - \Omega$ resistor and put a short across terminals A and B as shown in Fig.

2.205 (b). As seen, $10 - \Omega$ resistor also becomes short-circuited.

(ii) Let us now find I_{SC} . The battery sees a parallel combination of 4Ω and 8Ω in series with a 4Ω resistance. Total resistance seen by the battery = $4+4\parallel 8=20/3\Omega$. Hence, I=20+20/3=3 A. This current divides at point C of Fig. 2.205 (b). Current going along path CAB gives I_{SC} . Its value = $3\times4/12=1$ A.



(iii) In Fig. 2.205 (c), battery has been removed leaving behind its internal resistance which, in this case, is zero.

Resistance of the network looking into the terminals A and B in fig. 2.205 (d) is

$$R_i = 10 || 10 = 5 \Omega$$

- (iv) Hence, Fig. 2.205 (e), gives the Norton's equivalent circuit.
- (v) Now, join the 5 Ω resistance back across terminals A and B. The current flowing through it, obviously, is $I_{AB} = 1 \times 5/10 = 0.5$ A

Example 2.100. Using Norton's theorem, calculate the current flowing through the 15 Ω load resistor in the circuit of Fig. 2.207 (a). All resistance values are in ohm.

Solution (a) Short-Circuit Current Isc

As shown in Fig. 2.207 (b), terminals A and B have been shorted after removing 15 Ω resistor. We will use Superposition theorem to find I_{SC} .

(i) When Only Current Source is Present

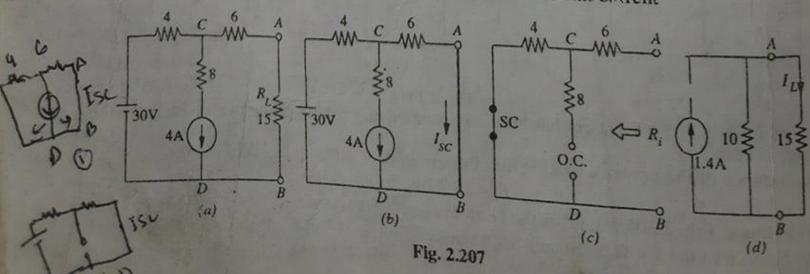
In this case, 30-V battery is replaced by a short-circuit. The 4 A current divides at point D between parallel combination of 4 Ω and 6 Ω . Current through 6 Ω resistor is

$$I_{sc}' = 4 \times 4/(4+6) = 1.6 \text{ A}$$

-from B to A

(ii) When Only Battery is Present

In this case, current source is replaced by an open-circuit so that no current flows in the branch CD. The current supplied by the battery constitutes the short-circuit current



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 $I_{sc}^{"} = 30/(4+6) = 3 \text{ A}$

-from A to B

 $I_{sc} = I_{sc}^{"} - I_{sc}^{"} = 3 - 1.6 = 1.4 \text{ A}$

-from A to B

(b) Norton's Parallel Resistance

As seen from Fig. 2.207 (c), $R_i = 4 + 6 = 10 \Omega$. The 8 Ω resistance does not come into the picture because of an 'open' in the branch CD. Fig. 2.207 (d) shows the Norton's equivalent circuit along with the load resistor. $I_L = 1.4 \times 10/(10 + 15) = 0.56 \text{ A}$

30. Maximum Power Transfer Theorem

Although applicable to all branches of electrical engineering, this theorem is particularly useful for analysing communication networks. The overall efficiency of a network supplying maximum power to any branch is 50 per cent. For this reason, the application of this theorem to power trape mission and distribution networks is limited because, in their case, the goal is high efficiency and not maximum power transfer.

However, in the case of electronic and communication networks, very often, the goal is either to receive or transmit maximum power (through at reduced efficiency) specially when power involved is only a few milliwatts or microwatts. Frequently, the problem of maximum power transfer is of crucial significance in the operation of transmission lines and antennas.

As applied to d.c. networks, this theorem may be stated as follows:

A resistive load will abstract maximum power from a network when the load resistance is Family to the resistance of the network as viewed from the output terminals, with all energy sources remove leaving behind their internal resistances.

In Fig. 2.230 (a), a load resistance of R_L is connected across the terminals A and B of a network which consists of a generator of e.m.f. E and internal resistance R_k and a series resistance R which in fact, represents the lumped resistance of the connecting wires. Let $R_i = R_k + R$ = internal resistance of the netowirk as viewed from A and B.

According to this theorem, R_L will abstract maximum power from the network when R_L =

Proof. Circuit current
$$I = \frac{\sqrt{k_L}}{R_L + R_i} = \frac{\sqrt{k_L}}{R_L + R_i}$$

Power consumed by the load is

$$P_{L} = I^{2}R_{L} = \frac{\sqrt{E_{1}^{2}R_{L}}}{(R_{L} + Ri)^{2}}$$

For P_L to be maximum, $\frac{1}{dR_L} = 0$.

Differentiating Eq. (i) above, we have

$$\frac{dP_L}{dR_L} = \mathbb{E}_{\mathbb{R}^2}^2 \left[\frac{1}{(R_L + R_i)^2} + R_L \left(\frac{-2}{(R_L + R_i)^3} \right) \right] = \mathbb{E}_{\mathbb{R}^2}^2 \left[\frac{1}{(R_L + R_i)^2} - \frac{2R_L}{(R_L + R_i)^3} \right]$$

It is worth noting that under these conditions, the voltage across the load is half the openvoltage at the terminals A and B.

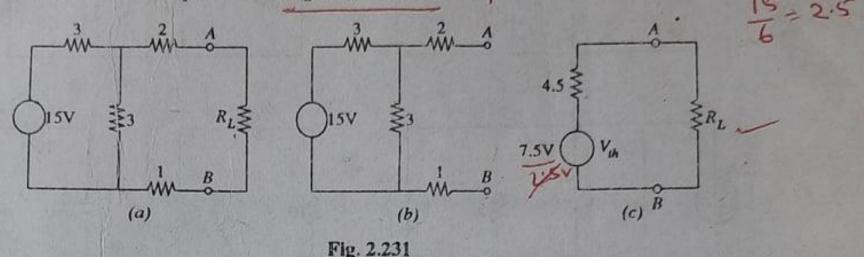
Let us consider an a.c. source of internal impedance $(R_1 + j X_1)$ supplying power to impedance $(R_L + jX_L)$. It can be proved that maximum power transfer will take place when the modu of the load impedance is equal to the modulus of the source impedance i.e. $|Z_1| = |Z_1|$

Where there is a completely free choice about the load, the maximum power transfer is of when load impedance is the complex conjugate of the source impedance. For example, if impedance is $(R_7 + jX_1)$, then maximum transfer power occurs, when load impedance is $(R_1 + jX_2)$ nown that under this condition, the load power is = $E^2/4R$

when load impedance is the complex conjugate of the source impedance. For example, if some impedance is $(R_7 + jX_1)$, then maximum transfer power occurs, when load impedance is $(R_1 - jX_1)$. It can be shown that under this condition, the load power is $= E^2/4R_1$.

Example 2.115. In the network shown in Fig. 2.231 (a), find the value of R_L such that maximum possible power will be transferred to R_L . Find also the value of the maximum power and the power supplied by source under these conditions. (Elect. Engg. Paper I Indian Engg. Services 1989)

Solution. We will remove R_L and find the equivalent [hevenin's source for the circuit to the left of terminals A and B. As seen from Fig. 2.231 (b) V_{th} equals the drop across the vertical resistor of 3 Ω because no current flows through 2 Ω and 1 Ω resistors. Since 15 V drops across two series resistors of 3 Ω each, $V_{th} = 15/2 = 7.5$ V. Thevenin's resistance can be found by replacing 15 V source with a short-circuit. As seen from Fig. 2.231 (b), $R_{th} = 2 + (3|13) + 1 = 4.5 \Omega$. Maximum power transfer to the load will take place when $R_L = R_{th} = 4.5 \Omega$.



Maximum power drawn by $R_L = V_{th}^2/4 \times R_L = 7.5^2/4 \times 4.5 = 3.125 \text{ W}.$

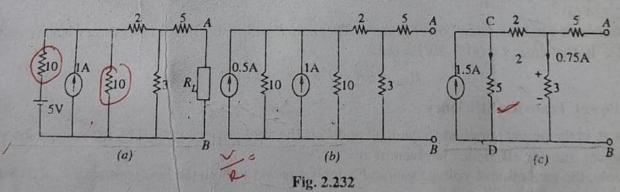
Since same power is developed is R_{th} , power supplied by the source = $2 \times 3.125 = 6.250$ W.

*Example 2.116 In the circuit shown in Fig. 2.232 (a) obtain the condition from maximum power transfer to the load R1. Hence determine the maximum power transferred.

(Elect. Science-I Allahabad Univ. 1992)

Solution. We will find Thevenin's equivalent circuit to the left of terminals A and B for which purpose we will convert the battery source into a current source as shown in Fig. 2.232 (b). By combining the two current sources, we get the circuit of Fig. 2.232 (c), It would be seen that opencircuit voltage V_{AB} equials the drop over 3 Ω resistance because there is no drop on the 5 Ω resistance connected to terminal A. Now, there are two parallel paths across the current source each of resistance 5 Ω. Hence, current through 3 Ω resistance equals 1.5/2 = 0.75 A.) Therefore, $V_{AB} = V_{ij} = 3 \times 0.75 =$

2.25 V with point A positive with respect to point B.



For finding R_{AB} , current source is replaced by an infinite resistance.

$$R_{AB} = R_{th} = 5 + 3 \parallel (2 + 5) = 7.1 \Omega$$

The Thevenin's equivalent circuit along with R_1 is shown in Fig. 2.233. As per Art. 2.30, the condition for MPT is that $R_{\rm I} = 7.1 \,\Omega$.

Maximum power transferred = $V_{th}^2 / 4R_L = 2.25^2 / 4 \times 7.1$ = 0.178 W = 178 mW.

Example 2.117. Calculate the value of R which will absorb maximum power from the circuit of Fig. 2.234 (a). Also, compute the value of maximum power.

Solution. For finding power, it is essential to know both I and R. Hence, it is essential to find an equation relating I to R.

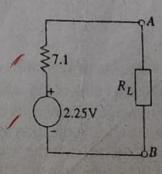


Fig. 2.233

2.31. Power Transfer Efficiency

If P_L is the power supplied to the load and P_T is the total power supplied by the voltage source,

Then power transfer efficiency is given by $\eta = P_L/P_T$.

Now, the generator or voltage source E supplies power to both the load resistance R_L and to the internal resistance $R_i = (R_s + R)$.

$$P_T = P_L + P_i$$
 or $E \times I = I^2 R_L + I^2 R_i$

$$\eta = \frac{P_L}{P_T} = \frac{I^2 R_L}{I^2 R_L + I^2 R_i} = \frac{R_L}{R_L + R_i} = \frac{1}{1 + (R_i / R_L)}$$

The variation of η with R_L is shown in Fig. 2.235 (a). The maximum value of η is unity when $R_L = \infty$ and has a value of 0.5 when $R_L = R_L$. It means that under maximum power transfer conditions, the power transfer efficiency is only 50%. As mentioned above, maximum power transfer condition is important in communication applications but in most power systems applications, a 50% efficiency is undesirable because of the wasted energy. Often, a compromise has to be made between the load power and the power transfer efficiency. For example, if we make $R_L = 2 R_i$, then

 $P_1 = 0.222 E^2/R_1$ and $\eta = 0.667$.

It is seen that the load power is only 11% less than its maximum possible value, whereas the power transfer efficiency has improved from 0.5 to 0.667 i.e. by 33%.