

## 4. ~~ktu~~ ~~Students~~ ~~of~~ ~~loaders~~

- Linker - Links
- Loading - Place object program in memory.
- Linking - Compile two or more object programs
- Relocation - changing the starting address
- Linkage Editor - Provides linking without loading

These types of loaders

1. <del>Absolute loader</del> <del>Bootstrap</del> Bootstrap	Absolute	Relocating
<ul style="list-style-type: none"><li>• Loads the first program usually OS</li><li>• Stored in ROM</li><li>• Predefined</li><li>• Single Pass Loader name is only needed</li><li>• It is a special type of absolute loader</li></ul>	<ul style="list-style-type: none"><li>• Starting address is already specified</li><li>• Exact location is known</li><li>• Simple loader.</li><li>• To verify the program</li><li>• T - loads object code at specified address</li><li>• E - goes to starting address to start execution.</li></ul>	

~~Algorithm for Absolute loader~~

begin

read Header record

Verify pgm name blungen.

read first Text record

while record type ≠ E do

begin

move obj code to specified loc in mem

read next obj pgm record

end

jump to addr specified in end record

end

When a computer is first turned on or restarted a special type of absolute loader called a bootstrap loader is executed this bootstrap loads the first program to be run by the computer usually an operating system. The bootstrap likely begins at address 0 in the memory of the m/c. It loads of the OS starting at address 80. Each byte of object code to be loaded is represented on device F<sub>1</sub> as two hexadecimal digits. The object code from device F<sub>1</sub> is always loaded into consecutive bytes of memory starting at address 80.

BOOT STRAP 0

CLEAR A

LDX #128

LOOP JSUB GETC

RMO A,S

SHIFTL S4  
JSUB GETC  
ADDR SA  
STCH OIX  
TIAR XA J LOOP  
GETC TD INPUT  
JEQ GETC  
RD INPUT  
COMP #4  
JEQ 80  
COMP #48  
JLT GETC  
SUB #48  
COMP #10  
SUB #7  
RETURN RSUB  
INPUT BYTE X'F1'  
END LOOP

KTU

#### Absolute Loader

Advantages

- Simple loader
- no memory limitations

disadvantages

- Program has to specify address
- Specifying subroutines is difficult

## Machine Dependent features of Loader:

- Relocation
- Program linking
- Linking load

It depends on m/c architecture.

Relocation is used bcz the exact locat' is not specified in absolute loader.

Relocation can be done using modification record, relocation bit or b/w changes.

```

0000 COPY START 0
0000 FIRST STL RETADR 000
:
0006 CLOOP +JSUB RDRECDN 0000
0007
000A
0013 +JSUB WRECD
0014
:
0033 RETADR RESW 2
H n n
T n n
E ^ n
M n 000007 A 05
M n 0000014 A 05.

```

2-7 startadding

modification record describe each part of obj' code that must be change in the above prgm. The lines that are effected by relocat' are 0006 and 0013 all modification record have same format. The modification record has value of starting address. The modification record gives the piece of code is:

M n 0000007 A 05
M n 0000014 A 05

Relocation bit:

It is used in SIC m/c because SIC doesn't used relative addressing. Relocation bits are used on m/c that are direct addressing and has a fixed instruction format which means it has relocation bit.

Relocation bit associated with each objcode is used to indicate whether prg or not this word should be change when the program is relocated.

If relocation bit of objcode is set to '1' the program starting address is to be added to this code. When the prgm is relocated. A bit of '0' indicates the no modification is necessary.

Eg: Bit mask of FFC indicates that all '10' words of objcode in the Text record are to be modified during relocation. 11<sup>th</sup> and 12<sup>th</sup> word need not be modify.

Program listing

```

0000 PROGA START 0
      EXTDEF LISTA,ENDA
      EXTREF LISTB, ENDB, LISTC,ENDC
      :
0020 REF1 LDA LISTA    032010
0023 REF2 TLDT LISTB+4 11000004
0027 REF3 LDX #ENDA-LISTA 0500014
0040 LISTA EQU,* 006A-0040 = 0014
      :
0054 ENDA EQU *
0054 REF4 WORD ENDA-LISTA + LISTC
0057 REF5 WORD END(-LISTC-10)
005A REF6 WORD END(-LISTC + LISTA)
005D REF7 WORD ENDA - LISTA - (ENDB-LISTB)
0060 REF8 WORD LISTB - LISTA
      END REF1

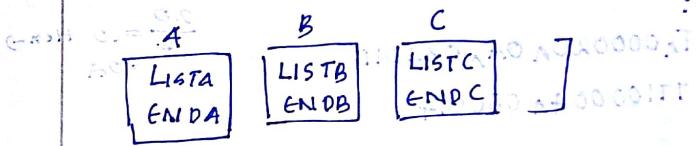
```

LDA	00
LDT	74
LDX	04

→ 0020 REF1 LDA LISTA  
PC-relative

$$\text{displacement} = TA - PC \\ = 0040 - 0023 = 01D$$

[ There are three ~~control section~~ programs in a control section ]



Source Schematic

```

LOC   Source Schematic
0000 PROGA START 0
      EXTDEF LISTA,ENDA
      EXTREF LISTB, ENDB, LISTC,ENDC
      :
0020 REF1 LDA LISTA    032010
0023 REF2 TLDT LISTB+4 11000004
0027 0 REF3 LDX #ENDA-LISTA 0500014
0040 LISTA EQU,* 006A-0040 = 0014
      :
0054 ENDA EQU *
0054 REF4 WORD ENDA-LISTA + LISTC
0057 REF5 WORD END(-LISTC-10)
005A REF6 WORD END(-LISTC + LISTA)
005D REF7 WORD ENDA - LISTA - (ENDB-LISTB)
0060 REF8 WORD LISTB - LISTA
      END REF1

```



MNEUMONIC	OPCODE	ASCII.	HEX	Regis
STL	47	A	41	A
LDA	00	E	45	X000
COMP	28	G	46	X000
JSUB	48	H	47	X000
CLEAR	B4	I	4F	X000
JEQ	30	J	5B	S

Very Imp.

~~PASS ONE OF LINKING LOADER~~

```

begin
    got PROGADDR from Operating System
    Set CSADDR to PROGADDR
    While not end of input db
        begin
            Read next input record
            Set CSLTH to control section length
            Search ESTAB for control section name
            If found then
                Set error flag
            else
                Enter control section name into ESTAB with
                value
            while record type ≠ E00
                begin
                    ...
                end
            end
        end
    end

```

Read next input record  
 If record type = 'D' then define record  
 For each symbol in the record to  
 update symbol in a global symbol  
 begin  
 Search ESTAB for symbol  
 If found then  
 Set error flag (duplicate external symbol)  
 else  
 Enter symbol into ESTAB with value  
 (CSADDR + indicated address)

End (for)  
 End (while ≠ E00)  
 add CSLTH to CSADDR (skipping address for next  
 control section)

End (while not E00)  
 End (pass 1)

Data structures of linking loader

PROGADDR } Variables like I need just  
 CSADDR } to keep track of where program starts  
 ESTAB } Global table of symbols

ESTAB is a table similar to SYMTAB.

To store external symbols

PROGADDR - to store starting address of  
 the programme.

(SADDR - to choose the control section.

Linking loader:  
Linking loader is a loader that consists of a set of object programs that are to be linked together. First pass assigns address to all external symbols. Second pass performs actual coding relocating and linking. There are 3 data structures used.

PROGADDR: It is the begin address where link program is to be loaded. This value is supplied by OS.

Control section Address (SADDR): It is

the starting address of control section currently being loaded by the loader.

This value is added in all relative addresses which in the control section to convert them to the actual address.

Internal symbol table ESTAB is created

to store external name and address of each external symbol using a hash organization.

Algorithm for Pass 2 of a linking loader:

```
begin
    CSADDR to PROGADDR
    EXECADDR to PROGADDR
    while not end of input do
        begin
            read next input record (Header Record)
            set CSLTH to control section length
            while record type ≠ 'E' do
                begin
                    read next input record
                    if record type = 'T' then
                        begin
                            if object code is in character form
                                convert into internal representation
                                move object code from record to
                                location [CSADDR + Specified address]
                            end (if 'T')
                            else if record type = 'M' then
                                begin
                                    search ESTAB for modifying symbol
                                    if found then
                                        add or subtract symbol value at
                                        location [CSADDR + Specified address]
                                    else
                                        set error flag [undefined external
                                        symbol]
                                end
                            end
                        end
                    end
                end
            end
        end
    end
```

End{ if ('M') }  
 End{ while != 'E' }  
 if an address is specified { in End: record } then  
 Set EXECADDR to (CSADDR + Specified address)  
 add CSLTH to CSADDR abd assign to has been status  
 End{ while not EOF }  
 (to location given by EXECADDR { to start execut' of loaded program })  
 { pass 2 }  
 (to start of first basic statu)

- Pass 1
- 1. Resolve External reference.
  - 2. Store its address to ESTAB
  - 3. If refer records define record.
- Pass -2
- 1. Actual linking, loading, relocation
  - 2. Execution starts from EXECADDR.
  - 3. Text record, Modification, End record.
  - Modification record also relocates depending on + or - symbol.
  - Efficiency of Pass -2 can be increased by replacing External symbol with ref no

Machine independent loader features

X → 1. ALS  
 2. Loader options  
 LOADER DESIGN OPTIONS

- Linkage Editor  
 - Dynamic Linking  
 - Boot & Swap Loader  
 - Automatic Library Search / call

ALS allows progs. to use stds sub routines without explicitly including them in the prgm to be loaded the routines are automatically transferred from a library as they are needed during linking

eg: Sqrt already in library, if programmer calls the sqrt Assembly directive present before the sqrt is .EXTREF Then ESTAB is created even though address is not found after pass -1. Then Library automatically enters its address

SORT	←	unresolved forward reference

**Step 1:** Enter symbols from each R record into External II table. When D is encountered address is assigned at the end of pass-1 symbols in EXSTAB that remain undefined ~~separately~~ are called unresolved forward references header searches the library that contain definitions of these symbols and processes the ~~#~~ subroutine found.

A special file structure is used for library that gives name of each subroutine and pointer to its address

~~M/C Independent Loader Features~~

**GLIMIT**: Write code to replace RDREC, WRREC with READ & write FDJ from utility library set of input and Loader option

INCLUDE READ(UTLIB)utility library  
INCLUDE WRITE(UTLIB)

DELETE RDREC, WRREC → DELETE name.  
CHANGE RDREC, READ } CHANGE symbol 1, ei  
CHANGE WRREC, WRITE } symbol 2

#### LOADER DESIGN OPTIONS:

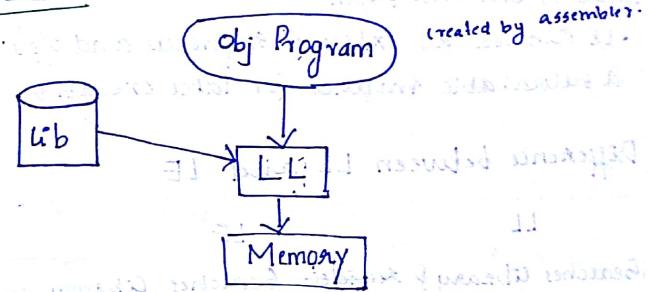
Linking → b4 load time (linkage editor)  
during load time (linking loader)  
after load time (linking loader)  
during execution (dynamic linking)

**Linking Loader**: Linking loader during load.

**Linkage Editor**: first link then stores in library at required it takes from library.

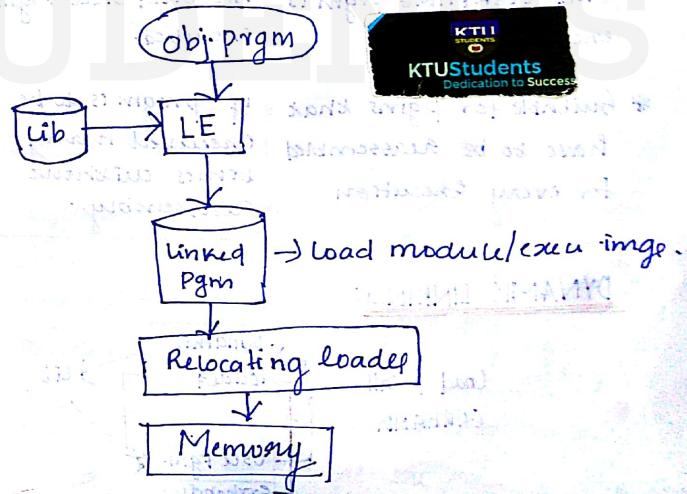
LL

Linking loader



LE

Linkage Editor



LE produces linked version of program (load module / executable image) which is written to a library for later use. LE performs

- Relocation of all control sections relative to start of the link program

- It resolves all external references and o/p's a relocatable module for later execution

→ Difference between LL and LE

LL	LE
* Searches library & <del>resolves</del> resolves external reference EVERYTIME prgm is executed.	Searches library and resolves external reference ONLY ONCE Prgm is executed.
* Suitable for prgms that have to be assembled for every execution	If prgm is to be executed many times without reassembly.

### DYNAMIC LINKING:

fig. #

Load & call  
ERRHANDL

