**VARENDRA UNIVERSITY**

****

***Department of Computer Science & Engineering***

|  |
| --- |
| **Subject** :PHY (112) |
| **Name of Experiment** :To Verify the Voltage Regulation Using Zener Diode. |
| **Group No.07** |

***Lab Report***

**Submitted date: 10/04/2016**

|  |  |  |
| --- | --- | --- |
| Student Name: Md. Nazim Uddin |  | Md. Sarwar Parvez  Mst.Sarmin Sultana |
| ID: 161311113 | Lecturer |
| Section: C (1st semester) | Dept.of CSE |

**Submitted by: Submitted to:**

Digital Electronics Lab

**VARENDRA UNIVERSITY, RAJSHAHI.**