```
Timing Analysis::Edge< T >
 # from
 # to
 + from()
 + fanouts size()
 # Edge()
 # set fanout()
 # add fanout()
    Timing Analysis::Multi
      Fanout Edge< T >
    + Multi Fanout Edge()
    + to()
                < Timing Point >
Timing_Analysis::Multi
Fanout Edge< Timing Point >
+ Multi Fanout Edge()
+ to()
 Timing_Analysis::Timing_Net
 + Timing Net()
 + ~Timing_Net()
 + name()
 + wire_delay_ model()
```