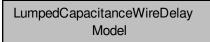
## WireDelayModel #\_lumped\_capacitance # interpolator + WireDelayModel() + ~WireDelayModel() + simulate() + delay\_at\_fanout\_node() + slew\_at\_fanout\_node() + setFanoutPinCapacitance() + root\_delay() + root\_slew() + clear()



+ lumped\_capacitance()

- + LumpedCapacitanceWireDelay Model() + simulate()
  - + delay\_at\_fanout\_node()
  - + slew\_at\_fanout\_node()
  - + setFanoutPinCapacitance() + root delay()
  - + root\_slew()
- + clear()