```
Timing_Analysis::Edge< T >
 #_from
 # _to
 + from()
 + fanouts size()
 # Edge()
 # set fanout()
 # add fanout()
   Timing Analysis::Single
     Fanout Edge< T >
   + Single_Fanout_Edge()
   + to()
                < Timing_Point >
Timing_Analysis::Single
 Fanout Edge< Timing Point >
+ Single Fanout Edge()
+ to()
 Timing_Analysis::Timing_Arc
 + Timing_Arc()
 + ~Timing_Arc()
 + clear()
 + delay()
 + slew()
 + delay()
 + slew()
 + arc_number()
 + gate_number()
```