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Circuit_Netlist

+ Circuit_Netlist()
+ ~Circuit_Netlist()
+ addCellInst()
+ updateTopology()
+ getNetsSize()
+ getNet()
+ getGateT()
+ getTopologicIndex()
```

+ get_net_topologic_index()

+ getGatesSize()
+ getGate()
+ getNetT()

+ verilog()
+ timing_arcs()
+ timing_points()