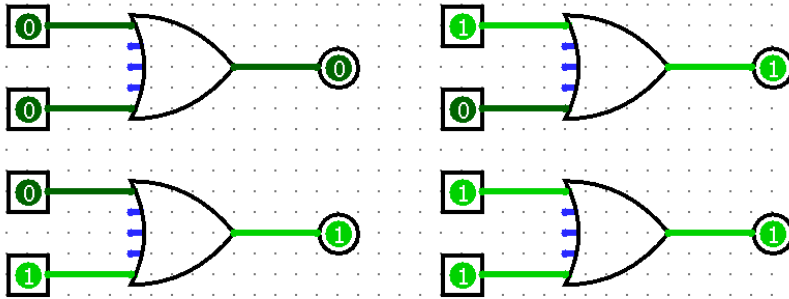
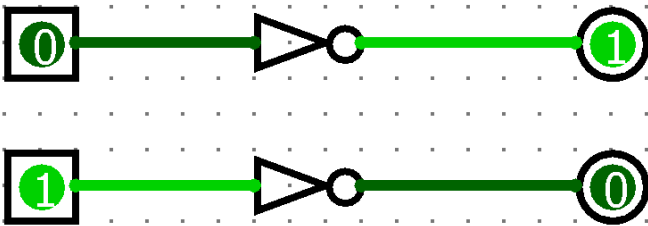


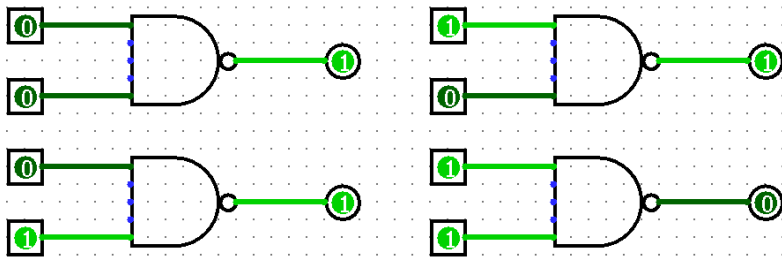
Verification of Truth Table for AND Gate



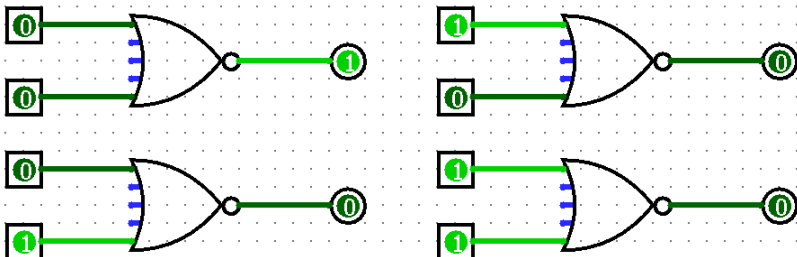
Verification of Truth Table for OR Gate



Verification of Truth Table for NOT Gate



Verification of Truth Table for NAND Gate



Verification of Truth Table for NOR Gates