

Opcode	Macro instruction	T state	Micro operation	Active	CON
	ALL	T1	MAR = PC	Ep, Lm	30 00 00
		T2	PC = PC + 1	Cp	80 00 00
		T3	IR = RAM [MAR]	CE, Li	0C 00 00
0H 4	LDA address	T4	MAR = IR (3..0)	Lm, Ei	12 00 00
		T5	A = RAM [MAR]	CE, La	09 00 00
		T6	None	None	00 00 00
1H 2	MOV B, C	T4	B = C	Lb, Ec	00 00 90
		T5	None	None	00 00 00
		T6	None	None	00 00 00
2H 3	MOV A, C	T4	A = C	La, Ec	01 00 10
		T5	None	None	00 00 00
		T6	None	None	00 00 00
3H 1	MVI C, byte	T4	C = IR (3..0)	Ei, Lc	02 00 20
		T5	None	None	00 00 00
		T6	None	None	00 00 00
4H 5	SUI byte	T4	TEMP = IR (3..0)	Ei, Lt	02 01 00
		T5	A = ALU(A-TEMP)	Cin, Eu, La	01 06 00
		T6	None	None	00 00 00
5H 6	INR A	T4	A = ALU(A+1)	S1, Cin, Eu, La	01 16 00
		T5	None	None	00 00 00
		T6	None	None	00 00 00
6H 7	XRA C	T4	TEMP = C	Ec, Lt	00 01 10
		T5	A = ALU(A XOR TEMP)	S2, S1, S0, Eu, La	01 3A 00
		T6	None	None	00 00 00

7H 8	ORA C	T4	TEMP = C	Lt, Ec	00 01 10
		T5	A = ALU(A OR TEMP)	S2, S1, Eu, La	01 32 00
		T6	None	None	00 00 00
8H 9	ANA B	T4	TEMP = B	Lt, Eb	00 01 40
		T5	A = ALU(A AND TEMP)	S2, Eu, La	01 2200
		T6	None	None	00 00 00
9H 10	ORI byte	T4	TEMP = IR(3..0)	Lt, Ei	02 01 00
		T5	A = ALU(A OR TEMP)	S2, S1, Eu, La	01 32 00
		T6	None	None	00 00 00
AH	CMA	T4	A = ALU(A')	S2, S0, Eu, La	01 2A 00
		T5	None	None	00 00 00
		T6	None	None	00 00 00
BH	MUL2	T4	SHL A	Sa	00 40 00
		T5	None	None	00 00 00
		T6	None	None	00 00 00
CH	JMP address	T4	PC = IR(3..0)	Ei, Lp	42 00 00
		T5	None	None	00 00 00
		T6	None	None	00 00 00
DH	OUT	T4	OUT = A	Lo, Ea	00 80 08
		T5	None	None	00 00 00
		T6	None	None	00 00 00
EH	NOP	T4	None	None	00 00 00
		T5	None	None	00 00 00
		T6	None	None	00 00 00
FH	HLT	T3	None	HLT'	00 00 00