DSD LAB Section AALU Specification Group 5

S ₂	S ₁	S ₀	Operation
0	0	0	B – A
0	0	1	B-1
0	1	0	B + 1
0	1	1	A + B
1	0	0	A·B
1	0	1	B'
1	1	0	A or B
1	1	1	$A \oplus B$

Full Specification

S ₂	S ₁	S ₀	C _{in}	Operation	Туре
0	0	0	0	B-A-1	Arithmetic
0	0	0	1	B-A	Arithmetic
0	0	1	0	<mark>B-1</mark>	Arithmetic
0	0	1	1	В	Arithmetic
0	1	0	0	В	Arithmetic
0	1	0	1	B+1	Arithmetic
0	1	1	0	B+A	Arithmetic
0	1	1	1	B+A+1	Arithmetic
1	0	0	Х	B.A	Logical
1	0	1	Х	B'	Logical
1	1	0	Х	B or A	Logical
1	1	1	Х	B xor A	Logical