





Description	Designator	Quantity	Value	Footprint
Buck Controller	U1	1	TPS40057	TSOP65P640X120_HS-17N
Capacitor	C1	1	3.3uF	CAPC_0603_1608X08L
Capacitor	C2, C6, C8	3	1uF	CAPC_0603_1608X08L
Capacitor	C3	1	100pF	CAPC_0603_1608X08L
Capacitor	C4, C9	2	330pF	CAPC_0603_1608X08L
Capacitor	C5	1	22pF	CAPC_0603_1608X08L
Capacitor	C7	1	100nF	CAPC_0603_1608X08L
Capacitor	C10, C11	2	22uF	CAPAE6053X59M
Capacitor	C12, C13	2	330uF	CAPAE115102X102N
Capacitor	C14, C15	2	180uF	CAPAE10083X104N
Inductor	L1	1	6.8uH	INDP8580X40N
N Channel MOSFET	Q1, Q2	2	Infineon BSC093N04LSGATMA1	INF-PG-TDSON-8-1_V
Resistor	R1	1	1K	RESC_0603_1608X08L
Resistor	R2	1	92K	RESC_0603_1608X08L
Resistor	R3	1	57K	RESC_0603_1608X08L
Resistor	R4	1	18.7K	RESC_0603_1608X08L
Resistor	R5	1	97.6K	RESC_0603_1608X08L
Resistor	R6	1	6.49K	RESC_0603_1608X08L
Resistor	R7	1	100K	RESC_0603_1608X08L
Resistor	R8	1	26.7K	RESC_0603_1608X08L

Design Rules Verification Report

Filename : S:\dev\ws2811\pcbs\buck3\buck3.PcbDoc

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=10mil) (InNet('24V')), (InNet('GND'))	0
Board Clearance Constraint (Gap=0mil) (OnLayer('Top Overlay'))	0
Board Clearance Constraint (Gap=0mil) (All)	0
Short-Circuit Constraint (Allowed=No) (All), (All)	0
Un-Routed Net Constraint (All)	0
Clearance Constraint (Gap=5mil) (All), (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Width Constraint (Min=5mil) (Max=50mil) (Preferred=5mil) (All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Hole Size Constraint (Min=1mil) (Max=100mil) (All)	0
Hole To Hole Clearance (Gap=10mil) (All), (All)	0
Minimum Solder Mask Sliver (Gap=2mil) (All), (All)	0
Silk To Solder Mask (Clearance=0mil) (IsPad), (All)	0
Silk to Silk (Clearance=0mil) (All), (All)	0
Net Antennae (Tolerance=0mil) (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Total	0