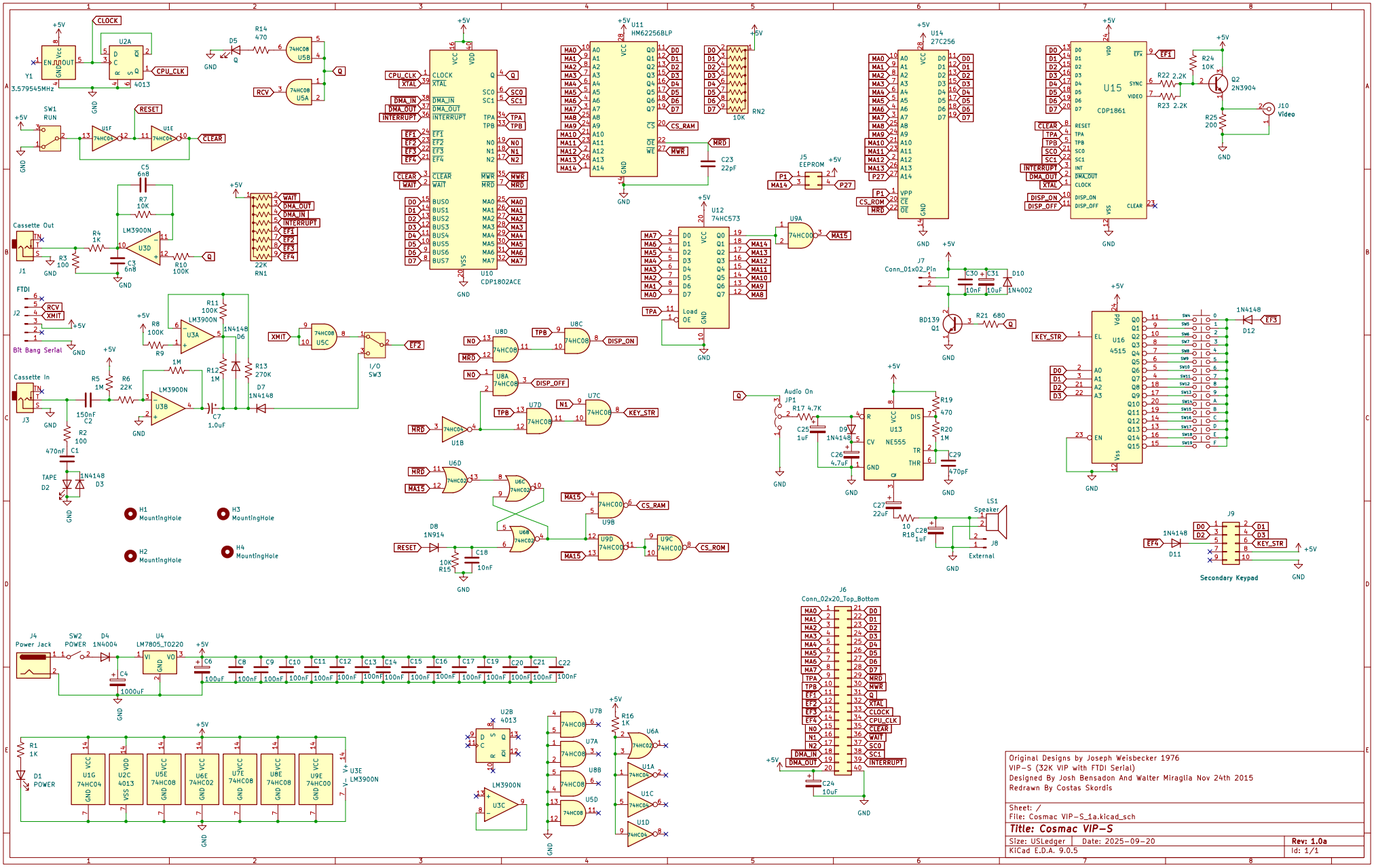
Y1
3.579545MHz
U2A
4013
CLOCK
CPU_CLK
SW1
RUN
RESET
CLEAR
U1F
74HC04
U1E
74HC04
D5
R14
470
U5B
74HC08
U5A
74HC08
RCV
Q
U11
HM62256BLP
U10
CDP1802ACE
RN2
10K
U14
27C256
U15
CDP1861
R24
10K
R22 2.2K
R23 2.2K
R25
200
Q2
2N3904
J10
Video
C5
6n8
R7
10K
LM3900N
U3D
Cassette Out
J1
R4
1K
R3
100
C3
6n8
R10
100K
RN1
22K
WAIT
DMA_OUT
DMA_IN
INTERRUPT
EF1
EF2
EF3
EF4
J5
EEPROM
P1
MA14
P27
CS_ROM
MRD
U12
74HC573
U9A
74HC00
MA15
J7
Conn_01x02_Pin
C30
10nF
C31
10uF
D10
1N4002
BD139
Q1
R21
680
FTDI
J2
RCV
XMIT
Blt Bang Serial
R8
100K
R9
R11
100K
U3A
1N4148
D6
R13
270K
D7
1N4148
XMIT
U5C
74HC08
I/O
SW3
EF2
Cassette In
J3
R5
1M
R6
22K
150nF
C2
R2
100
470nF
C1
TAPE
D2
1N4148
D3
U3B
1M
R12
1M
C7
1.0uF
U8D
74HC08
N0
MRD
U8C
TPB
DISP_ON
U8A
DISP_OFF
U7D
U7C
N1
KEY_STR
U1B
U6D
U6C
74HC02
U6B
MA15
U9B
74HC00
CS_RAM
U9D
U9C
CS_ROM
D8
1N914
RESET
10K
R15
C18
10nF
Audio On
JP1
R17 4.7K
C25
1uF
D9
1N4148
C26
4.7uF
U13
NE555
R19
470
R20
1M
C29
470pF
C27
22uF
R18
10
C28
1uF
LS1
Speaker
J8
External
U16
4515
KEY_STR
D12
1N4148
EF3
D11
1N4148
EF4
J9
Secondary Keypad
H1
MountingHole
H2
MountingHole
H3
MountingHole
H4
MountingHole
J4
Power Jack
SW2
POWER
D4
1N4004
U4
LM7805_TO220
C4
1000uF
C6
100uF
C8
C9
C10
C11
C12
C13
C14
C15
C16
C17
C19
C20
C21
C22
100nF
R1
1K
D1
POWER
U1G
74HC04
U2C
4013
U5E
74HC08
U6E
74HC02
U7E
74HC08
U8E
74HC08
U9E
74HC00
U3E
LM3900N
U2B
4013
U3C
LM3900N
U7B
74HC08
U7A
U8B
U5D
R16
1K
U6A
74HC02
U1A
U1C
U1D
J6
Conn_02x20_Top_Bottom
C24
10uF
Original Designs by Joseph Weisbecker 1976
VIP-S (32K VIP with FTDI Serial)
Designed By Josh Bensadon And Walter Miraglia Nov 24th 2015
Redrawn By Costas Skordis
Sheet: /
File: Cosmac VIP-S_1a.kicad_sch
Title: Cosmac VIP-S
Size: USLedger
Date: 2025-09-20
Rev: 1.0a
KiCad E.D.A. 9.0.5
Id: 1/1