## 

Carmi Merimovich

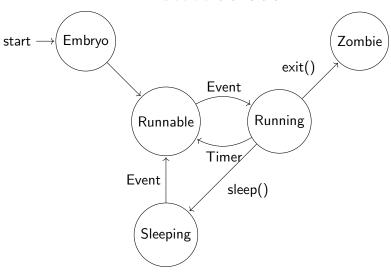
Tel-Aviv Academic College

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#### struct proc

```
2337 struct proc {
    uint sz; // Size of process memory (bytes)
    pde_t* pgdir; // Page table
    char *kstack; // Bottom of kernel stack for this pr
    enum procstate state; // Process state
    volatile int pid; // Process ID
    struct proc *parent; // Parent process
    struct trapframe *tf; // Trap frame for current sys
    struct context *context; // swtch() here to run pro
    void *chan; // If non zero , sleeping on chan
    int killed; // If non zero, have been killed
    struct file * ofile [NOFILE]; // Open files
    struct inode *cwd; // Current directory
    char name[16]; // Process name (debugging)
```

### state transition



transition are due to INTERRUPTS.

ΑII

## proc structure storage

```
RUNNABLE, RUNNING, ZOMBIE }
```

2334 **enum** procstate { UNUSED, EMBRYO, SLEEPING,

```
struct {
  struct spinlock lock;
  struct proc proc[NPROC];
} ptable;
```

#### Context

```
kinit1 (end, P2V(4*1024*1024)); // phys page alloca
1219
     kvmalloc(); // kernel page table
    seginit(); // set up segments
1222
    pinit();
                // process table
1224
1226
     kinit2(P2V(4*1024*1024), P2V(PHYSTOP)); // must co
1227
     userinit(); // first user process
     mpmain();
```

# mpmain()

```
static void mpmain(void) {
    cprintf("cpu%d:_starting_%d\n", cpuid(), cpuid());
    idtinit(); // load idt register
    xchg(&(mycpu()->started), 1); // tell startothers()
    scheduler(); // start running processes
}
```

#### scheduler

```
void scheduler(void) {
 struct proc *p;
 struct cpu *c = mycpu();
 c \rightarrow proc = 0;
 for (;;) { sti();
  acquire(&ptable.lock);
  for(p = ptable.proc; p < &ptable.proc[NPROC]; p++) {
   if (p->state != RUNNABLE) continue;
   c \rightarrow proc = p;
   switchuvm(p);
   p\rightarrow state = RUNNING:
   swtch(&c->scheduler, p->context);
   switchkvm();
   c \rightarrow proc = 0;
  release(&ptable.lock);
```

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# scheduler() operation

- For each proc struct p with state RUNNABLE the following is executed:
  - c−>proc = p;
  - switchuvm().
  - swtch().
  - switchkvm().
  - c->proc=NULL.

## Auxiliary context

- The primary processor begins its C code in main().
- The auxiliary processors begins their C code in mpenter().
- The state on entering either main() or mpenter() is the same.
- There is a separate stack of each processor.

```
static void mpenter(void) {
    switchkvm();
    seginit();
    lapicinit();
    mpmain();
}
```

# mycpu()

```
struct cpu* mycpu(void) {
2436
    int apicid, i;
    if (readeflags()&FL_IF)
     panic("mycpu_called_with_interrupts_enabled\n");
    apicid = lapicid();
    for (i = 0; i < ncpu; ++i)
     if (cpus[i].apicid == apicid)
      return &cpus[i];
    panic ("unknown_apicid \n");
```