Computer-Aided VLSI System Design

Course Information for Week 1

Lecturer: Po-Jen Chen

Graduate Institute of Electronics Engineering, National Taiwan University





TA Contact

Lead TA & Final Project	陳柏任	d13943013@ntu.edu.tw	
TA	張均豪	r12k41024@ntu.edu.tw	
Lecture: Verilog-1	徐以帆	r13943005@ntu.edu.tw	
Lecture: Verilog-2	孫振庭	f12k41031@ntu.edu.tw	
Lecture: Synthesis	王傑立	d13943012@ntu.edu.tw	
Lecture: STA	Minh Khoa Tran	r12k41036@ntu.edu.tw	
Lecture: APR	蔡岳峰	f12943014@ntu.edu.tw	
Lab	黃佳穎	r13943003@ntu.edu.tw	
HW1	曾柏豪 r13943123@r		
HW2 顏子茗		r13943119@ntu.edu.tw	
HW3	吳璨霖	r13943017@ntu.edu.tw	
HW4	江承恩	r13943008@ntu.edu.tw	
HW5	楊耀凱	r12943121@ntu.edu.tw	



Online Meeting Room (1/2)

Google Meet

https://meet.google.com/pqf-czma-vhr

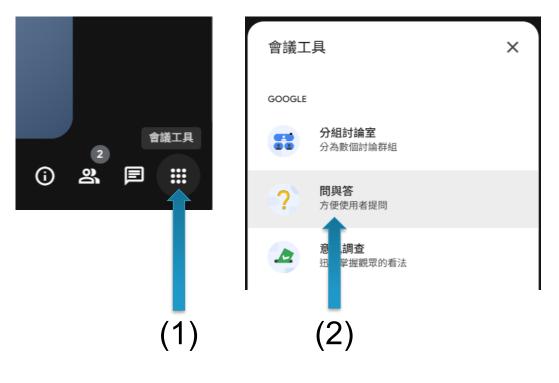
- Please log in using "[Student ID]-[Name]" format
 - Example: D13943013-陳柏任
- Use the private mode of your web browser to log in so that you can change your display name

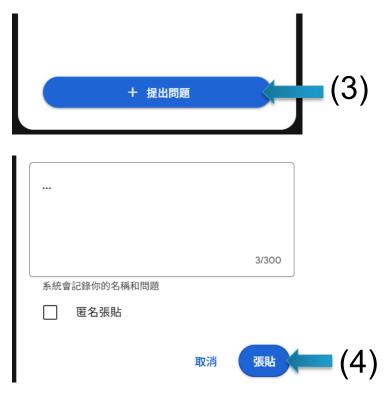




Online Meeting Room (2/2)

- Students cannot use the chatroom by default
- ❖ If you would like to ask questions, click "會議工具" at the bottom right, select "問與答", and post your questions







Lab Information

- A video for Lab0 is on NTU Cool
 - Introduction of IC Design LAB Server connection
- For Lab1 to Lab10
 - TA will randomly select students who are required to complete the lab and upload the results to NTU Cool
 - These submissions will count towards your participation score
 - The list of selected students will be announced after each lab
- ❖ Particularly, some labs will be conducted on ADFP server
 - A final project team of 2 students share the same ADFP account
 - Lab submission deadline will be extended



Important TODO for Week 1

- If you are not yet enrolled in the class:
 - 1. Complete the online Course Registration Form
- **For those who have completed enrollment:**
 - 1. Complete the online IC Design LAB Server Application
 - 2. Complete the online TSRI Membership Application
 - 3. Complete the online **TSRI Process Application**
- For any questions, please email <u>d13943013@ntu.edu.tw</u>
 - If you would like to audit, please send me an email
 - Title should start with [CVSD 2025 Fall]
 - Email with wrong title will be moved to trash automatically



Course Registration Form

Form: https://forms.gle/T8YAC41hkWBGM1RW8

Deadline: 9/5 23:59



- If registering for research purposes, you need to send an additional email and CC your advisor
- The authorization code will be sent via email before the end of this week



IC Design LAB Server Application

Form: https://reurl.cc/Ny1kk9

Deadline: 9/9 23:59

授課教授 / 指導教授 * 您的回答	 楊家驤 (Chia-Hsiang	Yang)
課號 (請查詢過後填寫, 修課者填錯一律不)		3 ,
(https://nol2.aca.ntu.edu.tw/nol/guest/inde		
課程名稱 (Ex: 積體電路設計; 若為研究請打	J研究用; 若為專題請打專題用) *	
您的回答	電腦輔助積體電路 (Computer-Aided VLSI	



TSRI Membership Application

Website: https://www.tsri.org.tw/main.jsp

Deadline: 9/9 23:59



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教育訓練

技術推廣

國際合作

資訊公開

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INTEGRATION
TRANSCENDS LIMITATION

Important Notice:

You must fill in your advisor's information in the membership profile and ensure verification by your advisor

需於會員資料中填寫指導教授並確認通過



TSRI Process Application

Form: https://forms.gle/rqdDpWHxGRPC1uz19

Deadline: 9/9 23:59

- ❖ You also need to complete TSRI Legal Literacy Advocacy Test (法治素養宣導測驗)
 - After you log in to TSRI, you can find the link to the test

