- Easy construction hardware and compilers.

- Minimizing cost. - Minimizing power.

- Maximizing performance.

The stored-program concept, in which a both programs and

data can be stored in memory as numbers.

- There is exactly one operation.
- It has exactly three operands.

1. Simplicity favors regularity.

3. Make the common case fast.

- 2. Smaller is faster.

The natural unit of data used by a particular computer design.

It is usually the size of addressability of the system, meaning

the largest address is two raised to the power of the word size

minus one.

- The farther the signal must travel to reach a register, the slower a cycle may become.
- Addressing large numbers is constrained by the size of the
- instruction.

- Energy conservation.

With data transfer instructions - instructions that move data from memory to the registers (*loads*) and back (*stores*).

ARM uses byte-addressing. Hence its *alignment restriction* requires that addresses must be in multiples of the word size.

Some other architectures uses word-addressing, in which case addresses are simply nonnegative integers.

Big-endian systems use the leftmost byte as the word's

address.
Little-endian systems like ARM use the rightmost byte.

The process of moving program variables that have no space in registers into memory. Compilers will attempt to spill

variables that are least likely to be needed again in the near

future.

- Memory takes longer to access.

- Memory takes more energy.

- Registers can be accessed and operated on in one instruction. Memory loads require instructions that do no
- computation on the loaded values.

A constant value, as opposed to a reference to a register.

... arrays.

- The side of the sign bit feels arbitrary.

"negative".

- The sign of the result of an operation may need to be added in a second step since it cannot be anticipated in advance
- in a second step since it cannot be anticipated in advance.
   Zero is represented twice, once "positive" and once

Since it, like binary, is a power of 2, it is easy to convert to and from binary.

Hardware design is made more complicated.

common to all the formats.

This can be partially mitigated by keeping some properties