

What are the goals of instruction set architecture (ISA) design?

ISAs are essential to supporting which model of computation?

What is the format of an ARM instruction?

What are the four underlying principles of hardware design?

What is a word?

Why not have a larger number of registers?

How do computers get around the small number of registers?

How is ARM addressing different from other architectures?



Describe endianness in terms of addressability.

What is spilling?

Why is it more desirable for a program variable to be in a register than in memory?