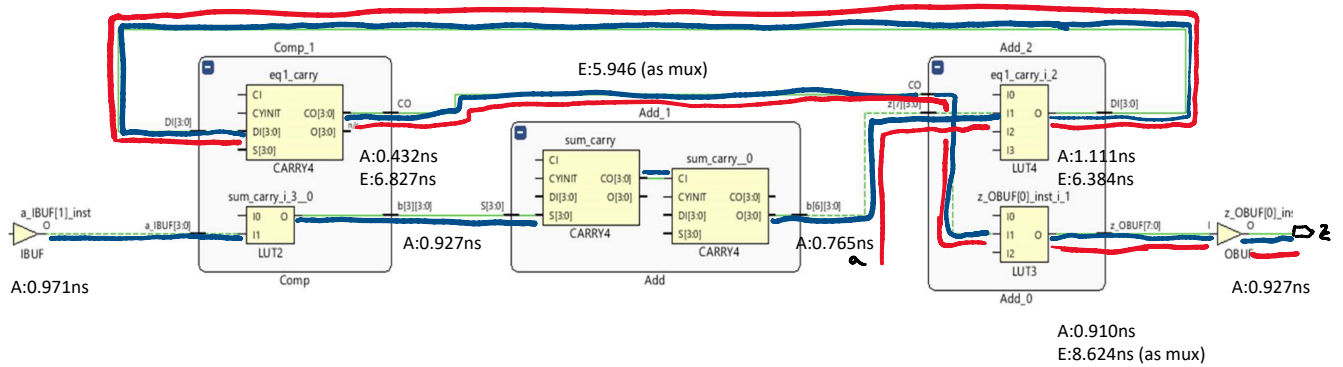


Estimated: Adder>Comparator>Mux>Out



Actual: Buffer>Comparator>Adder>Adder>Comparator>Adder>Buffer>Out



Vivado seems to have found a way around using the specific components that we constructed with only 2 adders and a comparator instead of 2 adders, a comparator, and a mux.