**Schematic and Board Layout Design Review Notes**

TEAM BEING REVIEWED: 10

REVIEWING TEAM: 6

DATE: 11/7/18

**Instructions:**

1. Reviewed team explains the concept of operation of the project.
2. Reviewer reads through schematic, layout, and BOM attempting to answer basic questions such as: what is the power supply? What is this IC and what does it do? Does the layout make sense?
3. Reviewer reads through schematic, layout, and BOM looking for best practices, such as: Are there enough bypass caps? Are the traces the right thickness?
   1. Reviewers are encouraged to use the Schematic and Layout Checklists that are posted on the D2L site.
4. One person on the reviewed team interacts and answers reviewer’s questions. A different person on the reviewed team takes notes, organized in the format below.

* **SCHEMATIC**
  + CRITICAL
    - Check Pinout of CPU
  + MAJOR
    - Check Power dissipation for different LED display
    - Power draws of extra LEDs
  + MINOR
    - Use White background
* **LAYOUT**
  + CRITICAL
    - Put power connector on edge. J2 is connection
  + MAJOR
    - Don’t put battery near heat source!
    - But as far away from battery regulation as possible
    - Add in diodes
  + MINOR
    - Weird lines on different components, greyed out? Help why does it do this?
    - Straighten out traces
    - Check Thickness
* **Bill of Materials (BOM)**
  + No Problems