

744 Processor CC6 May Not Restore Trap Registers

Description

Following a core C6 (CC6) power state transition, the processor core may not restore the following registers:

- MSRC001_0053, IO Trap Register 3
- MSRC001_0052, IO Trap Register 2
- MSRC001_0051, IO Trap Register 1
- MSRC001_0050, IO Trap Register 0

Instead, the registers are set to zero when the processor resumes from CC6 state.

This erratum only applies on processor models that have a single core per compute-unit (Compute Unit Status Register[*DualCore*], D18F5x80[16] is 0b).

Potential Effect on System

The processor may not observe debug traps after the processor has entered and exited CC6 mode.

Suggested Workaround

BIOS should not use these trap registers.

Fix Planned

No fix planned