

144 CLFLUSH to Shadow RAM Address Will Not Invalidate

Description

WrDram and RdDram bits in extended MTRR type registers are used to copy BIOS ROM to corresponding DRAM and then execute out of DRAM. When these are configured to direct writes to ROM (WrMem =0b) and reads to DRAM (RdMem =01b), the CLFLUSH instruction will not invalidate shadow RAM addresses in the cache.

Potential Effect on System

CLFLUSH instruction is ineffective for shadow RAM space.

Suggested Workaround

Use the WBINVD instruction instead of CLFLUSH in shadow RAM space.

Fix Planned

No