563 Processor May Store Incorrect Resume Flag in VMCB

Description

A processor core may incorrectly store rFLAGS.RF = 1 in the VMCB when the processor is performing an SVM interception on a CLI instruction, including an intercept on an exception that occurred during execution of the CLI instruction.

Potential Effect on System

None expected under normal conditions. Instruction breakpoints on CLI instructions may not be received for programs running under a hypervisor.

Suggested Workaround

None required.

Fix Planned

No