573 Processor May Incorrectly Update Instruction Pointer After FSINCOS Instruction

Description

After execution of an FSINCOS instruction, the processor core may incorrectly update the instruction pointer (rIP) and execute incorrect instructions or may hang.

Potential Effect on System

Unpredictable system behavior after execution of an FSINCOS instruction.

Suggested Workaround

Contact your AMD representative for information on a BIOS update.

Fix Planned

No