

## 704 Processor May Report Incorrect Instruction Pointer

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### Description

Under a highly specific and detailed set of internal timing conditions, the processor may store an incorrect instruction pointer (rIP) while processing an interrupt or a debug trap exception (#DB).

### Potential Effect on System

Unpredictable system behavior.

### Suggested Workaround

Contact your AMD representative for information on a BIOS update.

### Fix Planned

No fix planned