

385 Processor May Report Incorrect Address For an L3 Cache Error Machine Check

Description

The processor may report an incorrect address at NB Machine Check Address Register MSR0000_0412 when executing a machine check for an L3 cache error. In addition, when disabling an L3 cache index by writing to L3 Cache Index Disable Registers F3x[1C0, 1BC] [Index], the processor may not disable the intended L3 cache index.

Potential Effect on System

Operating system software may take inappropriate action due to an incorrectly reported L3 cache error.

Suggested Workaround

System software should program F3x1B8[23] to 1b before enabling the L3 cache using CR0[30] (CD).

Fix Planned

No