659 VMCB Interrupt Shadow Status May Be Incorrect

Description

The processor may fail to clear the VMCB INTERRUPT_SHADOW field (VMCB offset 068h bit 0) when intercepting or interrupting an SVM guest that is executing a Move String instruction with a REP prefix under interrupt shadow. This erratum does not occur on the last iteration of the Move String instruction.

Potential Effect on System

The SVM guest may continue to operate under interrupt shadow until the Move String instruction has completed. This may delay servicing of a pending interrupt.

Suggested Workaround

Contact your AMD representative for information on a BIOS update.

Fix Planned

Yes