734 Processor May Incorrectly Store VMCB Data

Description

Under a highly specific and detailed set of internal timing conditions during a #VMEXIT for a virtual machine guest that has multiple virtual CPUs, the processor may store incorrect data to the virtual machine control block (VMCB) reserved and guest save areas and may also store outside of the VMCB.

Potential Effect on System

Data corruption.

Suggested Workaround

Contact your AMD representative for information on a BIOS update.

Fix Planned

No fix planned