

Table 3-5 Power on/Reset Sequence Parameters

Parameter	Description	Min	Max	Units
Tr1	Rise time of IOVCC from 0% to 100%	0.1	10	ms
Tr2	Rise time of VDDA from 0% to 100%	0.1	10	ms
Tprt	Time for RSTN to remain low after IOVCC/VDDA power is ready	1	--	ms
Trio	Time for INT to set input to output after RSTN becomes high	6	--	ms
Trsi	Time of starting to report point after resetting	TBD	TBD	ms
Trpt	Time for RSTN to remain low before power off	1	--	ms
Trst	Reset time	10	--	us