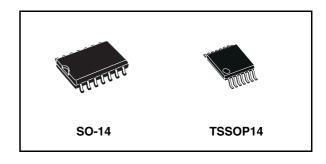


74LCX08

Low voltage CMOS QUAD 2-input AND gate with 5V tolerant inputs

Features

- 5V tolerant inputs
- High speed:
 - t_{PD} = 4.1ns (Max) at V_{CC} = 3V
- Power down protection on inputs and outputs
- Symmetrical output impedance:
 - $II_{OH}I = I_{OL} = 24mA$ (Min) at $V_{CC} = 3V$
- PCI bus levels guaranteed at 24mA
- Balanced propagation delays:
 - $t_{PLH} \cong t_{PHL}$
- Operating voltage range:
 - V_{CC} (Opr) = 2.0V to 3.6V
- Pin and function compatible with 74 series 08
- Latch-up performance exceeds 500mA (JESD 17)
- ESD performance:
 - HBM > 2000V (MIL STD 883 method 3015); MM > 200V



Description

The 74LCX08 is a low voltage CMOS QUAD 2-input AND gate fabricated with sub-micron silicon gate and double-layer metal wiring C²MOS technology. It is ideal for low power and high speed 3.3V applications; it can be interfaced to 5V signal environment for inputs.

It has same speed performance at 3.3V than 5V AC/ACT family, combined with a lower power consumption.

All inputs and outputs are equipped with protection circuits against static discharge, giving them 2KV ESD immunity and transient excess voltage.

Order codes

Part number	Package	Packaging
74LCX08MTR	SO-14	Tape and reel
74LCX08TTR	TSSOP14	Tape and reel

Contents 74LCX08

Contents

1	Logic symbols and I/O equivalent circuit				
2	Pin s	settings	4		
	2.1	Pin connection	4		
	2.2	Pin description	4		
	2.3	Truth table	4		
3	Maxi	mum rating	5		
	3.1	Recommended operating conditions	5		
4	Elec	trical characteristics	6		
5	Test	circuit	8		
6	Wav	eforms	8		
7	Pack	rage mechanical data	9		
8	Revi	sion history	4		

1 Logic symbols and I/O equivalent circuit

Figure 1. IEC logic symbols

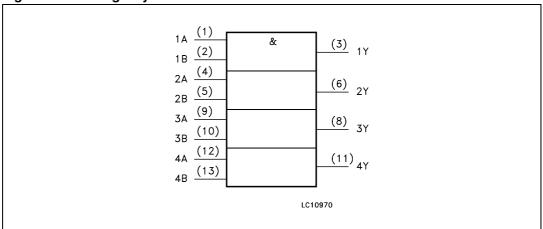
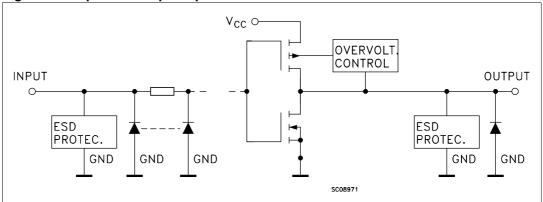


Figure 2. Input and output equivalent circuit

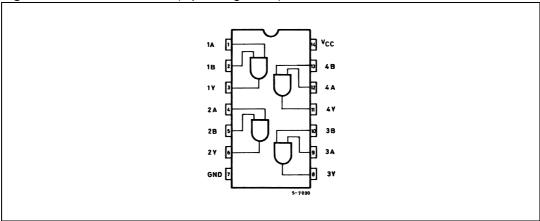


Pin settings 74LCX08

2 Pin settings

2.1 Pin connection

Figure 3. Pin connection (top through view)



2.2 Pin description

Table 1. Pin description

Table III III accomp						
Pin N°	Symbol	Name and function				
1, 4, 9, 12	1A to 4A	Data inputs				
2, 5, 10, 13	1B to 4B	Data inputs				
3, 6, 8, 11	1Y to 4Y	Data outputs				
7	GND	Ground (0V)				
14	V _{CC}	Positive supply voltage				

2.3 Truth table

Table 2. Truth table

Inp	Output	
A	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

74LCX08 Maximum rating

3 Maximum rating

stressing the device above the rating listed in the "absolute maximum ratings" table may cause permanent damage to the device. these are stress ratings only and operation of the device at these or any other conditions above those indicated in the operating sections of this specification is not implied. exposure to absolute maximum rating conditions for extended periods may affect device reliability. refer also to the STMicroelectronics sure program and other relevant quality documents.

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage	-0.5 to +7.0	V
V _I	DC input voltage	-0.5 to +7.0	٧
V _O	DC output voltage (V _{CC} = 0V)	-0.5 to +7.0	V
V _O	DC output voltage (high or low state) (1)	-0.5 to V _{CC} + 0.5	٧
I _{IK}	DC input diode current	-50	mA
I _{OK}	DC output diode current (2)	-50	mA
Io	DC output current	±50	mA
I _{CC}	DC supply current per supply pin	±100	mA
I _{GND}	DC ground current per supply pin	±100	mA
T _{stg}	Storage temperature	-65 to +150	°C
TL	Lead temperature (10 sec)	300	°C

^{1.} I_{O} absolute maximum rating must be observed

3.1 Recommended operating conditions

Table 4. Recommended operating conditions

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage (1)	2.0 to 3.6	V
V _I	Input voltage	0 to 5.5	V
V _O	Output voltage (V _{CC} = 0V)	0 to 5.5	V
V _O	Output voltage (high or low state)	0 to V _{CC}	V
I _{OH} , I _{OL}	High or low level output current (V _{CC} = 3.0 to 3.6V)	±24	mA
I _{OH} , I _{OL}	High or low level output current (V _{CC} = 2.7V)	±12	mA
T _{op}	Operating temperature	-40 to 85	°C
dt/dv	Input rise and fall time (2)	0 to 10	ns/V

^{1.} Truth table guaranteed: 1.5V to 3.6V

^{2.} V_O < GND

^{2.} V_{IN} from 0.8V to 2V at $V_{CC} = 3.0V$

Electrical characteristics 74LCX08

4 Electrical characteristics

Table 5. DC specifications

		Te	Test condition		Value		
Symbol	Parameter	V _{CC}		-40 to	85°C	Unit	
		(V)		Min	Max		
V_{IH}	High level input voltage	2.7 to 3.6		2.0		V	
V_{IL}	Low level input voltage	2.7 10 3.0			0.8	V	
		2.7 to 3.6	I _O =-100 μA	V _{CC} -0.2			
V.	High level output voltage	2.7	I _O =-12 mA	2.2		v	
V _{OH}		0.0	I _O =-18 mA	2.4		V	
		3.0 I _O =-24 mA	I _O =-24 mA	2.2			
		2.7 to 3.6	I _O =100 μA		0.2		
V.	Low level output	2.7	I _O =12 mA		0.4	v	
V _{OL}	voltage	0.0	I _O =16 mA		0.4	1 V	
		3.0	I _O =24 mA		0.55		
I _I	Input leakage current	2.7 to 3.6	V _I = 0 to 5.5V		±5	μΑ	
l _{off}	Power OFF leakage current	0	V _I or V _O = 5.5V		10	μΑ	
la a	Quiescent supply	0.7 to 0.0	$V_I = V_{CC}$ or GND		10		
I _{CC}	current	2.7 to 3.6	V_{I} or V_{O} = 3.6 to 5.5V		±10	μΑ	
ΔI_{CC}	I incr. per Input	2.7 to 3.6	V _{IH} = V _{CC} - 0.6V		500	μΑ	

Table 6. Dynamic switching characteristics

		Test condition		Value			
Symbol	Parameter	v _{cc}		T,	_λ = 25 °	Č	Unit
		(V)		Min	Тур	Max	
V _{OLP}	Dynamic low level quiet	3.3	$C_L = 50pF$ $V_{IL} = 0V, V_{IH} = 3.3V$		0.8		V
V _{OLV}	output ⁽¹⁾	ა.ა	$V_{IL} = 0V, V_{IH} = 3.3V$		-0.8		V

Number of outputs defined as "n". Measured with "n-1" outputs switching from HIGH to LOW or LOW to HIGH. The remaining output is measured in the LOW state.

6/15

Table 7. AC electrical characteristics

		Test condition				Value			
Symbol	Parameter	V _{CC}	CL	R_{L}	$t_s = t_r$	-40 to	85 °C	Unit	
		(V)	(pF)	(Ω)	(ns)	Min	Max		
t _{PLH} t _{PHL}	Propagation delay	2.7	50	500	2.5		4.8	ns	
tir	time	3.0 to 3.6	50	300	2.5	1.0	4.1	113	
toslh toshl	Output to output skew time (1) (2)	3.0 to 3.6	50	500	2.5		1.0	ns	

Skew is defined as the absolute value of the difference between the actual propagation delay for any two outputs of the same device switching in the same direction, either HIGH or LOW (t_{OSLH} = | t_{PLHm} - t_{PLHn}|, t_{OSHL} = | t_{PHLm} - t_{PHLn}|)

Table 8. Capacitive characteristics

		Test condition		Value			
Symbol	Parameter	V _{CC}		T	_A = 25 °	С	Unit
		(V)		Min	Тур	Max	
C _{IN}	Input capacitance	3.3	$V_{IN} = 0$ to V_{CC}		6		pF
C _{PD}	Power dissipation capacitance (1)	3.3	$f_{IN} = 10MHz$ $V_{IN} = 0 \text{ or } V_{CC}$		3.8		pF

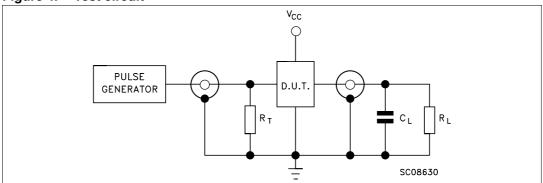
C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation. I_{CC(opr)} = C_{PD} x V_{CC} x f_{IN} + I_{CC}/4 (per gate)

^{2.} Parameter guaranteed by design

Test circuit 74LCX08

5 Test circuit

Figure 4. Test circuit



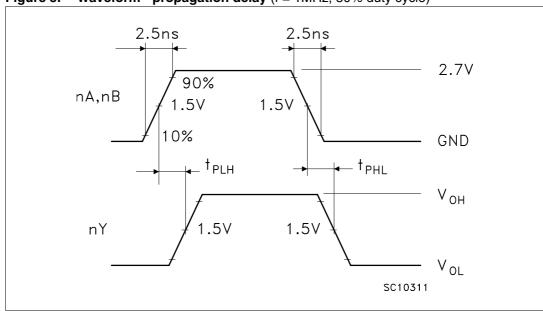
 $C_L = 50 pF$ or equivalent (includes jig and probe capacitance)

 $R_L = 500\Omega$ or equivalent

 $R_T = Z_{OUT}$ of pulse generator (typically 50 Ω)

6 Waveforms

Figure 5. Waveform - propagation delay (f = 1MHz; 50% duty cycle)

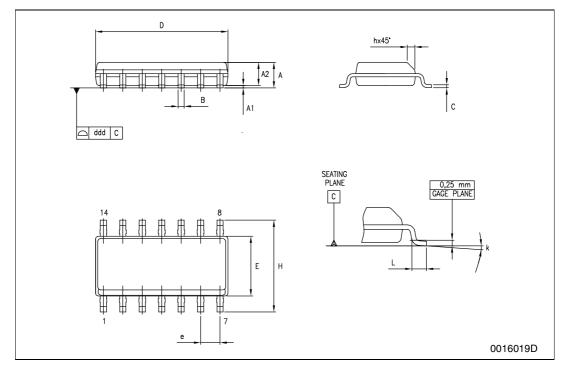


7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

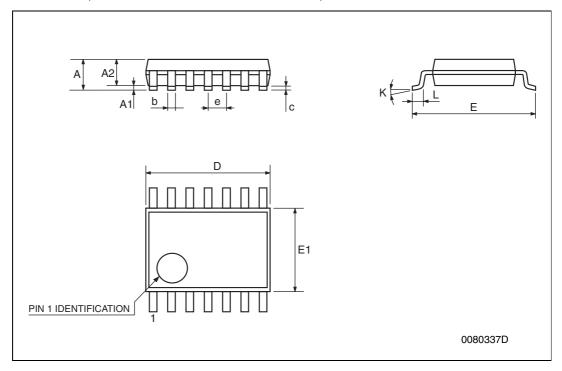
SO-1/	MECH	\mathbf{N}	DATA

DIM.		mm.			inch			
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.		
Α	1.35		1.75	0.053		0.069		
A1	0.1		0.25	0.004		0.010		
A2	1.10		1.65	0.043		0.065		
В	0.33		0.51	0.013		0.020		
С	0.19		0.25	0.007		0.010		
D	8.55		8.75	0.337		0.344		
Е	3.8		4.0	0.150		0.157		
е		1.27			0.050			
Н	5.8		6.2	0.228		0.244		
h	0.25		0.50	0.010		0.020		
L	0.4		1.27	0.016		0.050		
k	0°		8°	0°		8°		
ddd			0.100			0.004		



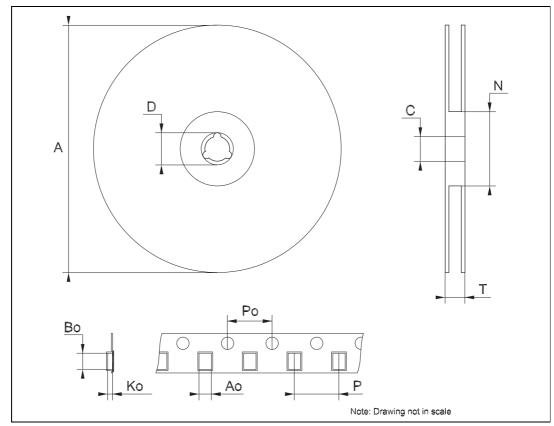
TSSOP14 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α			1.2			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.8	1	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
С	0.09		0.20	0.004		0.0089
D	4.9	5	5.1	0.193	0.197	0.201
E	6.2	6.4	6.6	0.244	0.252	0.260
E1	4.3	4.4	4.48	0.169	0.173	0.176
е		0.65 BSC			0.0256 BSC	
К	0°		8°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030



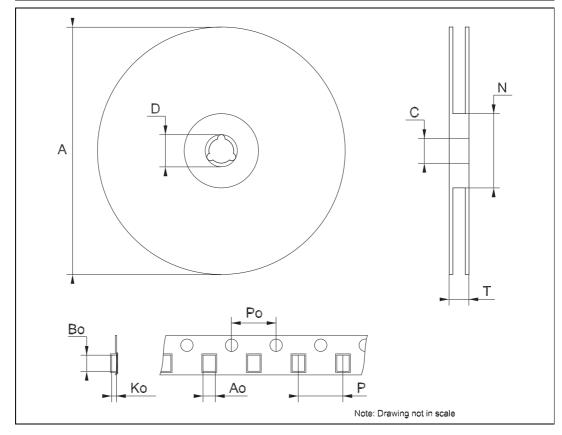
Tape &	Reel SC)-14 MECI	HANICAL	DATA
--------	---------	-----------	---------	------

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			22.4			0.882
Ao	6.4		6.6	0.252		0.260
Во	9		9.2	0.354		0.362
Ko	2.1		2.3	0.082		0.090
Po	3.9		4.1	0.153		0.161
Р	7.9		8.1	0.311		0.319



Tape & Reel TSSOP14 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			22.4			0.882
Ao	6.7		6.9	0.264		0.272
Во	5.3		5.5	0.209		0.217
Ko	1.6		1.8	0.063		0.071
Po	3.9		4.1	0.153		0.161
Р	7.9		8.1	0.311		0.319



Revision history 74LCX08

8 Revision history

Table 9. Revision history

Date	Revision	Changes
15-Sep-2004	4	Ordering codes revision - pag. 1.
10-Jul-2006	5	New template, temperature ranges updated

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZE REPRESENTATIVE OF ST, ST PRODUCTS ARE NOT DESIGNED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS, WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2006 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

