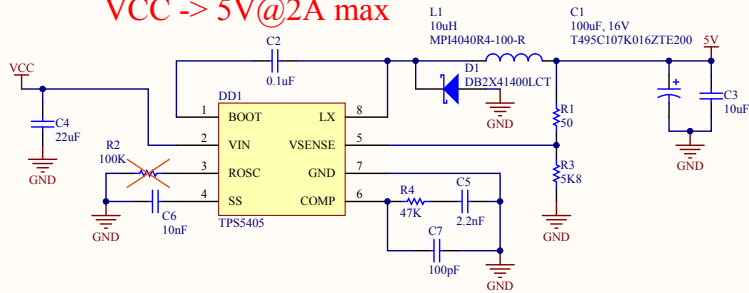
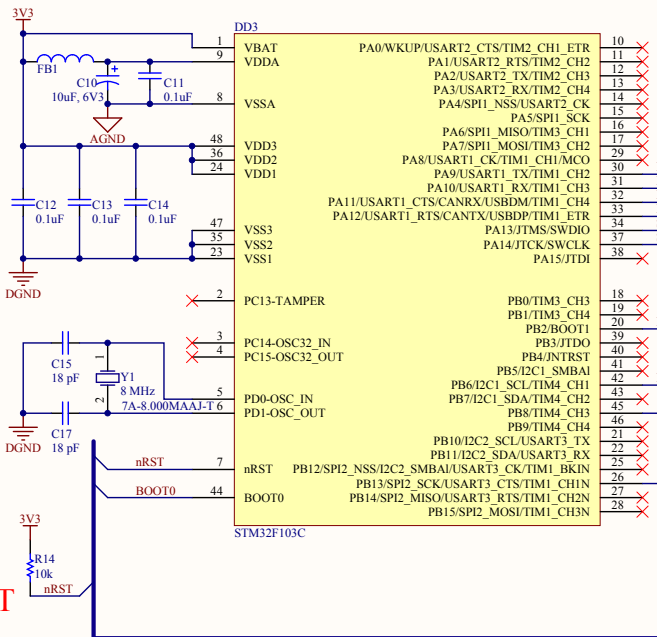
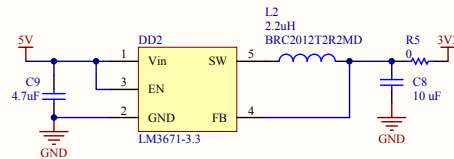


VCC -> 5V@2A max



5V -> 3V3@0.4A



RESET

UART

SWD

PWM OUTPUT

FEEDBACK INPUT

CAN/POWER INPUT

BOOT MODE SELECTION

Title		
Size	Number	Revision
A3		
Date:	07.08.2013	Sheet of
File:	C:\ALTIUM project\OpenGrab_SchDoc	Drawn By: