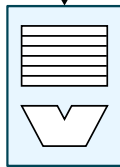
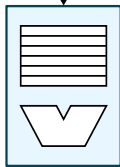
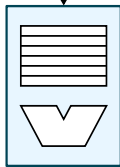
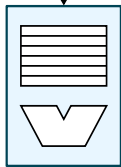


Host Interface

L1 Instruction \$
(32KB)

Instruction Memory Arbiter



Data Memory Arbiter

LLFU Arbiter

Int Mul/Div
FPU

L1 Data \$
(32KB)

Synthesizable PLL