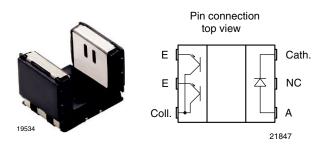
AUTOMOTIVE GRADE

GREEN (5-2008)\*



### Vishay Semiconductors

# Subminiature Dual Channel Transmissive Optical Sensor with Phototransistor Outputs



#### **DESCRIPTION**

The TCUT1300X01 is a compact transmissive sensor that includes an infrared emitter and two phototransistor detectors, located face-to-face in a surface mount package.

#### **FEATURES**

- · Package type: surface mount
- Detector type: phototransistor
- Dimensions (L x W x H in mm): 5.5 x 4 x 4
- AEC-Q101 qualified
- Gap (in mm): 3
- Aperture (in mm): 0.3
- Channel distance (center to center): 0.8 mm
- Typical output current under test: I<sub>C</sub> = 0.6 mA
- Emitter wavelength: 950 nm
- · Lead (Pb)-free soldering released
- Moisture sensitivity level (MSL): 1
- Compliant to RoHS Directive 2002/95/EC and ir accordance to WEEE 2002/96/EC



\*\* Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

#### **APPLICATIONS**

- · Automotive optical sensors
- · Accurate position sensor for encoder
- Sensor for motion, speed and direction

PRODUCT SUMMARY						
PART NUMBER	GAP WIDTH (mm)	APERTURE WIDTH (mm)	TYPICAL OUTPUT CURRENT UNDER TEST (1) (mA)	DAYLIGHT BLOCKING FILTER INTEGRATED		
TCUT1300X01	3	0.3	0.6	No		

#### Note

• Conditions like in table basic characteristics/coupler

ORDERING INFORMATION					
ORDERING CODE PACKAGING VOLUME (1) REMARKS					
TCUT1300X01	Tape and reel	MOQ: 2000 pcs, 2000 pcs/reel	Drypack, MSL 1		

#### Note

· MOQ: minimum order quantity



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
COUPLER					
Total power dissipation	T <sub>amb</sub> ≤ 95 °C	P <sub>tot</sub>	37.5	mW	
Junction temperature		T <sub>j</sub>	110	°C	
Ambient temperature range		T <sub>amb</sub>	- 40 to + 105	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 125	°C	
Soldering temperature	In accordance with fig. 16	T <sub>sd</sub>	260	°C	
INPUT (EMITTER)					
Reverse voltage		$V_{R}$	5	V	
Forward current	rd current T <sub>amb</sub> ≤ 95 °C		25	mA	
Forward surge current	current t <sub>p</sub> ≤ 10 μs		200	mA	
Power dissipation T <sub>amb</sub> ≤ 95 °C		$P_V$	37.5	mW	
OUTPUT (DETECTOR)					
Collector emitter voltage		$V_{CEO}$	20	V	
Emitter collector voltage		$V_{ECO}$	7	V	
Collector current		I <sub>C</sub>	20	mA	
Collector dark current	$T_{amb} = 85  ^{\circ}C,  V_{CE} = 5  V$	I <sub>CEO</sub>	3.3	μΑ	

#### **ABSOLUTE MAXIMUM RATINGS**

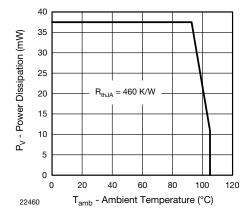


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

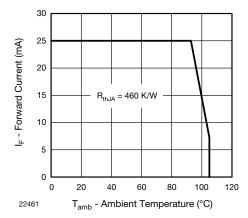


Fig. 2 - Forward Current Limit vs. Ambient Temperature



<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
COUPLER						
Collector current per channel	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 15 mA	I <sub>C</sub>	300	600		μΑ
Collector emitter saturation voltage	I <sub>F</sub> = 15 mA, I <sub>C</sub> = 0.05 mA V <sub>CEsat</sub>			0.4	V	
INPUT (EMITTER)						
Forward voltage	I <sub>F</sub> = 15 mA	V <sub>F</sub>	1	1.2	1.4	V
Reverse current	V <sub>R</sub> = 5 V	V <sub>R</sub> = 5 V			10	μΑ
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		25		pF	
OUTPUT (DETECTOR)						
Collector emitter voltage I <sub>C</sub>	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	20			V
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub> 7			V	
Collector dark current	V <sub>CE</sub> = 25 V, I <sub>F</sub> = 0 A, E = 0 lx I <sub>CEO</sub> 1		1	100	nA	
SWITCHING CHARACTERISTICS						
Rise time	$I_C$ = 0.3 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$ (see fig. 3)	t <sub>r</sub> 20		150	μs	
Fall time	$I_C$ = 0.3 mA, $V_{CE}$ = 5 V, $R_L$ = 100 $\Omega$ (see fig. 3)	t <sub>f</sub>	t <sub>f</sub> 30 150		150	μs

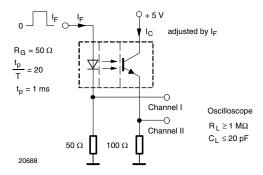


Fig. 3 - Test Circuit for  $t_{\text{r}}$  and  $t_{\text{f}}$ 

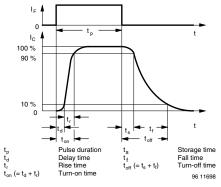


Fig. 4 - Switching Times

#### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

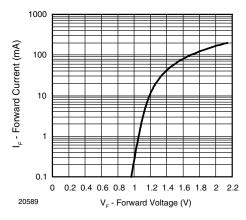


Fig. 5 - Forward Current vs. Forward Voltage

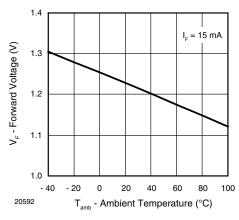


Fig. 6 - Forward Voltage vs. Ambient Temperature



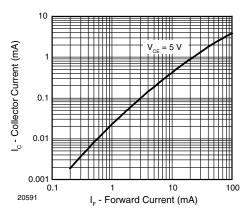


Fig. 7 - Collector Current vs. Forward Current

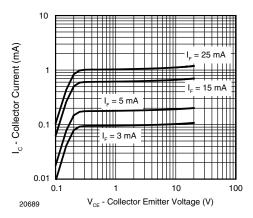


Fig. 8 - Collector Current vs. Collector Emitter Voltage

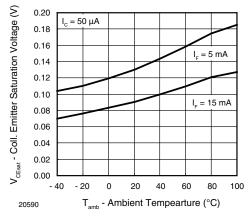


Fig. 9 - Collector Emitter Saturation Voltage vs.
Ambient Temperature

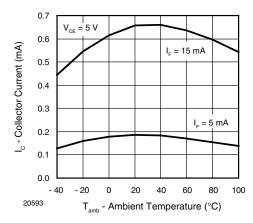


Fig. 10 - Collector Current vs. Ambient Temperature

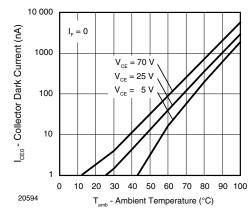


Fig. 11 - Collector Dark Current vs. Ambient Temperature

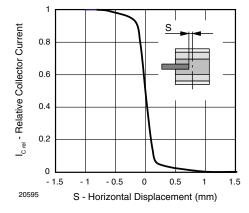


Fig. 12 - Relative Collector Current vs. Horizontal Displacement

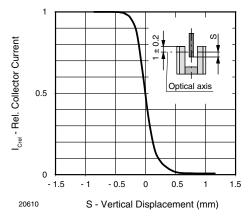


Fig. 13 - Relative Collector Current vs. Vertical Displacement

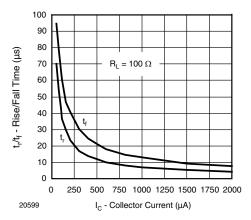


Fig. 14 - Rise/Fall Time vs. Collector Current

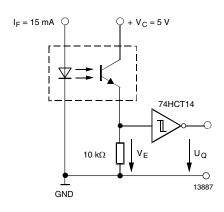


Fig. 15 - Application example

#### **REFLOW SOLDER PROFILE**

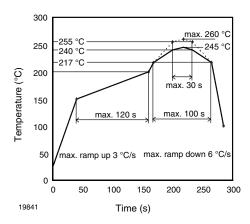


Fig. 16 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### **FLOOR LIFE**

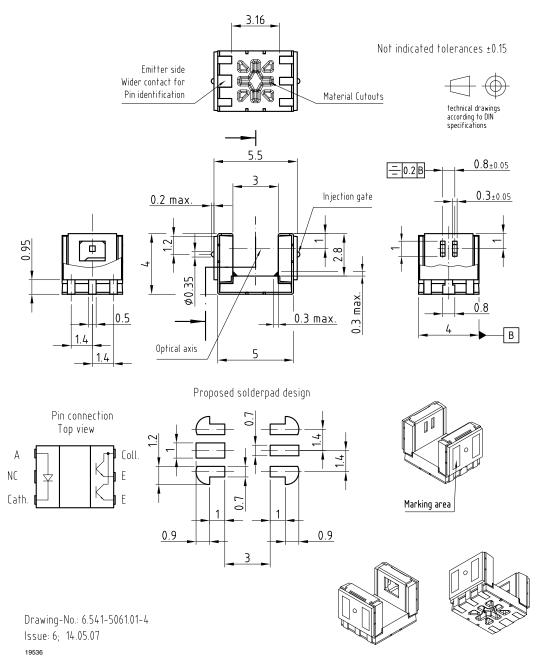
Level 1, acc. JEDEC, J-STD-020. No time limit.

RELIABILITY TESTS IN REFERENCE TO AEC-Q101 RELEASE				
TEST CONDITION		DURATION	LOT SIZE - REJECTS	
High temperature storage	T <sub>stg (max.)</sub> = 100 °C	1000 h	3 x 50 pcs - 0 pcs	
Low temperature storage	T <sub>stg (min.)</sub> = - 40 °C	1000 h	3 x 50 pcs - 0 pcs	
Temperature cycling	- 40 °C/+ 100 °C	1000 x	3 x 77 pcs - 0 pcs	
H3TRB	85 °C/85 % RH, emitters: $V_R = 4 \text{ V}$ , detectors: $V_{CEO} = 5 \text{ V}$	1000 h	3 x 77 pcs - 0 pcs	
Intermittent operational life	Emitters: $I_F$ = 80 mA DC, detectors: $V_{CE}$ = 16 V, duty cycle: 2 min on, 2 min off, $T_{amb}$ = 25 °C	1000 h (15 000 cycles)	3 x 77 pcs - 0 pcs	

RELIABILITY TESTS IN REFERENCE TO ENHANCED TEMPERATURE RELEASE ACC. AEC-Q101				
TEST	CONDITION	DURATION	LOT SIZE - REJECTS	
High temperature storage	T <sub>stg(max.)</sub> = 125 °C	1000 h	1 x 50 pcs - 0 pcs	
Temperature cycling	- 40 °C/+ 150 °C	1000 x	1 x 77 pcs - 0 pcs	
Power temperature cycle	- 25 °C/+ 85 °C, I <sub>F</sub> = 50 mA, V <sub>CE</sub> = 16 V, 2 min. on, 2 min. off	1000 h (15 000 cycles)	1 x 77 pcs - 0 pcs	

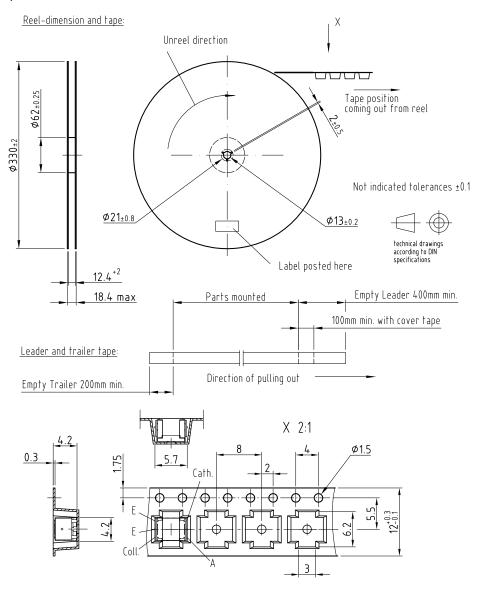


#### **PACKAGE DIMENSIONS** in millimeters



#### **PACKAGE DIMENSIONS** in millimeters

Volume/reel = 2000 pcs



Drawing-No.: 9.800-5092.01-4

Issue: 1; 14.05.07

20611



### **Packaging and Ordering Information**

PART NUMBER	MOQ (1)	PCS PER TUBE	TUBE SPEC. (FIGURE)	CONSTITUENTS (FORMS)
CNY70	4000	80	1	28
TCPT1300X01	2000	Reel	(2)	29
TCRT1000	1000	Bulk	-	26
TCRT1010	1000	Bulk	-	26
TCRT5000	4500	50	2	27
TCRT5000L	2400	48	3	27
TCST1030	5200	65	5	24
TCST1030L	2600	65	6	24
TCST1103	1020	85	4	24
TCST1202	1020	85	4	24
TCST1230	4800	60	7	24
TCST1300	1020	85	4	24
TCST2103	1020	85	4	24
TCST2202	1020	85	4	24
TCST2300	1020	85	4	24
TCST5250	4860	30	8	24
TCUT1300X01	2000	Reel	(2)	29
TCZT8020-PAER	2500	Bulk	-	22

#### Notes

#### **TUBE SPECIFICATION FIGURES**



With rubber stopper Tolerance: ±0.5mm Length: 575±1mm

Drawing-No.: 9.700-5097.01-4

Issue: 1; 25.02.00

15198

<sup>(1)</sup> MOQ: minimum order quantity

<sup>(2)</sup> Please refer to datasheets

### **Packaging and Ordering Information**

### Vishay Semiconductors Packaging and Ordering Information





Drawing-No.: 9.700-5139.01-4 Issue: 1; 10.05.00

Drawing refers to following types: TCRT 5000

15210

Fig. 2



Drawing-No.: 9.700-5178.01-4

Issue: 1; 25.02.00

15201

Fig. 3





# Packaging and Ordering Information Vishay Semiconductors



With rubber stopper Tolerance: ±0.5mm Length: 575±1mm

Drawing-No.: 9.700-5100.01-4

Issue: 1; 25.02.00

15199

15202

Fig. 4



Fig. 5

### **Packaging and Ordering Information**

### Vishay Semiconductors Packaging and Ordering Information





Drawing-No.: 9.700-5205.01-4

Issue: 1; 25.02.00

15196

Fig. 6



Drawing-No.: 9.700-5245.01-4

Issue: 1; 25.02.00 15195

Fig. 7





# Packaging and Ordering Information Vishay Semiconductors





Drawing-No.: 9.700-5222.01-4

Issue: 2; 19.11.04

20257

With stopper pins Tolerance: ±0.5mm Length: 450±1mm All dimensions in mm

Fig. 8



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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Revision: 02-Oct-12 Document Number: 91000