Introduction to Hardware Security Modules

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- 5. 'Fun' with standards

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- All information herein is already publically available.







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 - ► *Tamper reactive* hardware usually enclosed in tamper sensing membrane, with active response circuitry within.
- ► Tamper reactive hardware rare in HSMs; more common in things in adversarial environments like payments terminals.

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- Standard, sometimes modern crypto.
- ► Integration with standard APIs (OpenSSL, PKCS#11, Microsoft CNG, etc.)
- Sold to governments and industry.

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- But mainly: key management. Like:
 - ▶ Dual control: 'any 3 of these 5 people can use the key'
 - Complex key policies: 'this RSA key can only decrypt using OAEP'
 - Providing a decent way to back up key material

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- Don't do crypto any quicker (in general) than software running on a modern CPU.
- ▶ Don't fix a system using rubbish crypto (RC4, unauthenticated block cipher modes, RSA PKCS#1 encryption, MD5/SHA1 signatures, ...)

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Thank you!

Questions?

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