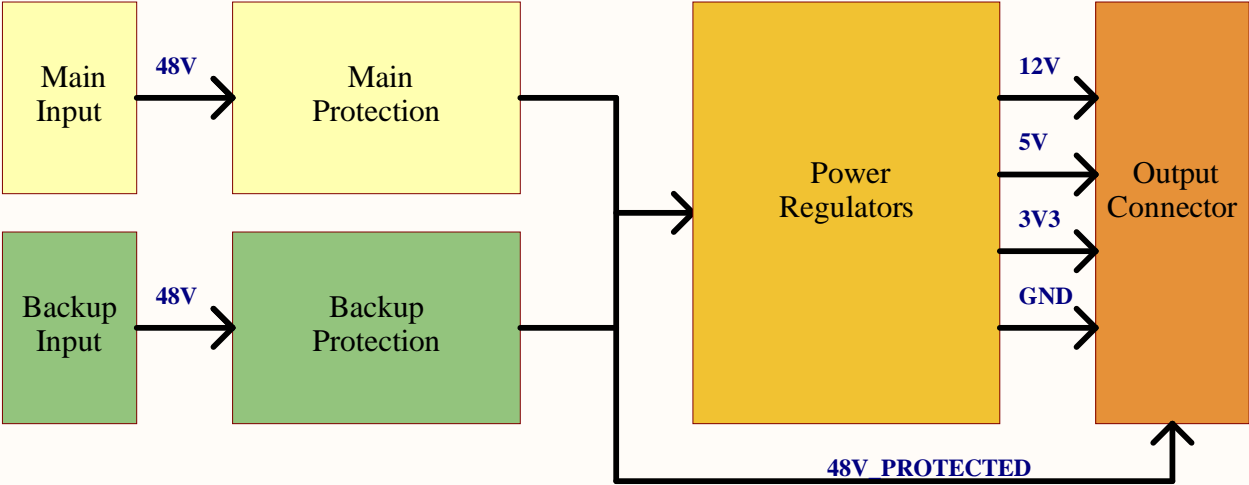
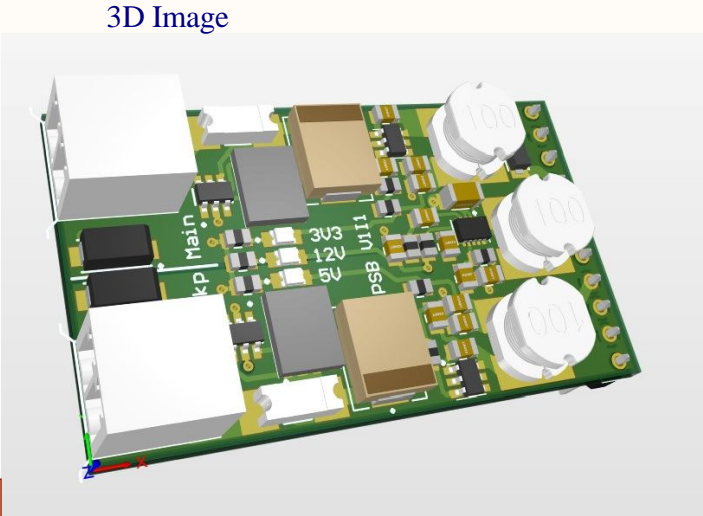


Power Supply Board (PSB)

Cover Page

13/04/2020
V1I1

Page	Index
1	Cover Page
2	Power Supply Board
3	Fabrication Layers And Assembly
4	Layer Stack Legend
5	PDN Analysis



DESIGN NOTE:
Example text for critical design notes.

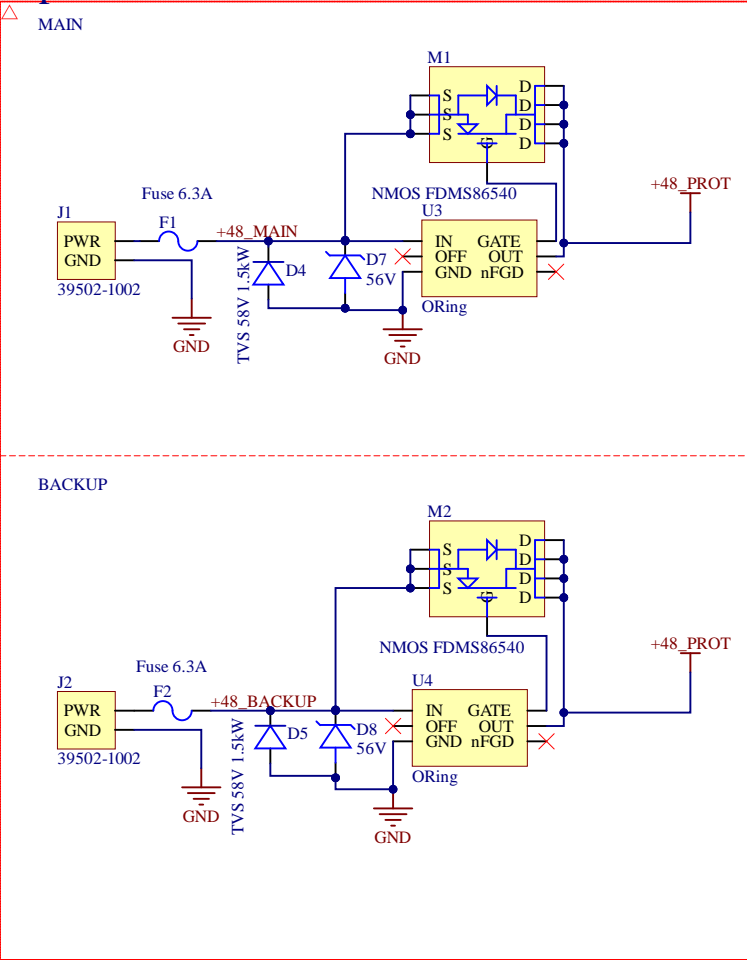
LAYOUT NOTE:
Example text for critical layout guidelines.

DRAFT

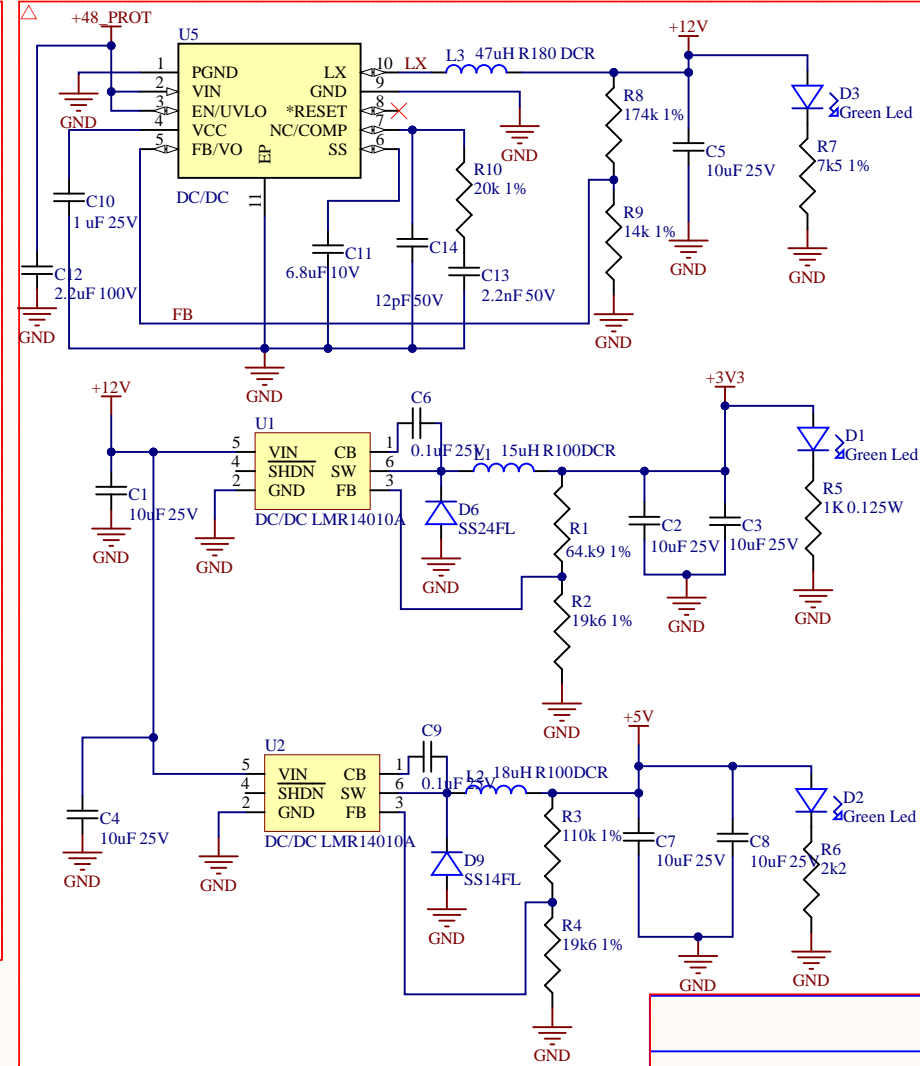
Title: Power Supply Board		Variant: [No Variations]	
Page Contents: [1] Cover Page.SchDoc		Designed By Federico De Benedetti	
Size:	DWG NO	Revision: V1I1	
Date:	13/04/2020	Sheet	1 of 2

Power Supply Board (PSB)

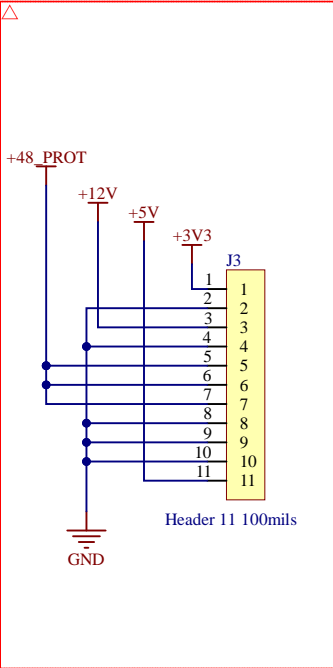
Input Protection



Low Voltage Regulators



Output Connector

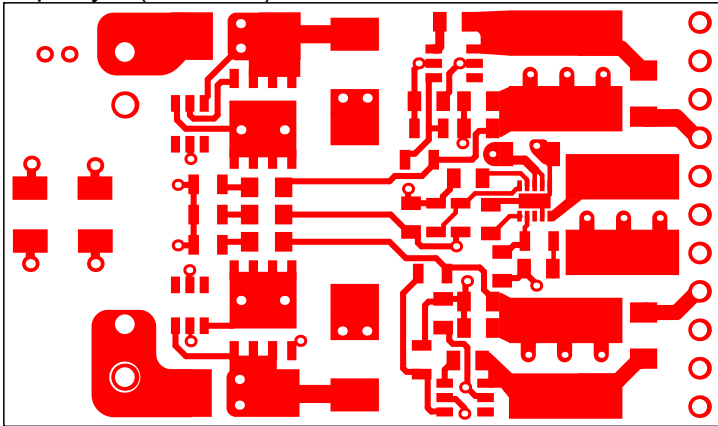


DESIGN NOTE:
Input Nominal Voltage 48V

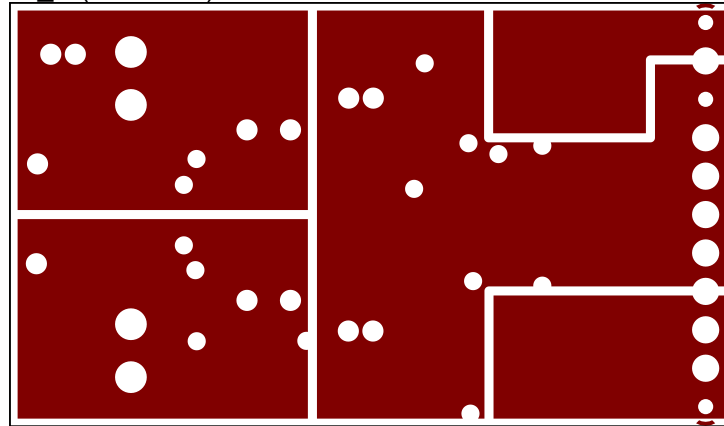
Title: Power Supply Board		Variant: [No Variations]	
Page Contents: [2] Power Supply Board.SchDoc		Designed By Federico De Benedetti	
Size:	DWG NO		Revision: VIII
Date:	13/04/2020		Sheet 2 of 2

Fabrication Layers And Assembly

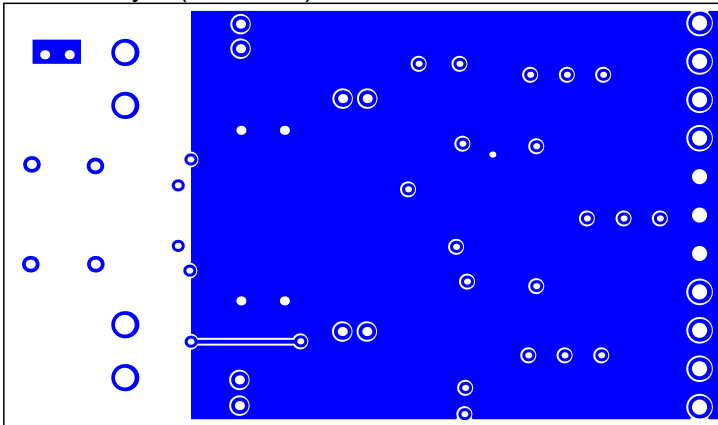
Top Layer (Scale 2:1)



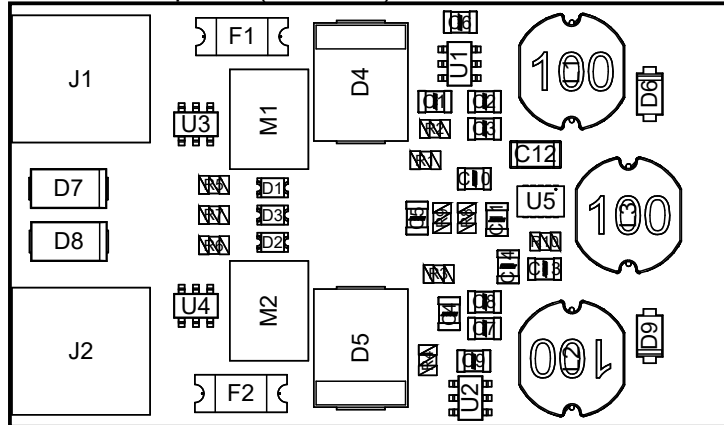
IN_B (Scale 2:1)



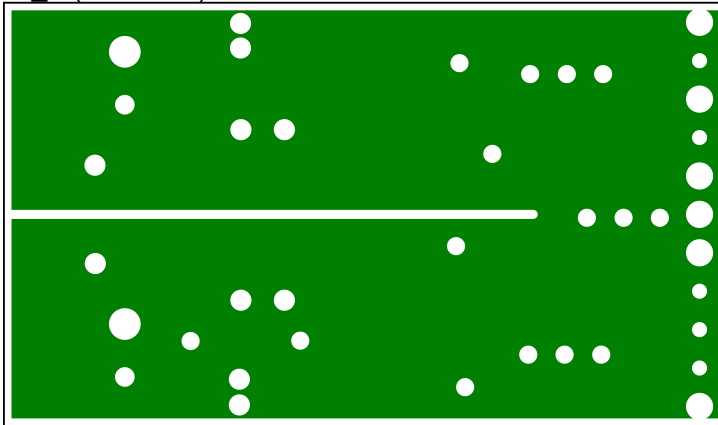
Bottom Layer (Scale 2:1)



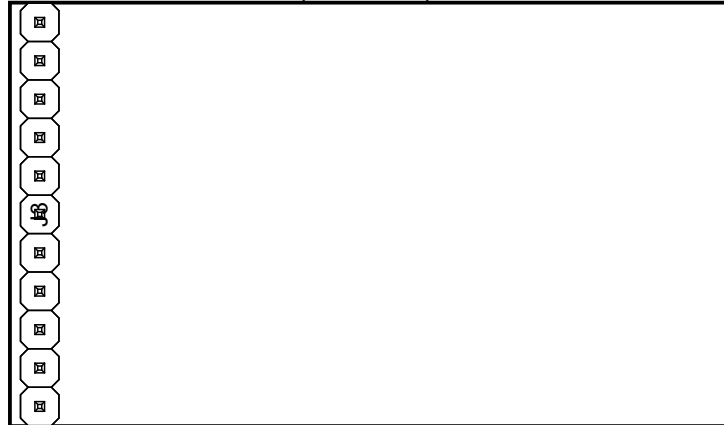
View from Top side (Scale 2:1)



IN_T (Scale 2:1)

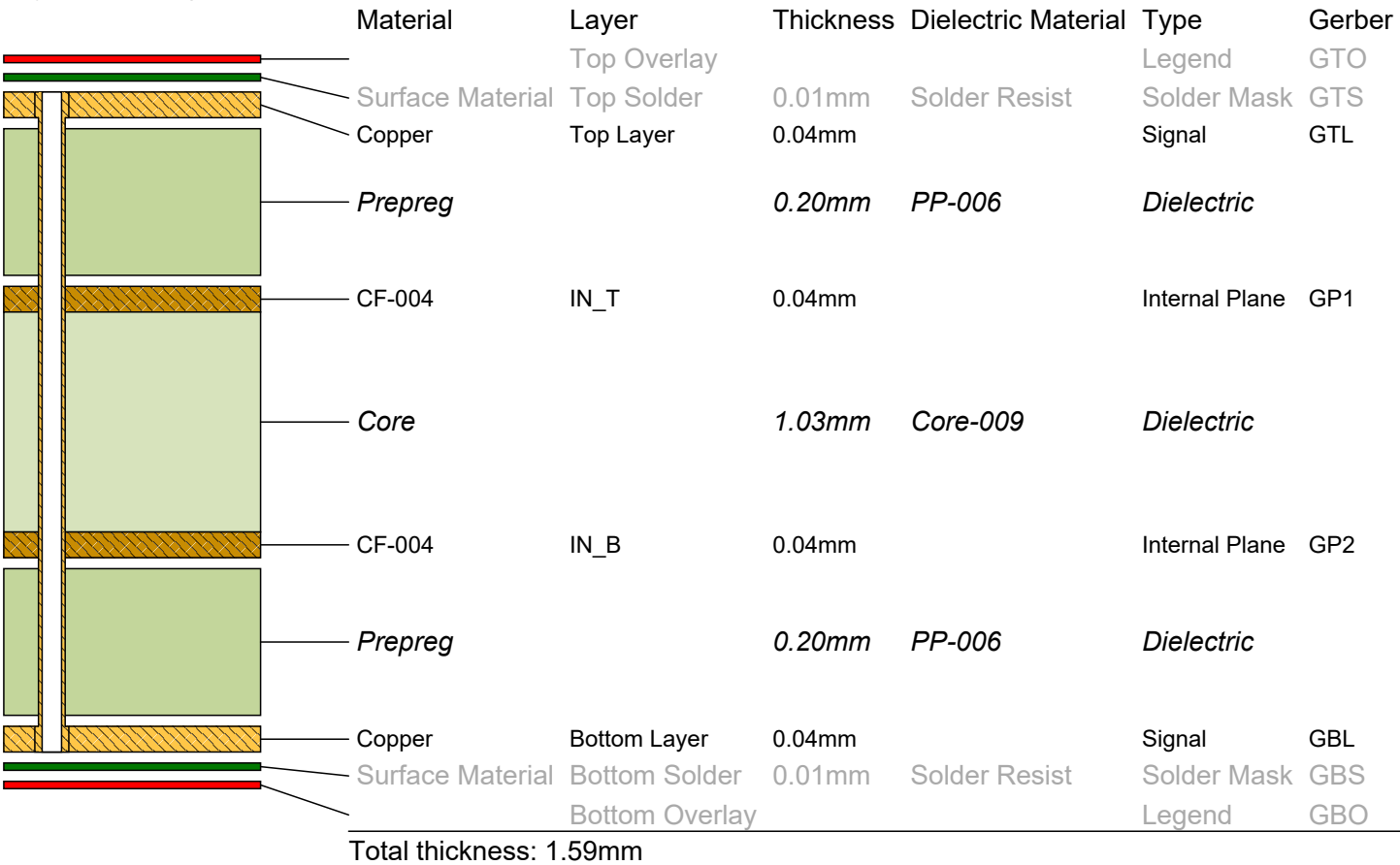


View from Bottom side (Scale 2:1)



Layer Stack Legend

Layer Stack Legend



A

B

C

D

E

PDN Analysys

1

2

3

4

A

B

C

D

E