

IBM SMS Data Application - General

There is quite a lot going on with the SMS data gathering application, though in reality it isn't all that complicated – but there is a LOT of detail. The end goal is to be able to synthesize HDL for use in programming a Field Programmable Gate Array (FPGA) to implement the machine's logic.

It will probably help to access a set of drawings, particularly from the IBM 1410, at least at first, as an aid in following this documentation. Recommended are:

- The SMS circuit card diagram volume at http://bitsavers.org/pdf/ibm/1410/drawings/1410_SMS_VOL_1.pdf
- Volume II containing the feature list, most of the card location charts and some ALD logic diagrams for the IBM 1410 at http://bitsavers.org/pdf/ibm/1410/drawings/1410_SYSTEM_VOL_II.pdf

For the database structure, the root directory for the project has some things worth looking at – especially the data model for the database. Also, one can use the MySQL workbench to look at the documentation for individual data elements. There is a very simple database isolation layer, found in sub-project MySQLFramework. The actual application is in sub-project IBM1410SMS. I wrote this because I found using an actual database entity framework created much more language/system/database dependency than I was comfortable with. Although the application was written to MySQL it should not be *too* hard to change it to use a different database.

C# Data entity object(s) for a given table or tables have to be re-created anytime a change is made to the table(s) involved. That is handled by the script SQLToEntity.pl found in the Tools folder of the IBM1410SMS project. This script writes the C# code for an entity into the Entities sub-folder.

The way I have used the application to date has been:

1. I used the information from the card location charts (most, but not all, of which are in Volume II, as referenced above) to build spreadsheets to use to pre-populate the card location information in the database.
2. I used the information from the SMS card volumes (there are two, one of which is referenced above) to analyze the circuits used for the cards that were identified during step 1.
3. Having completed the first two steps, I worked in parallel on the application code to edit information and to import information from the spreadsheets. This took place during the first few months of 2018.
4. Starting in May, 2018, I started processing the actual ALD logic diagrams and entering data into the database.

Database Structure

The basic structure of the database, while relational, can be thought of as a few important hierarchies (naturally, this is nowhere near complete.)

- Machine (e.g., the 1411 CPU for the IBM 1410)
 - Feature
 - Frame
 - Machine Gate (The IBM 1410 really doesn't have these)
 - Panel
 - Card Slot
- Volume Set
 - Volume
 - Page
 - Card Location Page
 - (Connection Location Page – not yet implemented)
 - Diagram Page
 - Tie Down Page
 - ALD Page (a Diagram Page with Diagram Blocks)
- Card Location Page
 - Card Location Block
 - Card Type
- SMS Card Type
 - Logic Family (e.g., SDRTL)
 - Card Gate
 - Logic Functions (IBM+, IBM-, "Standard")
 - Logic Voltage Levels
 - Gate Pin
- ALD Diagram Page
 - Diagram Block
 - Connection
 - DOT Function
 - Tie Down

Application Dialogs

Machines

At the top of the heap, we have machines. A machine is not intended to be an entire computer. IBM subdivided computers (like the IBM 1410) into a series of machines (1411 – the CPU, 1415 – the console, 1414 – the I/O synchronizer, and so on). In addition, the application assumes a machine called SMS exists, to which it will attach SMS card ECOs.

The machine form, like many of the forms in this application, uses a .NET Data Grid View control. This control is not associated with the database directly. Instead, it is associated internally with a List or BindingList which absorbs any updates or deletes made in the rows displayed.

Like most of the forms in the application, any updates, including deletes, do not actually occur until you click the Apply button. (Attempts to delete rows, however, are checked for referential integrity. These checks are done in the application, not the database).

A new machine can be defined by entering the data in the new row that has “*” in the left hand column.

NOTE: In some of the data grid views, one has to click F2 to edit a field. I have not yet looked into exactly why one sometimes has to do that.

This page is accessed via Edit => Machines

	Machine	Description
▶	1411	IBM 1410 CPU
▶	1415	IBM 1410 Console
	SMS	
*		

Frames, (Machine) Gates and Panels

In the SMS system, machines comprise one or more Frames, which comprise one or more Gates, which comprise one or more Panels, and panels are made of up card slots. Some machines (like the IBM 1410 machines) don't actually use gates. In that case, in the application, one just creates a gate with the same name as the frame in the application.

Each panel entry defines the card slots available in that panel. In SMS, IBM used a coordinate system to define slots, where a letter was used for the row (skipping I and O), and a number for the card column.

These pages are accessed via:

- Edit => Frames, and selecting the appropriate machine
- Edit => Machine Gates, and selecting the appropriate machine and frame
- Edit => Panels, and selecting the appropriate machine, frame and gate
-

EditFramesForm

Select machine: 1411

	Name
	B
	C
	D
	F
*	

Apply Cancel


EditMachineGatesForm

Select machine: 1411

Then Select Frame: B

	Name
	B
	C
	D
	F
*	

Apply Cancel

 Edit Panels

Select machine:

1411

▼

Then Select Frame:

C

▼

Then Select Gate:

C

▼

	panel	minRow	maxRow	minCol	maxCol
	1	A	Z	1	28
	2	A	Z	1	28
	3	A	V	1	28
	4	A	V	1	28
	7	L	L	51	51
*					

Apply

Cancel

Features

An IBM Machine might have installed, whether at the factory or via an RPQ (Request for Price Quotation) a number of different features. For example, the IBM 1410 might or might not have the accelerator feature, or be configured with various amounts of core storage.

The Features dialog allows editing the features for a particular machine type. These are then used by card location chart pages to identify cases where a particular card location is present or used if a machine has a given feature, for use during troubleshooting or potentially while installing a particular feature.

This page is accessed via the menu entry Edit => Features and then selecting the desired machine type.

*Note: Currently the feature table for the IBM 1410 has some duplicate entries, of a sort, most of them starting with '\$' or 'S'. One set was the result of features on a feature page, where some features, e.g. \$40 used a '\$' instead of an 'S'. Other features were added automagically during import of card location chart entries, which use 'S'. These may eventually need to be reconciled. In the **meantime do NOT use the '\$' entries** when editing the card location chart or gates.*

Use THESE entries, that start with 'S':

EditFeaturesForm

Select machine: 1411

Code	Feature
P1	THOSE ADDITIONS TO BASIC MACHINE REQUIRED FOR THE PAPER TAPE PUNCH ATTACHMENT TO CHANNEL
P2	THOSE ADDITIONS TO L2 REQUIRED FOR THE PAPER TAPE PUNCH ATTACHMENT TO CHANNEL TWO
Y1	THOSE ADDITIONS TO THE BASIC MACHINE REQUIRED FOR THE ATTACHMENT OF THE PRIORITY FEATURE T
S2	Added Automatically by Card Location Chart Import
S10	Added Automatically by Card Location Chart Import
NOTE	Added Automatically by Card Location Chart Import
F11	Added Automatically by Card Location Chart Import
S40	Added Automatically by Card Location Chart Import
S20	Added Automatically by Card Location Chart Import
S60	Added Automatically by Card Location Chart Import
S4	Added Automatically by Card Location Chart Import
S6	Added Automatically by Card Location Chart Import
AUTS	Automatic Start
O0	Portable CE Start/Stop Box (Not listed in feature list, but appears in ALD)
C0	Referred to in CH 2 Not Ready/Busy, page 13.66.05.1 Should maybe be L2?
*	

Apply Cancel

Do NOT use these entries, that begin with '\$'

EditFeaturesForm

Select machine: 1411

Code	Feature
**	HARDWARE REQUIRED IN THE BASIC SYSTEM
\$10	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 10K STORAGE ONLY
\$2	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 20K STORAGE OR GREATER
\$20	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 20K STORAGE ONLY
\$4	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 40K STORAGE OR GREATER
\$40	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 40K STORAGE ONLY
\$6	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 60K STORAGE OR GREATER
\$60	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 60K STORAGE ONLY
\$8	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 80K STORAGE OR GREATER
\$80	THOSE ADDITIONS TO THE BASIC SYSTEM REQUIRED TO IMPLEMENT 80K STORAGE ONLY
Z0	THOSE ADDITIONS TO THE BASIC MACHINE REQUIRED TO IMPLEMENT COMPATIBILITY WITH THE 1401 MACH
Z1	THOSE ADDITIONS TO Z0 REQUIRED TO IMPLEMENT COMPATIBILITY WITH THE 1401 WITH 16K STORAGE.
L1	THOSE ADDITIONS REQUIRED TO PERFORM OVERLAP ON THE FIRST CHANNEL
L10	THOSE ADDITIONS REQUIRED TO PERFORM OVERLAP WITH 10K STORAGE ONLY
L2	THOSE ADDITIONS REQUIRED TO ADD A TAPE AND/OR FILE SYSTEM TO THE SECOND CHANNEL OF THE CPL
B1	THE BASIC I/O BUFFER, INCLUDING THOSE ADDITIONS TO THE BASIC MACHINE (. .) REQUIRED TO IMPLEMEN

Apply Cancel

ECOs

Many artifacts can reference Engineering Change Orders, or ECOs. These can either relate to a particular machine type (e.g. 1411, for the IBM 1410 CPU) or to the application-defined machine type “SMS”, used to capture ECO levels of SMS card drawing pages.

ECOs are accessed from the menu via Edit => ECOs, and then selecting the desired machine type or “SMS”.

When an ECO is entered while editing an Automated Logic Diagram (ALD) page, it is automatically entered into the ECO table. The description can be changed later, if desired.

Currently the application does not really use the ECO, though in theory one could have multiple ALD pages with the same page number, but different ECO levels if the application allowed for that. The database does make allowance for this eventual possibility by containing the ECO in the cardLocationPage and the diagramECOtag tables, but more work would be necessary, including identifying the “defining” (probably latest) ECO in the diagramPage table, which it does not currently contain. Then one could associate particular pages with particular ECOs into a given volume – and then use those throughout the application.

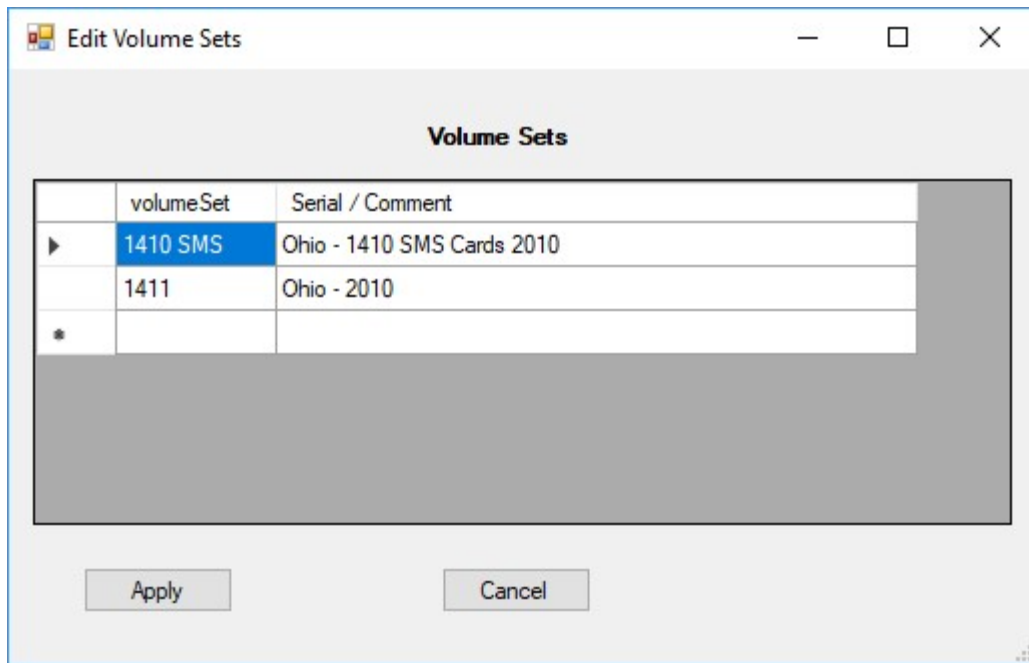
Alternatively, for now, one would need to “clone” the database and then alter it for a different machine. Definitely a shortcoming in the application.

Note: The database for machine 1411 currently contains an ECO with a blank ECO number that I should probably investigate (see below)

The screenshot shows a window titled "EditECOsForm" with a "Select machine:" dropdown menu set to "1411". Below the menu is a table with two columns: "ECO" and "description". The table contains 17 rows of data. The first row has a red exclamation mark icon in the "ECO" column. The "ECO" column contains values: 0250786, 0253453, 109306, 109333, 109353, 109370C, 109370D, 109370E, 109370F, 109370G, 109370H, 109370J, 109370L, 109372, and 109386. The "description" column contains the text "Added by ALD Diagram Page Form" for all rows except the first two. At the bottom of the window are "Apply" and "Cancel" buttons.

ECO	description
0250786	
0253453	
109306	Added by ALD Diagram Page Form
109333	Added by ALD Diagram Page Form
109353	Added by ALD Diagram Page Form
109370C	Added by ALD Diagram Page Form
109370D	Added by ALD Diagram Page Form
109370E	Added by ALD Diagram Page Form
109370F	Added by ALD Diagram Page Form
109370G	Added by ALD Diagram Page Form
109370H	Added by ALD Diagram Page Form
109370J	Added by ALD Diagram Page Form
109370L	Added by ALD Diagram Page Form
109372	Added by ALD Diagram Page Form
109386	Added by ALD Diagram Page Form

Volume Sets



Volume Sets are also at the top of the heap of the paper trail. A volume set is an associated set of volumes (which in turn contain diagram pages) – typically associated with a given machine type and/or serial number, or, perhaps, a given source for the materials (which is the case in this example). The association in the application is completely arbitrary. Volume sets and volumes have no association with a “machine” as described above.

This screen is accessed via Edit => Volume Sets

Volumes

The screenshot shows a window titled "Edit Volumes" with a standard Windows interface (minimize, maximize, close buttons). At the top, there is a label "Select Volume Set:" followed by a dropdown menu displaying "Machine Type: 1411 S/N: Ohio - 2010 ID: 100006". Below this is a table with the following columns: "Vol. Name", "Order", "Machine S/N", "First Page", and "Last Page". The table contains 14 rows of data, including "SMS", "Unassigned Sheet", and volumes I through XI, each with a specific order and page range. The last two rows are "Unassigned" (Order 12) and "TIEDOWN" (Order 13). To the right of the table is a vertical scrollbar. At the bottom of the window are two buttons: "Apply" and "Cancel".

Vol. Name	Order	Machine S/N	First Page	Last Page
SMS	0			
Unassigned Sheet	0			
I	1	SN03950001	98.11.02.0	98.16.02.0
II	2	SN03950001	11.04.00.0	12.12.67.1
III	3	SN03950001	12.13.01.1	13.14.14.1
IV	4	SN03950001	13.42.10.1	14.13.05.1
V	5	SN03950001	14.14.01.1	14.42.05.1
VI	6	SN03950001	14.45.01.1	15.41.12.1
VII	7	SN03950001	15.49.01.1	15.70.33.0
VIII	8	SN03950001	16.11.01.1	17.12.05.1
IX	9	SN03950001	17.13.01.1	36.10.04.1
X	10	SN03950001	36.11.0.1	45.50.16.1
XI	11	SN03950001	25.04.03.0	25.97.01.0
Unassigned	12	SN03950001		
TIEDOWN	13	SN03950001		

A volume is intended to represent a particular multi-ring volume binder within a given volume set. These would be associated with the classic dark blue IBM binders that could be found in a cart in the machine room or nearby, for CE's to use when diagnosing problems with the computer. In the application, the volumes are associated with a given volume set. A volume can be associated with a given machine serial number – but this is just information – it is not used by the application. (The ID:##### displays the internal database key for the volume set, for informational purposes).

Volumes can also be identified for SMS card type entries, as these are derived from pages in their own separate IBM "blue binders".

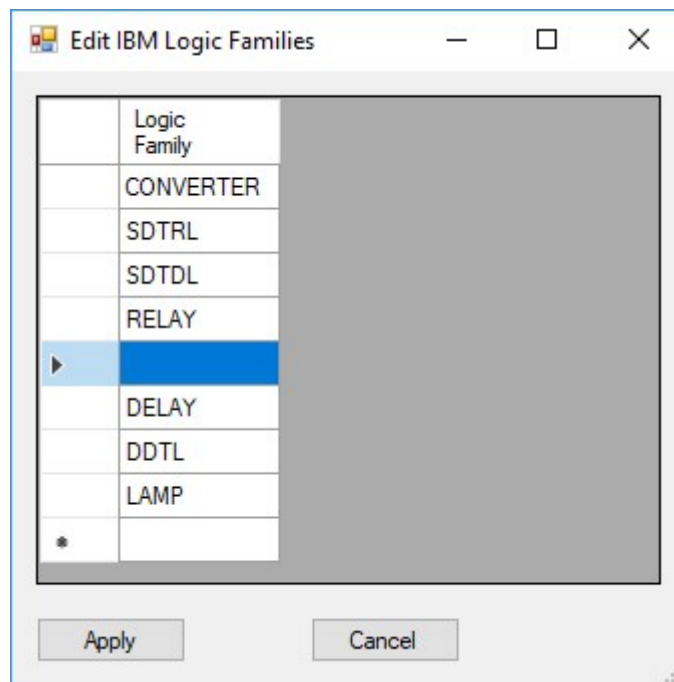
This screen is accessed via Edit => Volumes, and then selecting the appropriate Volume Set in the drop down list.

Logic

Logic levels and operations in the application are defined by the Logic section of the menu, which contains entries for editing logic families, logic voltage levels, IBM logic functions, and “standard” logic functions.

Logic Families

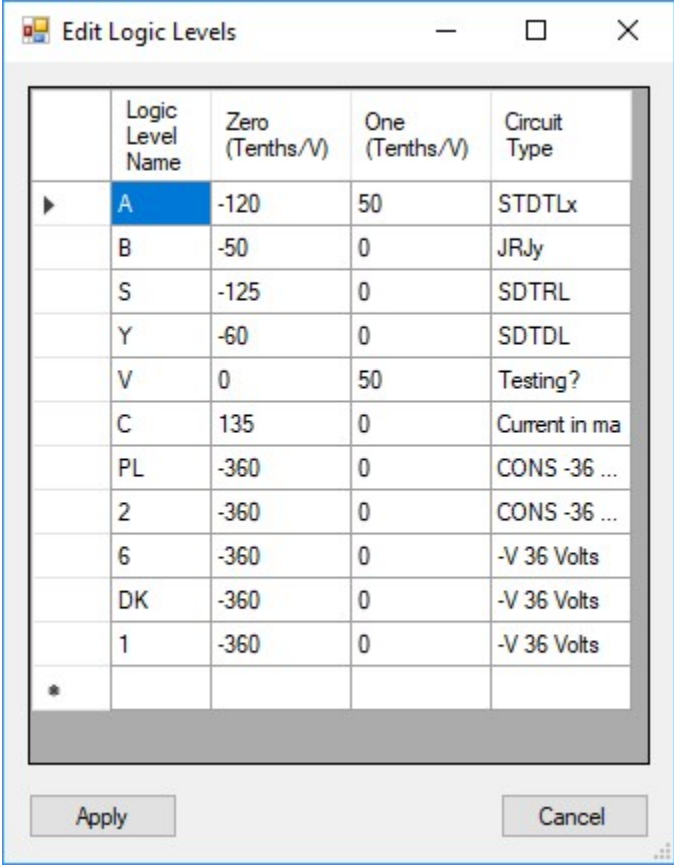
IBM defined several logic families for use in SMS. The IBM 1410, for which I built this application, uses only a subset of them. Logic families are used when defining SMS card types (described later) – a given card is presumed to use only one of the logic families. This screen shot of the Logic Family editor, accessed by Logic => IBM Logic Families, show some of them. (Not sure why the blank logic family is in there, possibly an artifact of importing a card type that did not have a family specified). The logic families are for documentation only, and not used for anything else (in particular, they are not used for HDL generation).



Logic Voltage Levels

The various IBM logic families used (usually different) specified voltage levels to represent 0 or 1. These can be different for an IBM SMS Automated Logic Diagram logic block input and output, and are represented most often by a single letter, or left blank in the logic block. For example, the most typical level in the IBM 1410 is "S" (logic family SDTRL), where +S is ideally 0v and -S is ideally -5 v. The values in the logic level table, accessed via Logic => Logic Voltage Levels *are specified in tenths of a volt*. As with logic families, the voltage levels are not really used in the application. Most of the levels in the screen shot below were obtained from the manual IBM Customer Engineering, Instruction Reference, 1410 System Fundamentals, S223-2589, page 77. I used levels where +S, for example, is presumed to be logic '1' and -S is presumed to be logic '0'.

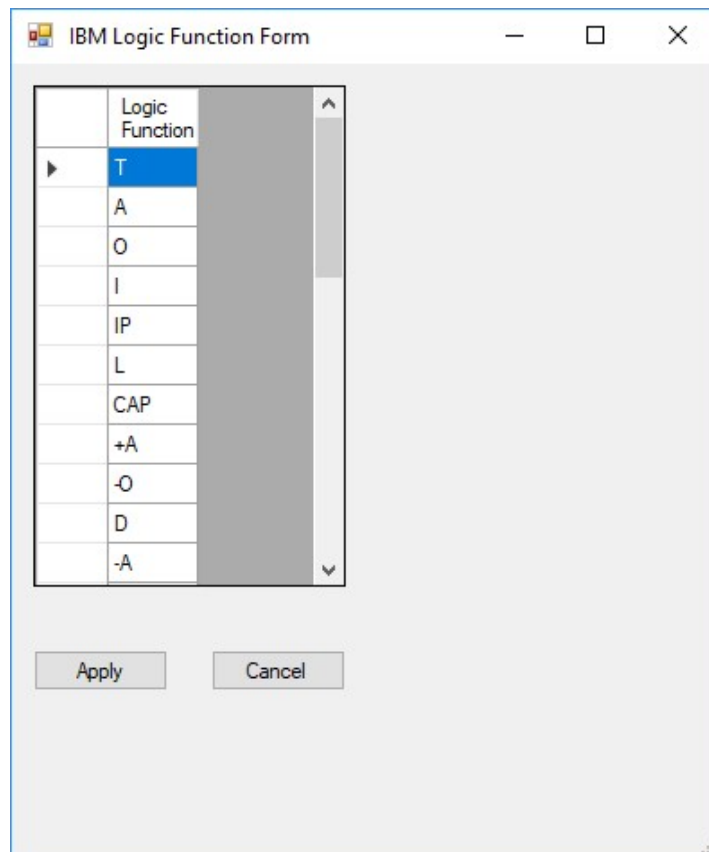
This is a screenshot of the Logic Levels dialog, accessed by Logic => Logic Voltage Levels in the menu.



	Logic Level Name	Zero (Tenths/V)	One (Tenths/V)	Circuit Type
▶	A	-120	50	STDTLx
	B	-50	0	JRJy
	S	-125	0	SDTRL
	Y	-60	0	SDTDL
	V	0	50	Testing?
	C	135	0	Current in ma
	PL	-360	0	CONS -36 ...
	2	-360	0	CONS -36 ...
	6	-360	0	-V 36 Volts
	DK	-360	0	-V 36 Volts
	1	-360	0	-V 36 Volts
*				

IBM Logic Functions

At the top of each ALD logic block, there is a logic function symbol, for example +A or -O or IP, and so on. These represent logic functions *as IBM defined them*, representing “positive logic” and “negative logic”. But, care is needed when interpreting the diagrams, because it seems that **built into many (but not all) of these names is an assumption that the output of the gate is inverted**. For example, SDTRL gates are often NAND in terms of $-S == 0$ and $+S == 1$. But they show up in the ALD diagrams as +A (or, equivalently, -O). As with families and voltage levels the application does *not* use these IBM logic functions. They are for documentation only. One thing worthy of note: if the IBM logic function has an extra “A” or “O” at the end, one will usually find that the output of the gate goes to a “DOT Function”. Whether a DOT function performs an actual AND or an OR depends on how the circuits are constructed, and may differ from the logic implied by that trailing letter. For example, SDTRL “DOT Functions” operate as “OR” gates, using the logic values of $+S == 0V$ and $-S == -12V$, because SDTRL uses PNP transistors, and when they are on, they pull the voltage to 0. So turning any one transistor on in the gates feeding the DOT function will pull the voltage to 0 – a logical OR. There are also lots of special IBM logic functions: D (driver), DLY (delay), R (resistor), CAP (capacitor), IP (power inverter), L (load – usually a diode clamp) and so on. NOTE: Currently this table is NOT used to validate the logic function entered on a logic diagram – it is actually a free-form text field. Access this dialog via Logic => IBM Logic Functions.

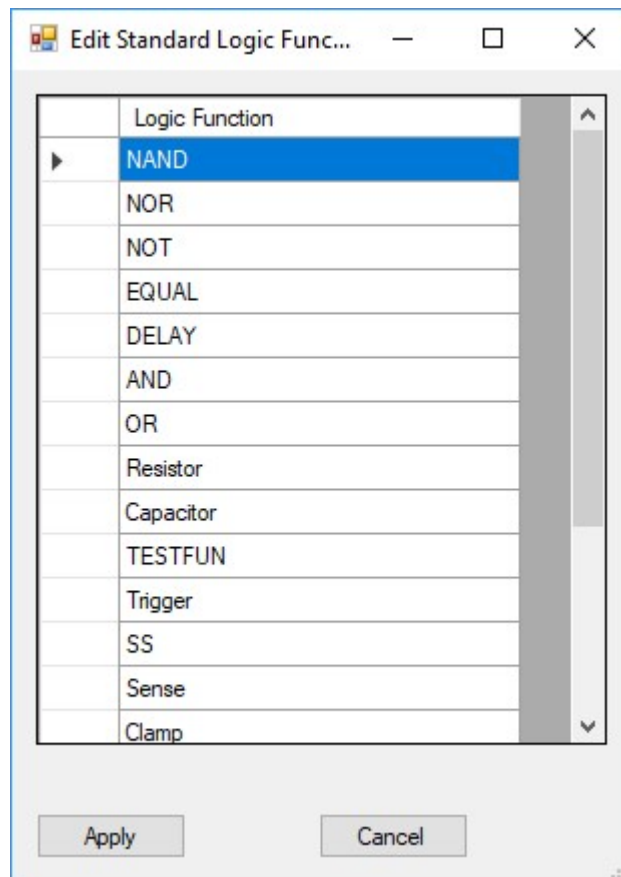


Logic Function
T
A
O
I
IP
L
CAP
+A
-O
D
-A

Apply Cancel

“Standard” Logic Functions

Because at some point the plan is to generate a representation of a machine from the ALD diagrams, I defined another logic function that I call the “Standard Logic Function”. These are the more familiar kinds of logic: NAND, NOR, NOT as well as a plethora of special ones (DELAY, EQUAL (for driver circuits that don’t invert), Resistor, Capacitor, Trigger (a flip flop), Sense (amplifier), OSC (oscillator), ONE (always logic ‘1’), ZERO (always logic ‘0’), Switch, “special” (which would require specific code in the application to generate HDL) and so on. These ARE used by the application. When an ALD logic block is defined to the application, the application requires the selection of a “Gate” (a circuit card gate, not to be confused with the machine gate, described earlier) which is associated with the card type and the particular pins used for that logic block by IBM. Each Gate, in turn, has associated with it one of the Standard Logic Functions (which is visible when the gate is selected in the application). The circuit card gate definition (described later) requires selection of one of these Standard Logic Functions when it is edited. The Standard Logic Function for a particular circuit card gate is determined by an analysis of the circuit in the card, for each gate on the card. This page is accessed via the menu Logic => Standard Logic Functions.



SMS Card Types

The information on SMS cards used by the IBM 1410 are handled specially in the application, rather than as pages, but are, in fact, derived from pages in the typical IBM blue volumes. There are three types of entities involving SMS cards: SMS Card Types, Therefore, an SMS card type is assigned a Volume First among these are the SMS card types, SMS card gates and card pins.

SMS card types are edited via the menu SMS Cards => Edit SMS Card Types, selecting the particular SMS volume containing the card type of interest and then selecting the card type itself.

This page also allows one to create a new SMS card type in a given volume by clicking on “New Card Type” or correcting/assigning a card type to a different volume using the “Changed Diagram Volume” pull down.

Each card type has a 3 or 4 character type (you can decide whether or not to include the trailing “-” in a card type, though I did not do so for the IBM 1410.) and an IBM part number.

The SMS Card Type entity identifies the volume in which the page describing the particular SMS card type appears, along with the logic family for the card, and various meta-data that appear on the SMS card type page. The Card Type itself is central to the application. The part number is used in pull-down lists and the like. The rest of the meta-data are not used by the application.

Edit SMS Card Type

Diagram Volume: Vol. Set 1410 SMS, Vol. II

Card (Existing): DMQ, Part No. 0372125 Part# 0372125 Type: DMQ New Card Type

Logic Family: DDTL Name: DDTL 1-6 WAY NL-1-2 WAY NL 2 LDS 390 OHM

NOTES

Note Name	Note
*	

CHANGED Diagram Volume: Vol. Set 1410 SMS, Vol. II

Card Height
☒ Single ☐ Double

APPROVAL: Date: 1/ 1/1960 Hole Pattern:

Type (Name): CARD ASM TSTR Dev. No.

Design: Date: 1/ 1/1960 Type: SMS Device:

Detail: Date: 1/ 1/1960 Scale: NONE

Check: Date: 1/ 1/1960 Draw: 1/ 1/1960

Appro: Date: 1/ 1/1960 Check: 1/ 1/1960

Apply Delete Cancel

ECOs

ECO	Date	Note Name	Approver
▶ 114262			
*			

SMS Card Gates

SMS card type gates (not to be confused with machine gates) is where “the rubber meets the road” in this application.

A given SMS card type may have one or more logic gates / flip flops / etc. These are determined by essentially reverse engineering the information on the SMS card page.

These are accessed in the application from the Menu via SMS Cards => Edit SMS Card Gates, selecting the appropriate SMS volume and then the desired Card Type.

The first column is an arbitrary gate number, usually assigned left to right, top to bottom.

The next column is “Pin”. In the application, each SMS card gate is said to have a “defining pin”. While this is somewhat arbitrary, it must be unique among the defining pins used in a given SMS card gate, and appears in the “Pin” column in the Edit Card Gates dialog.

The third column is “T#”, which stand for Transistor Number. It is not required by the application, but can be entered to avoid confusion in gate identification. Just pick one of the transistor numbers listed on the SMS card circuit diagram.

The next column identifies the “positive” logic function, as used on IBM ALD diagram pages. See “IBM Logic Functions” above. This is not actually used by the application, at least at present.

The next column is the “negative” logic function, as might appear on IBM ALD diagram pages. If the positive logic function is “+A”, then this will be “-O”, and if the positive logic function is “+O” then this will be “+A”. In other cases, like “I” (for inverter) or “DE” (for delay) it will be the same as the “positive” logic function. It is not actually used in the application.

The next column, “Logic Function: *is of critical importance to the application*”. This column is intended to be used during the process of HDL generation. It is the actual *physical* logic function performed by the gate, determined by reverse-engineering the SMS diagram page. See “Standard Logic Functions” above.

The Latch column is used to define cases where one gate is used in combination with another gate (via a cross connection) to form a latch. For gates that are not part of a Latch this will be a 0.

The next column defines whether or not the gate’s output(s) is open collector. Only at most ONE gate that is NOT open collector can participate in a “DOT function” on an ALD diagram page.

The next two columns define the gate’s *default* input and output logic levels (see Logic Voltage Levels, above). These can be overridden, however, on a given ALD logic diagram page.

Next is an optional “HDL Name”. It is used to identify a particular block of code in the HDL generation process for gates that cannot be readily defined by just the “Logic Fun.” column. For example, on gate type AEK this column contains “SMS_AEK” for the HDL Name. (AEK is a power inverter, but with some special logic which is not readily defined as a standard logic function.)

Finally there is the Comp. (component) value column. Some “gates” are really just single electronic components like load resistors or capacitors. This column identifies the value of the component (ohms, microfarads, etc.).

Edit Card Gates

Volume: Vol. Set 1410 SMS, Vol. I

Card Type: DGR, Part No.0370258

Gates

Gate #	Pin	T#	IBM+	IBM-	Logic Fun.	Latch	Open Coll.	In Lvl	Out Lvl	HDL Name	Comp. Value
1	A. 2		I	I	NOT	0	<input checked="" type="checkbox"/>	S	S		
2	H 7		L	L		0	<input type="checkbox"/>	S	S		750
3	L. 9		I	I	NOT	0	<input checked="" type="checkbox"/>	S	S		
4	G 5		L	L		0	<input type="checkbox"/>	S	S		750
5	K. 4		I	I	NOT	0	<input checked="" type="checkbox"/>	S	S		
6	E. 37		L	L		0	<input type="checkbox"/>	S	S		750
7	Q 6		I	I	NOT	0	<input type="checkbox"/>	S	S		
8	P. 7		I	I	NOT	0	<input type="checkbox"/>	S	S		

Apply Cancel

Edit Card Gates

Volume: Vol. Set 1410 SMS, Vol. I

Card Type: AEK, Part No.0370215

Gates

Gate #	Pin	T#	IBM+	IBM-	Logic Fun.	Latch	Open Coll.	In Lvl	Out Lvl	HDL Name	Comp. Value
1	D 0		IP	IP	Special	0	<input type="checkbox"/>	S	S	SMS_AEK	
2	F. 0		IP	IP	Special	0	<input type="checkbox"/>	S	S	SMS_AEK	
3	P. 0		IP	IP	Special	0	<input type="checkbox"/>	S	S	SMS_AEK	
*							<input type="checkbox"/>				

Apply Cancel

Each card gate connects to one or more pins. These pins are accessed from the menu via SMS Cards => Edit SMS Card Pins and then selecting the SMS Card Volume, Card Type and Gate (by number).

The first column is the Pin. This will usually be a letter, corresponding to an actual pinout on the card edge of the SMS card. However, there are also internal gate-to-gate connections that do not go to the card edge. The "Pin" column for these are generally left blank.

The next two columns identify gate to gate connections. If the "Pin" column is blank, then at least one of these should identify a gate from which the signal is derived (Input Gate) or to which the signal is sent (Output Gate). Note that it is also possible to have both a card edge pin identification and a gate-to-gate connection – but generally that should not be done, favoring instead using a common Pin in such cases.

The next two columns identify whether a given pin is used as an Input, an Output or both.

The next two columns identify whether a given pin is "DOT connected" (DOT-ed And/Or) with another gate. Dot Out indicates an output pin is connected to another gate's output on the same card. Dot In indicates that an given input pin is connected to multiple gates' outputs on the same card.

The "Map To" column is special. For pins that are part of a gate with logic function "Special" (and perhaps others down the road), this specifies the corresponding HDL entity port/parameter name for this pin to be used when generating HDL.

The Voltage (Tenths) column indicates voltage sources. (0 does NOT mean ground. Probably something worth making more clear)

Edit Card Gate Pins

Volume: Vol. Set 1410 SMS, Vol. I

Card Type: DGR, Part No.0370258

Gate: 1

Pins									
	Pin	Input Gate	Output Gate	In	Out	DOT In	DOT Out	Map To	Voltage (Tenths)
	A.	0	0	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		0
	F.	0	0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		0
*				<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		

Apply Cancel



Edit Card Gate Pins



Volume: Vol. Set 1410 SMS, Vol. I

Card Type: AEK, Part No.0370215

Gate: 1

Pins

	Pin	Input Gate	Output Gate	In	Out	DOT In	DOT Out	Map To	Voltage (Tenths)
	C.	0	0	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	OUT1	0
	D.	0	0	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	IN1	0
	E.	0	0	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	IN2	0
*				<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>		

Apply

Cancel

Pages

Volumes handled by this application are comprised of Tie Down Pages, Card Location Pages, Logic Diagram Pages. A card location page is a chart which identifies which modules are in which slots of a panel(in a gate, frame and machine). A logic diagram chart is where the machine's logic was printed out – the Automated Logic Diagram.

Note: Pages from the volumes describing SMS card circuit diagrams are not currently handled as pages. Instead they are handled via the SMS Cards menu, as described earlier.

Card Location Pages

A Card Location page is a chart / matrix of which cards are located in which machine card slots, and often also identifies the ALD Diagram sheet(s) which use the gate(s) on the card.

The top level dialog edits the page characteristics, and is accessed by Edit => Card Location Pages, and specifying the Machine, Volume Set, Volume and Page to be edited (or clicking “New Page” to create a new Card Location Page.

The screenshot shows a Windows-style dialog box titled "Edit Card Location Page Form". It contains several input fields and buttons for configuring a card location page. The fields are organized as follows:

- Select machine:** A dropdown menu with "1411" selected.
- Volume Set:** A dropdown menu with "1411" selected.
- Select Volume:** A dropdown menu with "II" selected.
- Select Page (existing):** A dropdown menu with "11.04.02.0" selected.
- OR:** A button labeled "New Page".
- Panel:** A dropdown menu with "Frame: C Gate: C Panel: 1" selected.
- Sheets:** A dropdown menu with "1" selected.
- Page Name:** A text field containing "11.04.02.0".
- Part No.:** A text field containing "5327094".
- Title:** A text field containing "CIRCUIT CARD LOCATION CHART: FRAME 11 GATE C PANEL".
- Stamp (opt):** An empty text field.
- ECO:** A dropdown menu with "253320" selected.
- Previous ECO (opt):** A dropdown menu with "252961" selected.
- Run (opt):** A text field containing "8849".

At the bottom of the dialog, there are four buttons: "Apply", "Remove", "Force Purge", and "Cancel".


Card Location Chart

The detail for card locations is accessed via Edit => Edit Card Location on the menu, and specifying the Machine, Page, Panel, Card Row and Card Column. Currently, to do this effectively requires that you find the card location chart of interest in the scanned diagrams or the spreadsheets derived from the card location charts, because of the correlation between Page and Panel. (For example, one would have to know that page 11.04.02.0 is the page containing the card location chart for IBM 1411 Frame C, (Gate C), Panel 1. (I have a TODO to allow one to select the page, and have it pre-populate the Panel, and vice versa.

The Card Type, Feature, Crossed out (with a great big "X" on the card location chart) and numbers that appear at the bottom of the block (Bottom Notes) are captured from card location chart.

In the data table, the Page column contains pages that have ALD Diagram logic blocks that refer to this card in this card slot. Sheet Column and Sheet Row identify the coordinates of the logic block that refers to this card. The ECO, if present, identifies the ECO level on the card location chart block. The On Sheet column means that during the spreadsheet capture, the card and card slot were verified to be consistent with the card location chart. Ignore means that this logic block was marked to be ignored for one reason or another. Missing means that while the card location chart indicates the given sheet location uses this card, it was found to be missing on the actual ALD diagram sheet.

Ignore is more complicated. This indicates that this entry should NOT be used when pre-populating information when capturing a logic block on an ALD diagram - for example, if it was found to not actually be present on the ALD diagram set. Ordinarily this is because a discrepancy between an ALD diagram page and the card location chart was noticed during data entry from the Card Location Chart.

 Edit Card Location

Drawing Page

Machine: 1411
Volume: Vol. Set 1411, Vol. II
Page: 11.04.02.0
Page ECO: 253320

Card Slot

Panel: Frame: C, Gate: C, Panel: 1
Card Row: E
Card Col: 8

Card Type: DGR
Feature: S10

Slot Crossed Out
☒

Bottom Note(s): 2, 3, 4, 6, 8 (1-2 chars. ea., Separate with Commas)

Sheet References

	Page	Sheet Col.	Sheet Row	ECO	On Sheet	Ignore	Missing	Note
	14.71.62.1	2	H		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	14.71.62.1	4	E		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
	14.71.62.1	5	H		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
*					<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	

Apply

Delete

Cancel

Edit Card Location Chart Dialog

Tie Down Page

Tie Downs are identified on (one or more) pages. These are typically used for cases where a signal should be tied to ground or a specific logic level to provide signals where the card(s) for a particular feature are not present because the feature is not installed in the machine.

The “W/O” column identifies the feature where, if it is NOT present, requires the use of a particular tie down. The “With” column identifies the feature where, if it IS present, requires the use of a particular tie down. The “Check” column simply captured the check marks that were or were not present for a given entry (likely identifying the cases where a particular tie down was used on a particular machine.)

By default, a tie down is to ground, unless “Other Pin” is identified.

The actual page number of the tie down list was captured, but is not displayed and is not selectable as it should be. (And the pull down should be pre-populated with appropriate value(s) once a Volume Set is selected.)

The screenshot shows a window titled "Edit Tie Downs" with a standard Windows interface (minimize, maximize, close buttons). Inside the window, there are two dropdown menus at the top: "Machine:" set to "1411" and "Volume Set:" set to "Machine 1411, S/N Ohio - 2010". Below these is a section titled "Tie Downs" containing a table with 14 columns: Pin, Page, Card Type, Frame, Gate, Panel, Row, Col, W/O, With, Check, Other Pin, and Note. The table contains 17 rows of data. The first row is highlighted with a blue background. At the bottom of the window are two buttons: "Apply" and "Cancel".

	Pin	Page	Card Type	Frame	Gate	Panel	Row	Col	W/O	With	Check	Other Pin	Note
▶	D	11.10.20.1	DEY	C	C	2	J	26	L1		<input type="checkbox"/>		
	D	11.10.25.1	DEY	C	C	2	K	14	L1		<input type="checkbox"/>		
	D	12.12.60.1	DAC	D	D	3	J	15	F3		<input checked="" type="checkbox"/>		
	K	12.12.67.1	DGR	D	D	4	C	11	F8		<input checked="" type="checkbox"/>		
	P	12.12.67.1	DGR	D	D	3	A	28	L2		<input type="checkbox"/>		
	H	12.12.67.1	DFS	D	D	3	H	28	L2		<input type="checkbox"/>		
	H	12.60.15.1	DFP	D	D	1	C	23	L2		<input type="checkbox"/>	F	CONNECT C23H TO C23F (NOT GROUND)
	K	12.62.02.1	TAU	D	D	3	F	5	M1		<input type="checkbox"/>		
	D	12.62.04.1	DAC	D	D	3	H	12	B1		<input type="checkbox"/>		
	E	12.62.04.1	DAW	D	D	3	G	26	F1		<input checked="" type="checkbox"/>		
	E	13.12.18.1	DFS	D	D	1	H	11	Y1		<input type="checkbox"/>		
	L	13.13.07.1	DFK	D	D	1	J	13	Y1		<input type="checkbox"/>		
	G	13.50.07.1	DFS	D	D	3	G	20	M1		<input type="checkbox"/>		
	D	13.60.03.1	DFS	D	D	3	G	18	B1		<input type="checkbox"/>		
	G	13.60.04.1	TAU	D	D	3	D	12	B1		<input type="checkbox"/>		

ALD Diagrams

The pages discussed heretofore are the support structure for the main event: ALD diagram pages. ALD diagram pages are the pages that are the “meat” of the application.

The top of the ALD diagram hierarchy is the ALD Diagram page, which is accessed via Edit => ALD Diagram Pages, and selecting the machine, volume set, volume and page. Selecting the machine, volume set and volume pre-populates the “Select Page” pull down. One can also add a new page to the volume using the “New Page” button.

This first screen contains meta-data for the page, such as its name (number), the IBM part number for the page itself and the title at the top of the page.

If the page has been stamped with a particular note (e.g. Field Use Only) that is also noted, along with any particular comments on the page (either generated on the ALD itself, or hand-written notes.).

If the page identifies one or more ECOs in its history, those are listed in the Diagram Page ECO’s table. The table can be edited.

Most sheets have **logical** connections from other sheets as inputs or to other sheets as outputs. The Sheet Edge Information table contains that information. The row is the **approximate** row on the ALD diagram sheet where the signal appears. They are NOT exact. Each signal name on a given sheet must be unique in the inputs and/or the outputs. The Count column is generated by this application, and identifies the number of other sheets related to a particular sheet edge signal. Inputs will ordinarily show a count of 1, of course. Except for the count column, this table can also be edited in place.

When a **physical** connection is made from a card in one gate or panel to another card in a different gate and/or panel, that is identified at the very bottom of the ALD diagram as a pair of connections from a given machine/gate/panel/card slot/pin to another machine/gate/panel/card slot/pin. Those are edited by clicking on the “Edit Edge Connections” button.

While this information was captured on the diagrams, it is not currently validated against the connection pages which show panel to panel connections – indeed, those connection pages are not currently in the database design at present. Doing so would allow one to validate these physical connections, nor are these connections cross-checked for inconsistencies. This is an identified “TODO”.

Finally, and probably most importantly/interestingly are the logic block / interconnections shown in the main part of the ALD page, accessed by clicking on the “Edit ALD Blocks” button.

Select machine:

1411

Volume Set:

1411

Select Volume:

II

Select Page (existing):

11.10.17.1

OR:

New Page

Page Name:

11.10.17.1

Part No.:

5327113

Title:

LOGIC GATE RING H-ACC

Stamp (opt):

Comment:

Edit Edge Connections

Edit ALD Blocks

Diagram Page ECO's

Sheet Edge Information

Tag	E.C. No.	Date
A	251773	06/13/62
*		

Left (In)	Right (Out)	Row	Signal Name	Count
<input checked="" type="checkbox"/>	<input type="checkbox"/>	B	+S LOGIC RING ON ADVANCE 1	1
<input checked="" type="checkbox"/>	<input type="checkbox"/>	B	+S LOGIC GATE G	1
<input checked="" type="checkbox"/>	<input type="checkbox"/>	C	+S LOGIC RING OFF ADVANCE 1	1
<input checked="" type="checkbox"/>	<input type="checkbox"/>	C	-S LOGIC GATE G	1
<input checked="" type="checkbox"/>	<input type="checkbox"/>	C	-S PROGRAM RESET 3	1
<input checked="" type="checkbox"/>	<input type="checkbox"/>	E	+S LAST LOGIC GATE 1	1
<input type="checkbox"/>	<input checked="" type="checkbox"/>	B	-S LOGIC GATE H	2
<input type="checkbox"/>	<input checked="" type="checkbox"/>	C	+S LOGIC GATE H	10
*	<input type="checkbox"/>			

Apply

Remove

Cancel

Top Level Edit ALD Diagram Page

Edit Edge Connectors

Machine:

1411

Volume:

1411/Ohio - 2010 Volume: II

Page:

11.10.17.1

Ref.	Connection	Connection	Connection	Connection	Connection	Connection	Connection	Connection
1	11C2A27H	11C7L51H	15A4A23P	15A4A24P				
2	11C2C01H	11C1C28H						
*								

Apply

Cancel

Edge Connections Table

Editing the ALD Diagram

Clicking on the “Edit ALD Blocks” button brings up a chart which looks a bit like the ALD diagram page itself. Columns are numbered from 5 down to 1 (as they are in the ALD diagram itself) and the rows are similarly labelled A through I.

In between each block are entries which indicate the existence of “DOT Functions”. A dash (“-”) is for no DOT function, an “A” for a **logical** “AND” and an “O” for a **logical** “OR”. DOT functions cannot be edited directly, but instead are accessed via the logic blocks which provide inputs to them or to which they provide their outputs.

The logic blocks themselves may be empty (do not appear on the ALD diagram sheet), or may have a machine/frame/gate/ECO designation/panel/row/column/card type designation, just as they appear on the ALD diagram pages. At the bottom of the block is also additional information added by the application: the identified **card** gate and the number of identified inputs / outputs to/from this logic block.

The ECO designation is a letter, which corresponds to the table of Diagram Page ECOs, just like it does on the original IBM ALD diagram page.

Clicking on an individual block brings up the Edit Diagram Logic Block dialog for editing the information associated with a given logic block.

Edit Diagram Blocks

Machine:
Page:

Volume:

	5	4	3	2	1	
A						A
B						B
C		11CA 1G03 DFE G1:2/1		11C. 1G03 DFE G4:3/1		C
D		11CA 1E11 DFE G1:2/3				D
E		11CA 1H13 DAC G1:3/1		11CA 1G03 DFE G3:3/1		E
F		11CA 1F04 DAX G1:2/1	11CA 1B09 DAX G3:2/1			F
G		11CA 1C04 DFG G5:2/1		11CA 1F03 DAW G2:3/1		G
H		11CA 1F03 DAW G3:3/3		11CA 1F03 DAW G1:3/1		H
I				11CA 1F04 DAX G2:2/1		I
	5	4	3	2	1	

Edit Diagram Blocks Dialog

Editing a Diagram Logic Block or DOT Function

Accessed by clicking on an individual logic diagram block, the Edit Diagram Logic Block dialog contains all of the information associated with an ALD diagram logic block, except for its connections. This includes its logic function, in IBM terms (Block Symbol), its input and output modes / levels (see logic voltage levels, above), the location of the card (machine/frame/gate/eco tag/panel/card location) and the card type. On the right is a graphic representation that is intended to match up with the representation on the printed ALD diagram page itself, which makes cross-checking a little easier.

Some blocks have a title indicating the use of a particular logic block which appears above the printed box.

Some “gates” (typically things like load resistors, capacitors, etc.) have outputs on the left hand side, in which case that box is checked.

Some logic gates (typically flip flops) occupy two squares on an ALD diagram page. In that case, the Extended box is checked, and an indication is made whether this block is extended to the one above it, for the one below it (they come in pairs, of course.)

The “Gate” pull down identifies which of the logic gates on a particular SMS card correspond to this particular logic block. This is done by matching up the gate with (the most) corresponding pins to the pins identified on the original ALD diagram page. Sometimes (especially with flip flops) the pins are arbitrarily shared between the two extended logic blocks, but for simple gates, they should correspond exactly.

Connections to and from other logic blocks or to and from signals on the sheet edges are edited by clicking on the “(Apply and) Edit connections” button. The “Apply and” is to remind the user that clicking on this button first applies any changes that may have been made on this dialog before opening the connections dialog.

Edit Diagram Logic Block

Diagram Machine: 1411

Volume: 1411/Ohio - 2010 Volume: II

Page: 12.12.44.1

Diagram Column: 4

Diagram Row: F

Block Title:

Feature:

Block Symbol: +AA

Input Mode/Level: S

Output Mode/Level: S

Output on LEFT

Card Location: Machine: 1411

Frame: C

Gate: C

ECO Tag: A

Panel: 1

Card Row: F

Card Column: 04

Card Type: DAX

Block Configuration:

Block Notes:

Extended

Above

Below

Gate: 1: B,D,G (NAND)

Apply

Delete

Cancel

Logic Block

+AA

--

S S

11CA

1F04

DAX

4F

(Apply and)

Edit Connections

Edit Diagram Logic Block Dialog

Edit Connections Dialog

Finally, by clicking on the Edit Connections button, we reach the page where connections are displayed. At the top are read only fields identifying the ALD diagram page and logic block that these connections relate to.

Then there are two tables which list and allow editing of an individual connection, along with buttons to Add Input or Output connections.

In these tables, the Pin identifies the input or output pin on this gate.

There are three types of connections, identified by the Type column, and which are captured in different tables in the database (column references here start with the "Pin" column as column number 1.)

- P – gate connections from or to pins on other logic blocks on this same page, in which case the third column contains the coordinate of the connected logic block and the associated pin.
- E – edge connections from or to signals identified on the edges of this ALD diagram page, in which case the third column identifies the other sheet upon which this signal originates or to which this signal is sent, and the last column contains the row and signal name.
- D – dot function connections from or to "DOT functions" (wired and/or connections) on this page, or, rarely, with gates on other pages, in which case the third column contains the coordinate of the logic block to the LEFT of the DOT function, the fourth column contains an "A" or "O" for the logical function of the DOT function. The next column contains a "+" or "-", indicating whether the DOT function is defined using negative logic or positive logic. These last two columns are guesses based on the logic function identified on the gates which connect to the DOT function, and are *not* expected to be used during HDL generation.

An individual connection may be edited by clicking the "Edit" button before the first actual data column. A new connection is added by clicking the Add Input button or the Add Output button.

Edit Connections

Diagram Machine: 1411

Volume: 1411/Ohio - 2010 Volume: II

Page: 12.12.44.1

Diagram Column: 2

Diagram Row: C

Card type: DFE

Inputs to this Logic Block / Gate

Add Input

	Edit	Pin	Type				*Ref -or- Row / Signal Name
▶	Edit	H	P	4C	C		
	Edit	K	P	4D	C		
	Edit	L	E	19.10.03.1			C / -S START INTERRUPT

Outputs from this Logic Block / Gate

Add Output

	Edit	Pin	Type				*Ref -or- Row / Signal Name
▶	Edit	G	D	2C	O	+	

Edit Connections Dialog

Editing a DOT Function

To edit a DOT function, click on the “A”, “O” or dash (to add a DOT function) that exists between the larger logic block entries on the diagram.

This dialog is a combination DOT function editing dialog (where all one can specify is AND or OR) and a list of connections to and from the DOT function. The rest of the information in the top of the dialog is derived from the parent dialogs and is read-only.

Note that connections to the DOT function from a logic block on the same page or to a logic block on the same page cannot be edited here. Instead, such connections are edited from the logic block gate involved.

One can add/delete/edit connections to the DOT function from the edge of the sheet (originating from other sheets) or from the DOT function to the edge of the sheet (sending signals to other sheets) from the Edit DOT function dialog. The resulting dialog is described later.

Machine: 1411 Page: 12.12.44.1

Volume: 1411/Ohio - 2010 Volume: II

Diagram Column: 2 Diagram Row: C

Logic Function
☐ AND ☒ OR Add Input/Output

Connections (Gates are Read only)

	Edit	In/Out	Type	Coord.	Pin/'Ref	Polarity	Row / Signal Name
▶	(N/A)	I	P	2C	G	+	
	Edit	I	E	16.42.01.1			C / +S SET B CYCLE CTRL *ARITH*
	Edit	I	E	12.60.04.1			C / +S SET B CYCLE CTRL * BR OPS
	(N/A)	I	P	2E	G	+	
	(N/A)	I	P	2G	E	+	
	(N/A)	I	P	2H	D	+	
	(N/A)	I	P	2I	C	+	
	Edit	O	E	12.12.21.1			C / +S SET B CYCLE CTRL

Apply Delete Cancel

Edit DOT Function Dialog

Logic Block (Gate) Connections

The three types of connections to and from logic blocks are edited using the same dialog – the Edit Logic Block (Gate) Connection dialog. There are two major flavors of this dialog, depending upon whether one edits the connection from the input side or the output side. The machine, volume, page, diagram column/row and card type fields are read-only, filled in from the information from the parent Edit Diagram Logic Block and Edit Connections dialogs. To move a connection to a different gate, deleted it from one gate and add it back in the correct location.

For an input connection from another logic block on the same page, specify the input pin, select “Gate” from the Type radio button list, and specify the logic block coordinate and pin of the source of the connection. Some logic block connections (most often connections that are outputs to the sheet edge on the right hand side), there is a number reference that refers to the connection list near the bottom of the page. Those are specified using the *REF pull down list.

For an output connection to a gate, the dialog is similar, but specifies the gate which receives the signal as an Input. One can edit a gate-to-gate connection from either side of the connection. Also, for output signals, one specifies whether the connection is positive logic or negative logic using the Polarity radio buttons. Generally, connections that appear on top half of the output of a logic block in the original IBM ALD diagram are positive logic, and negative ones come out from the bottom half of the logic block.

Diagram Machine: 1411 Volume: 1411/Ohio - 2010 Volume: II

Page: 12.12.44.1 Diagram Column: 2 Diagram Row: C

Card type: DFE

Edit Input to Pin: P

Polarity: ☒ + ☐ -

Type: ☒ Gate ☐ Dot Function ☐ Edge Connector

Coordinate: 4C Pin: C *REF

Coordinate:

Row/Signal:

Source Sheet: *REF *REF

Apply Populate Delete Cancel

Input connection from another logic block on the same page

Inputs that come from the (left hand side) sheet edge are specified by selecting the input pin, as before, but then selecting the “Edge Connector” radio button. This enabled one to select the edge signal from the “Row/Signal” pull down, and then specify the sheet that is the source of the signal, along with up to two references, as described above.

For an output connection to an Edge, the dialog is similar, except specifying an output signal on the right hand side of the original IBM ALD diagram page.

Diagram Machine: 1411 Volume: 1411/Ohio - 2010 Volume: II

Page: 12.12.44.1 Diagram Column: 2 Diagram Row: C

Card type: DFE

Edit Input to Pin: L

Polarity: ☐ + ☐ -

Type

☐ Gate

☐ Dot Function

☒ Edge Connector

Coordinate: Pin: *REF

Row/Signal: C / -S START INTERRUPT

Source Sheet: 19.10.03.1 *REF *REF

Apply Populate Delete Cancel

Input connection from a sheet edge

Inputs from DOT functions and outputs to DOT functions are specified by selecting the “Dot Function” radio button, and specifying the logic block coordinate which is to the immediate LEFT of the DOT function letter on the overall diagram.

Again, connections to and from DOT functions on the same page as the gates are edited like this, from the gates involved, rather than from the Edit DOT Function page.

Note that the type (And/Or) of the DOT function is not specified in the connection, but rather specified by editing the DOT function entry itself, as described earlier.

Edit Logic Block (Gate) Connection

Diagram Machine: 1411 Volume: 1411/Ohio - 2010 Volume: II

Page: 12.12.44.1 Diagram Column: 2 Diagram Row: E

Card type: DFE

Edit Input to Pin: A

Polarity
☐ + ☐ -

Type

☐ Gate

☒ **Dot Function**

☐ Edge Connector

Coordinate: 3E

Row/Signal:

Source Sheet: *REF *REF

Apply Populate Delete Cancel

Input connection from a DOT function

Editing DOT Function Edge Connections

As mentioned earlier, DOT functions can, sometimes, receive connections from a sheet edge or provide outputs to a sheet edge. These are accessed from the Edit DOT Function dialog page by clicking on the “Edit” button for the connection. A new edge connection for a DOT function is created by clicking the Add Input/Output button. (Again, connections to or from DOT functions from or to logic blocks (gates) on the same page are edited from the logic block dialog.)

For a new connection, one specifies the In or Out radio button. A DOT function sheet edge connection cannot be changed between input and output once created. Instead, delete the incorrect one, and add the correct connection.

As with logic block gate connections, the information at the top of the dialog is read-only, inherited from the parent dialogs. Once one specifies In or Out (when adding), one then selects a Row/Signal from the drop down list, specifies a source or destination sheet and optionally an edge connection reference from the drop down list.

For convenience, selecting the Row/Signal will pre-populate the Source/Destination Sheet.

The screenshot shows a dialog box titled "Edit DOT Function Edge Connection". It contains the following fields and controls:

- Machine: 1411
- Page: 12.12.44.1
- Volume: 1411/Ohio - 2010 Volume: II
- Diagram Column: 2
- Diagram Row: C
- Radio buttons: In (selected), Out
- Row/Signal: C / +S SET B CYCLE CTRL *ARITH*
- Destination Sheet: 16.42.01.1
- *REF: (dropdown arrow)
- Buttons: Apply, Delete, Cancel

DOT function Edge Connection dialog for an input to the DOT function

Importing Data

As described earlier, development started by creating some spreadsheets with data, as doing so was bound to be much more efficient than using the application to enter some things (like the card location charts.) Also, this allows the application to pre-populate quite a bit of information, which helps speed things along.

The spreadsheets used originally to import data (and, in some cases, kept up to date as errors were found while editing pages later) can be found in folder

IBM1410-ImportedSpreadSheets

The main spreadsheet is IBM1410-Drawing-Data.xlsx.

Individual .csv files were then created by exporting information from this spreadsheet to use by the import classes in the application.

Notice the use of past tense – these were one-time imports. Importing them again would be disastrous.

Data which can be imported includes:

- (Machine) Features
- SMS Card information (type, pins, internal connections, etc.)
- (Card) Location Pages to pre-create the pages containing card location charts
- Card Location Charts
- Tie Downs

Importing of cable interconnection pages has not yet been completed (or even properly designed in the database.)

Logic Synthesis

As stated earlier, the end-goal of this project is to be able to recreate the logic for an IBM SMS machine (and the IBM 1410 in particular) from the information gathered in the database.

The rudimentary beginnings of this process can be found in the “Generate HDL” portion of the menu.

The Generate HDL entry in the menu allows generation of HDL from individual pages.

The Generate Group entry is designed to support generation of HDL from a group of pages, so that their edge signals can be combined. The Page(s) to generate can use “%” style SQL syntax to specify the group of pages to use as input to the process.

Currently code only exists for VHDL generation (and it is incomplete and rudimentary), however the framework of the application is designed to support generation of more than one kind of HDL.

Reports

The Reports menu is for generating various types of reports that may turn out to be useful in order to locate and correct errors in the (manually) captured data.

Currently only one report exists – the Card Type Usage Report, which creates a report of every ALD page on which a particular card type appears, its coordinates, its levels (In and Out) and its pins, as an aid to locating errors.