

SYSTEM DRIVEN HARDWARE DESIGN - PCB ASSIGNMENT

OSCAR AGUILAR AGUILA ISAIAS

766468

Schema

-DZLINTO

D3_INT3

05

D8

SPLNISD SPLNOSI SPLSCK

SPLGND SPILRESET

₫—C RESET

SPL5V

D4 D5 04

06 D7

D10 D11 D12 D13

MI50 M051

5CK

1572 GND4

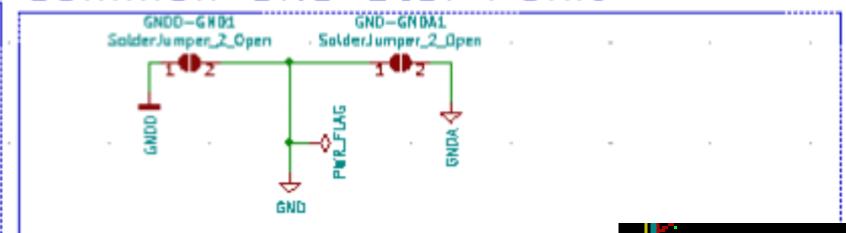
⊿RST2

ARST1

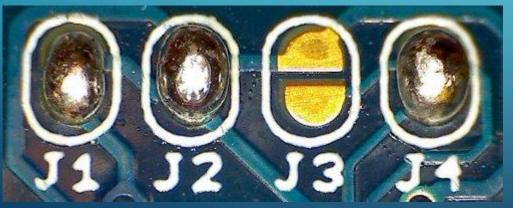
Rem VL.0.0

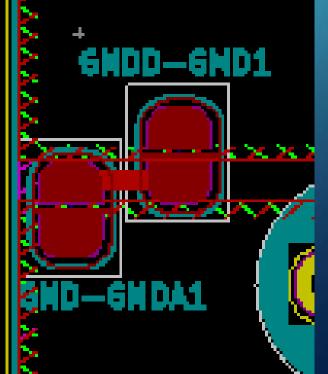
ld: 1/1

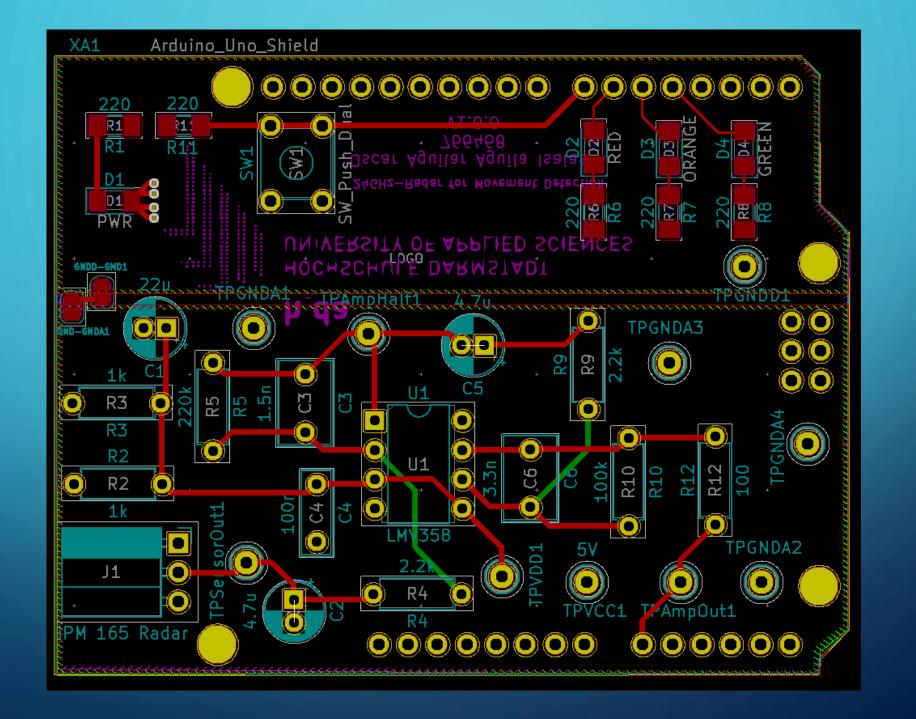
Common GND Star Point



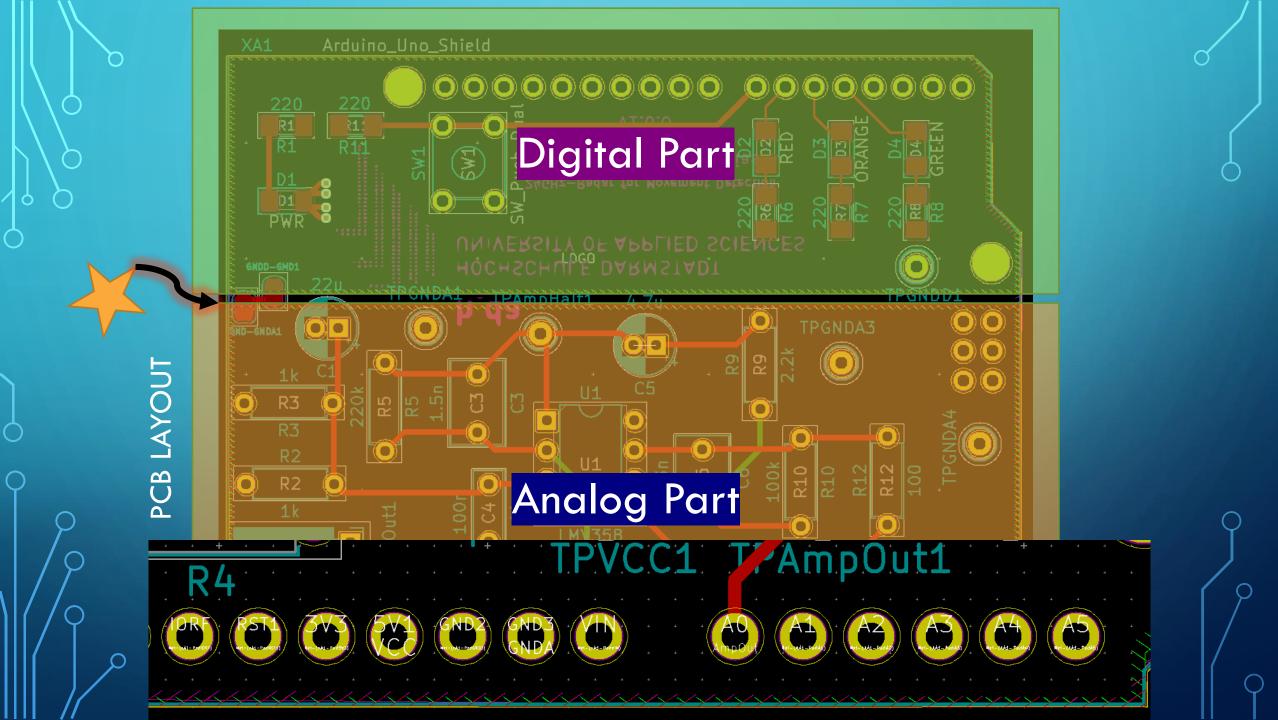
CAN BE UNSOLDERED for root-cause analysis

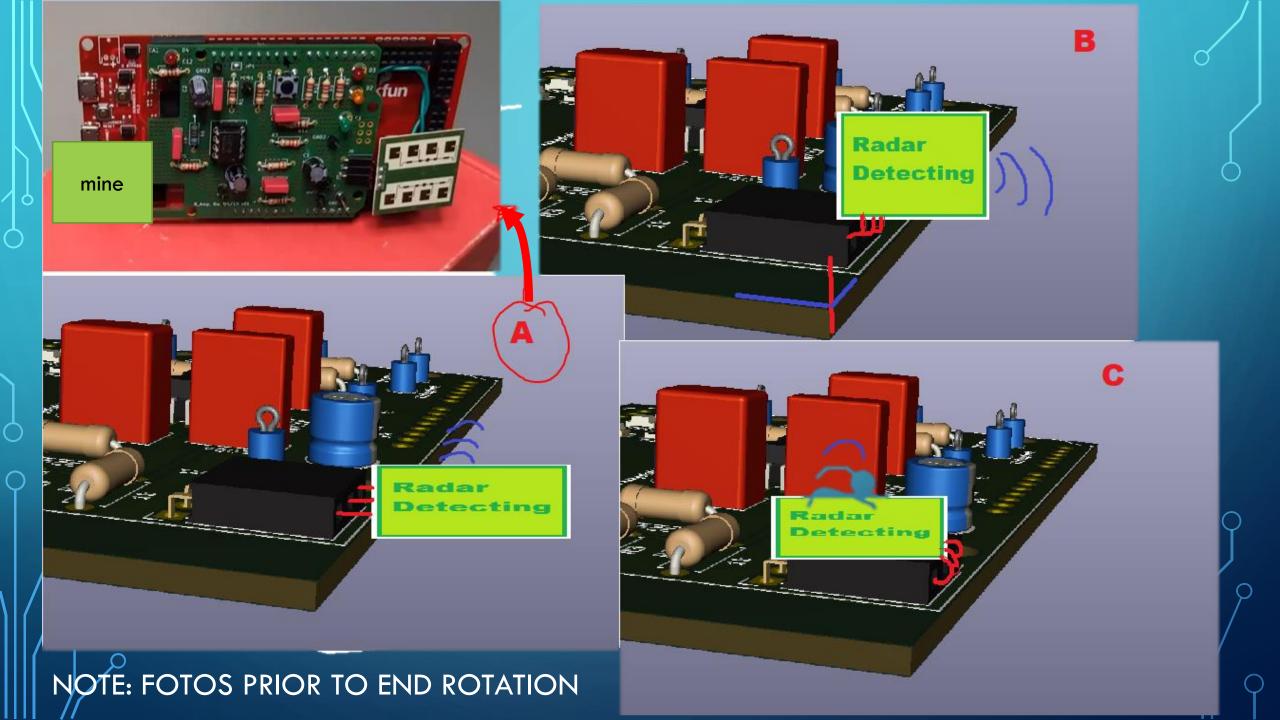






PCB LAYOUT



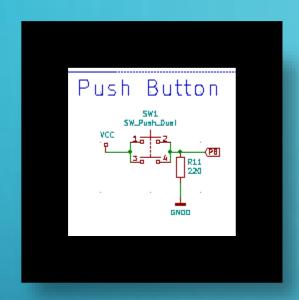


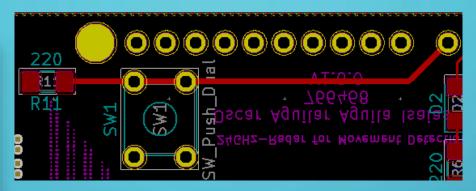
Arduino_Uno_Shield XA1 220 원scar Aguilar Aguila Isala - 766468 · 성 S 8 8 9 9 9 PWR HOCHSCHULE DARMSTADT UNIVERSITY OF APPLIED SCIENCES TPGNDA3 R9 U1 R3 R5 R5 R3 R2 R2 100r O LMV358 57 TPGNDA2 R4 165 Radar 0000000

NO

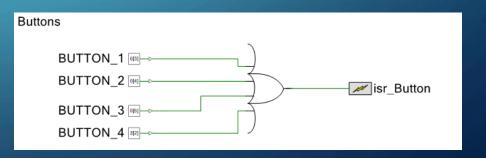
NOT AN AROUNO!!!

 Physical pull-down resistor for easier user set up





NO CONFIG? NO PROBLEM!





- VIAS on power indicator for reduced stress
- TP on every stage of amplification
- All networks had names

