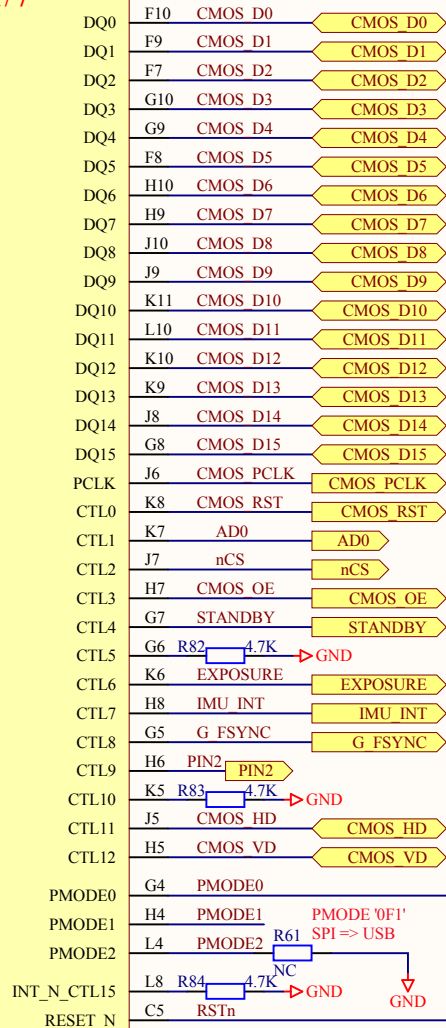


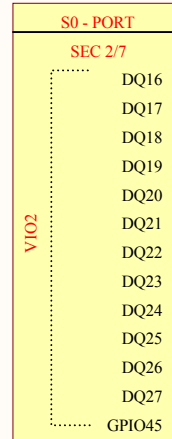
U1A

P- PORT SEC 1/7



CYUSB3014

U1B



CYUSB3014

3V3

R1

R3

4.7K

4.7K

GND

R4

100K

GND

5V0

R5

1K

GND

LED1

LED0805

GND

Q1

BC817

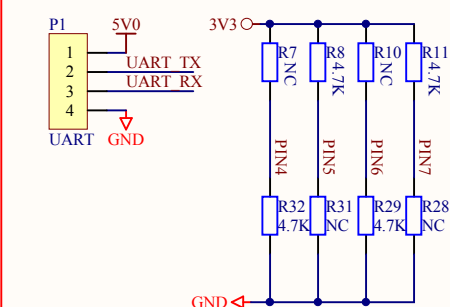
GND

R6

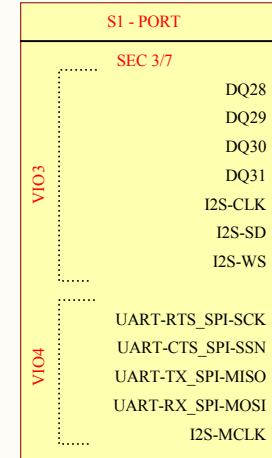
200 / 1%

GND

UART&&GPIO

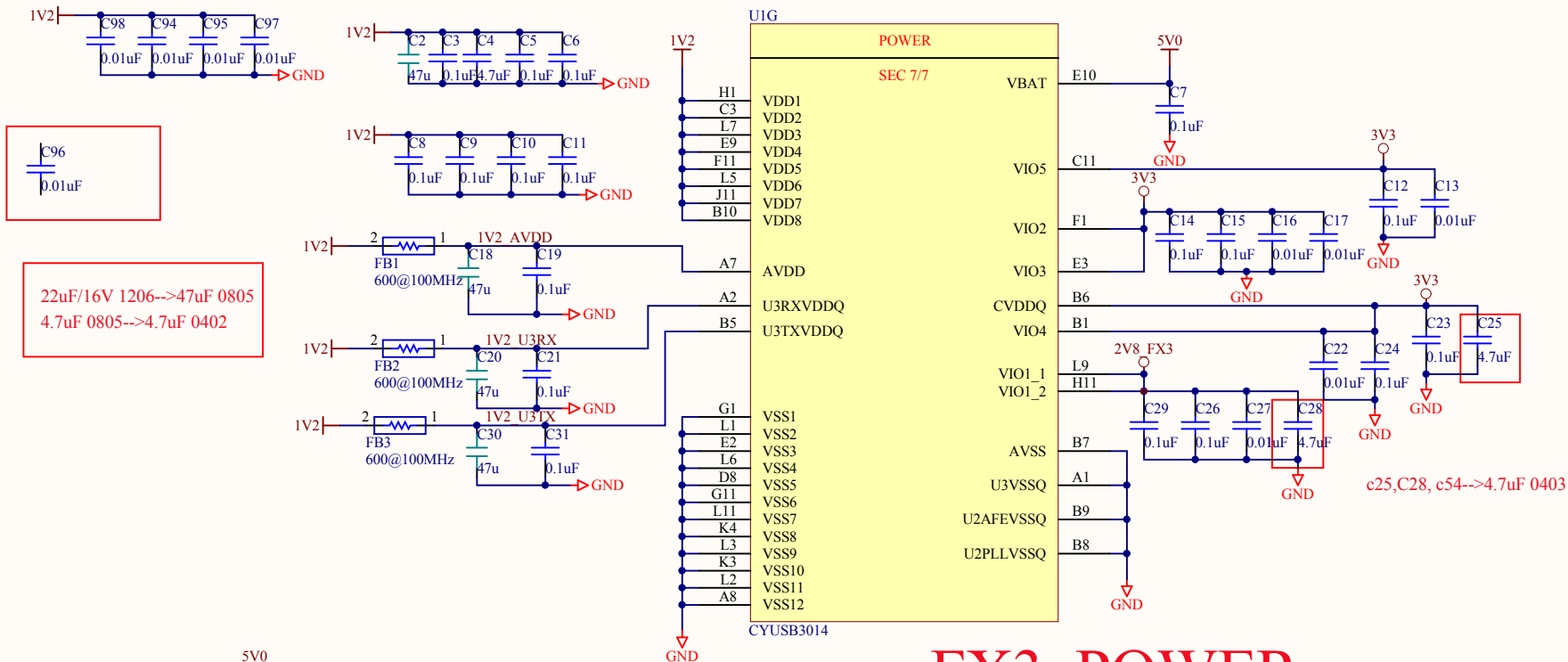


U1C

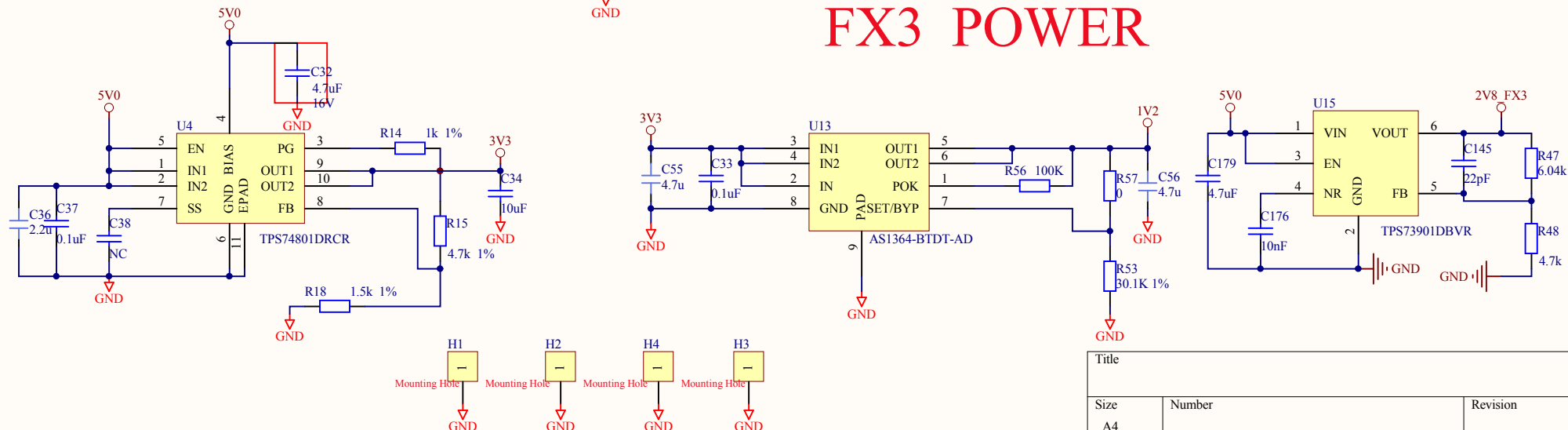


CYUSB3014

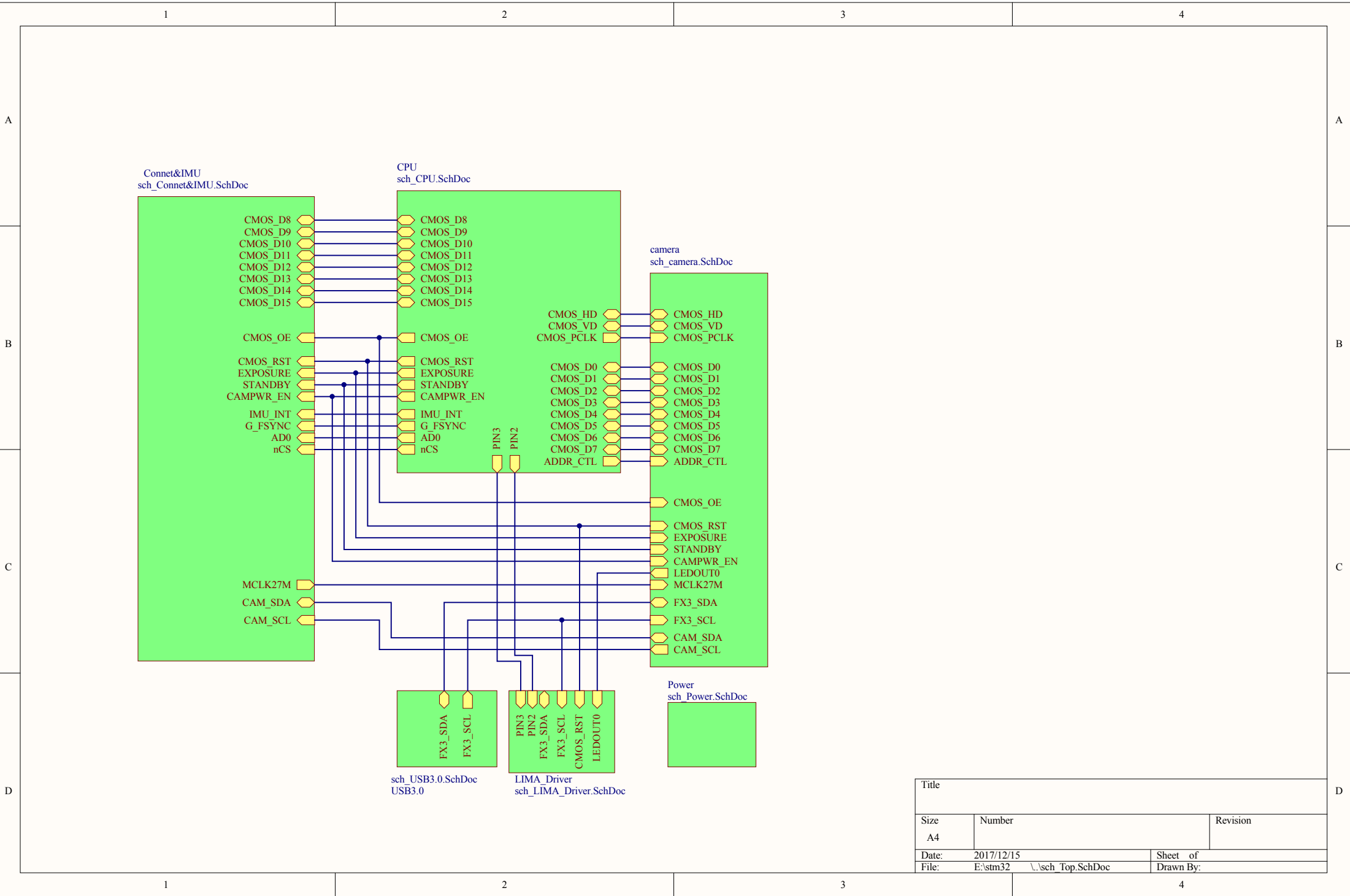
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Date:	2017/12/15	Sheet of
File:	E:\stm32 \sch CPU.SchDoc	Drawn By:



FX3 POWER

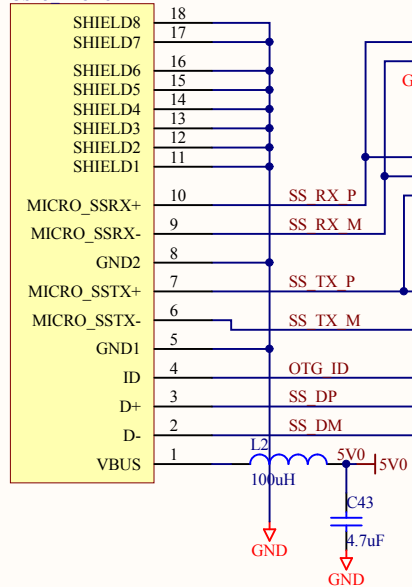


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Date:	2017/12/15	Sheet of
File:	E:\stm32 \.sch Power.SchDoc	Drawn By:



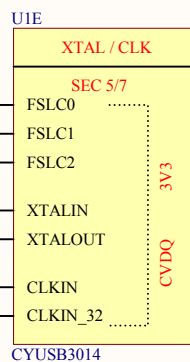
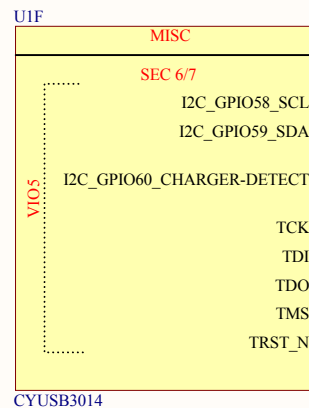
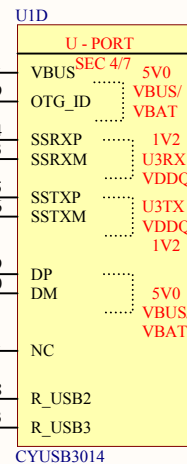
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Size	Number		Revision
A4			
Date:	2017/12/15		Sheet of
File:	E:\stm32 \\.sch_Top.SchDoc		Drawn By:

J1 USB3 MICROB



PART_NUMBER = SP3010-04UTG
manufacturer = Littlefuse

The following signal pairs:
SS_DP : SS_DM
SS_RX_P : SS_RX_M
SS_TX_P : SS_TX_M
UTXP : UTXM
are designated as differential pairs.
All differential pairs are to have differential-mode
characteristic impedance of 90 Ohm.
To the fullest reasonable extent they should be
length-matched, and layer changes should be
minimized.



Title		
Size	Number	Revision
A4		
Date:	2017/12/15	Sheet of
File:	E:\stm32 \.sch USB3.0.SchDoc	Drawn By: