**THE STEPS FOR INTERFACING BETWEEN TWO MODULES**

***Step1***: Create => Initiator and Target

* Initiator:
* Socket Initiator
* Variable
* Target:
* Socket Target & Register blocking transport
* Memory to store data from Initiator

***Step2***: Bind 2 socket

***Step3***: Start

* Tlm generic payload:
* Set sc\_time
* Initialize necessary trans attributes (command, address, data\_ptr, data\_length, streaming width, byte\_enable\_ptr,dmi\_allowed,...)
* Virtual b\_transport:
* Get attribute of trans
* Work with attribute
* Check response