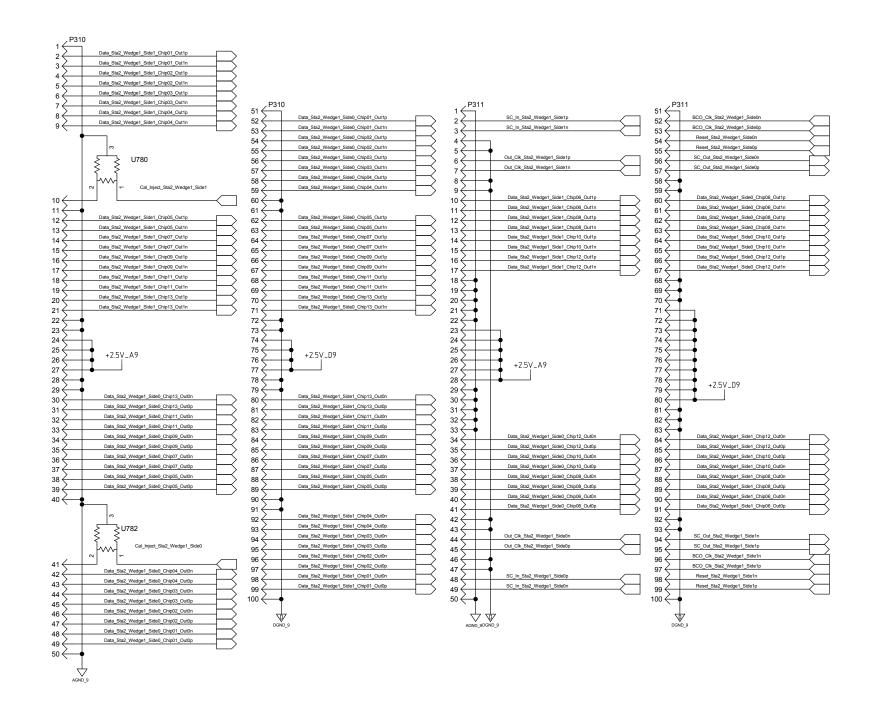
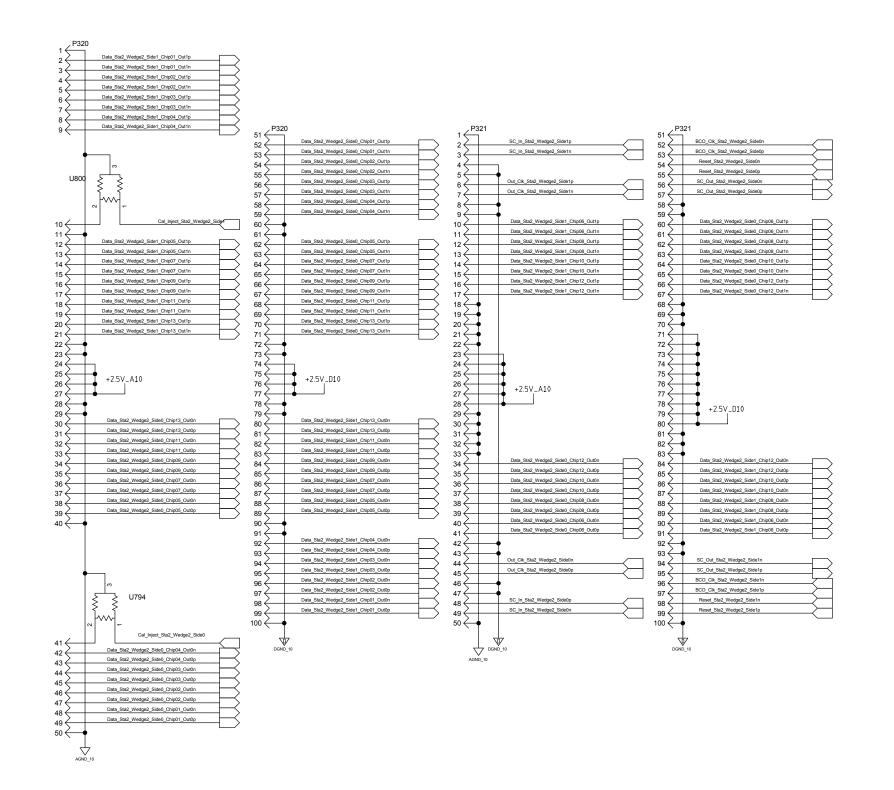


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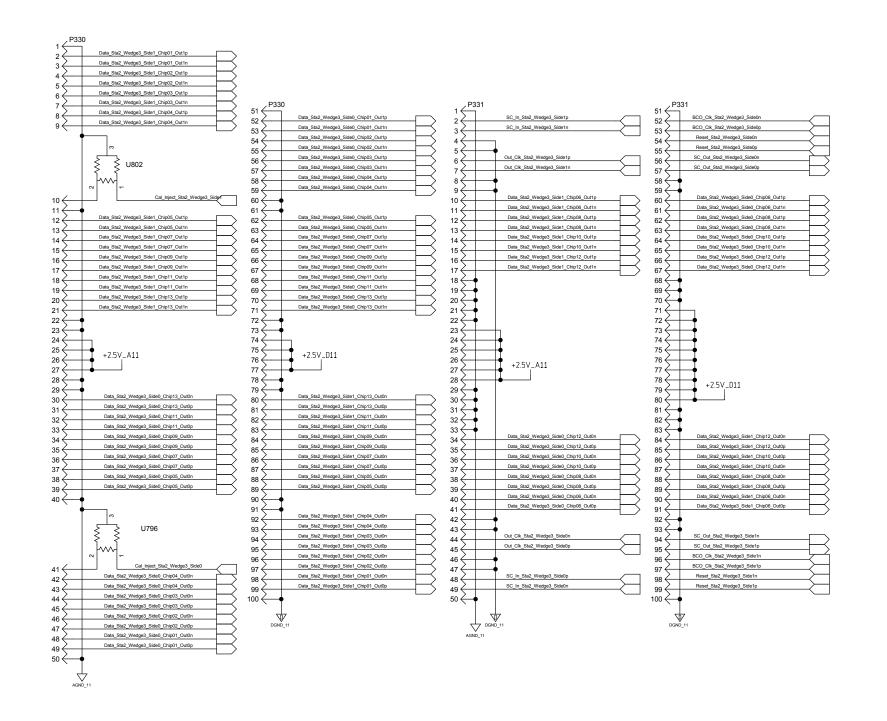
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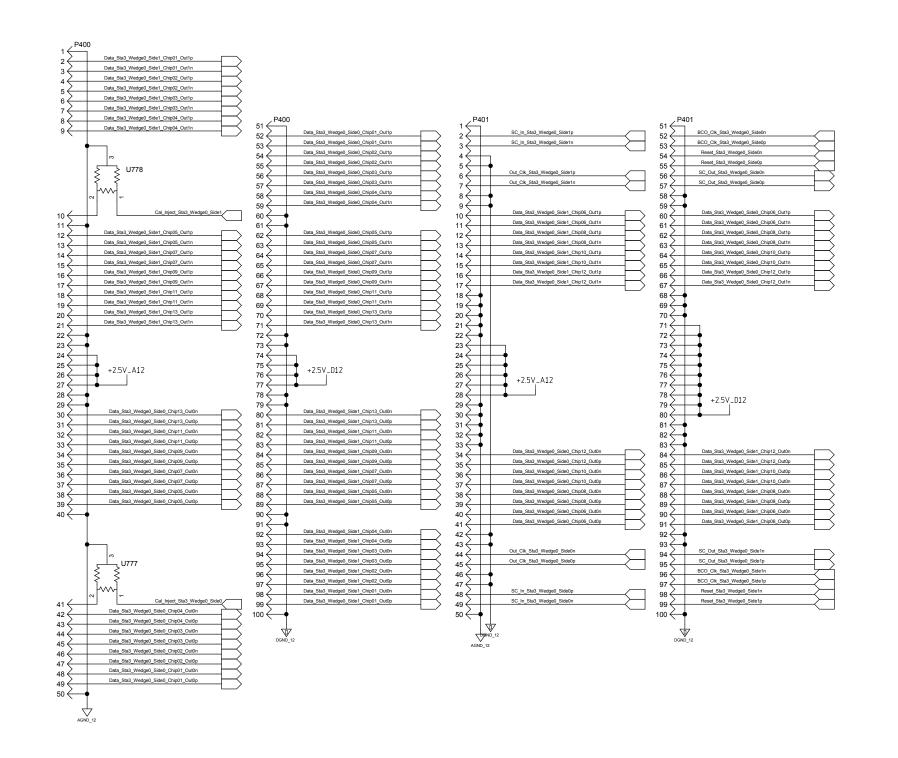
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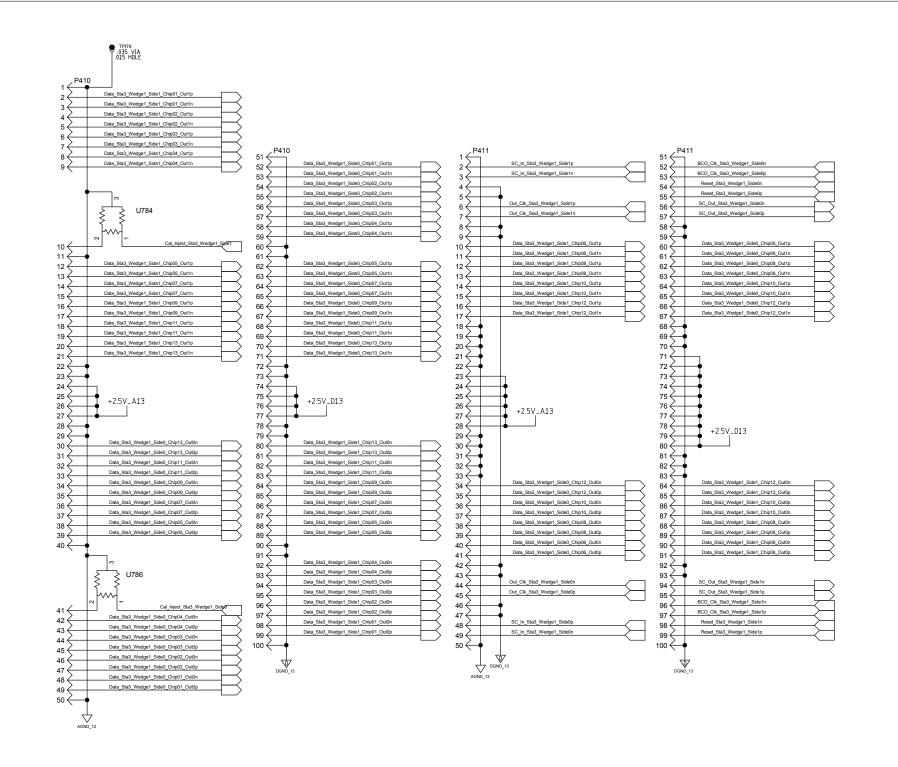
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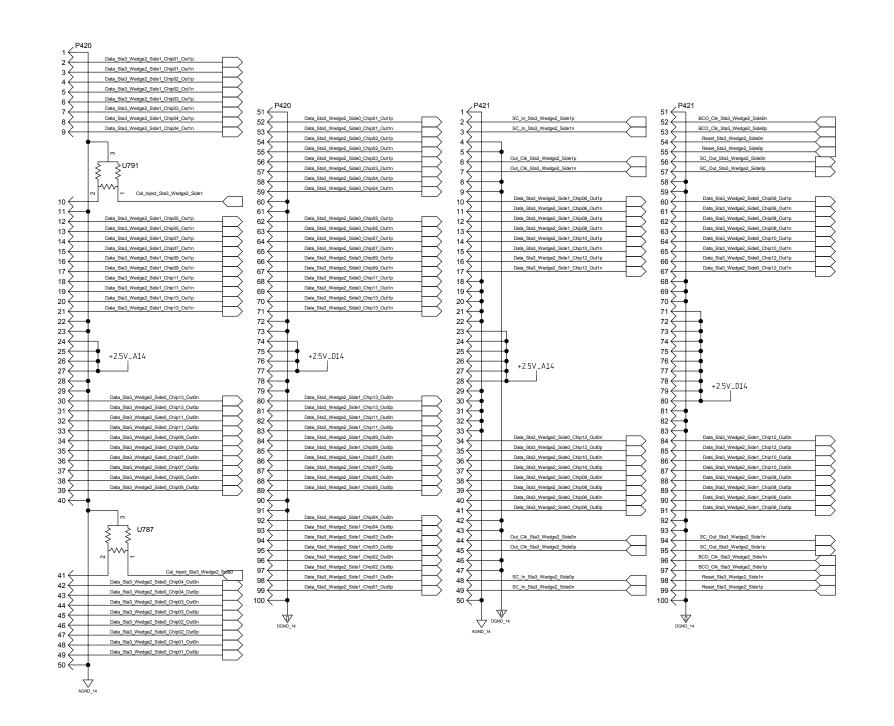
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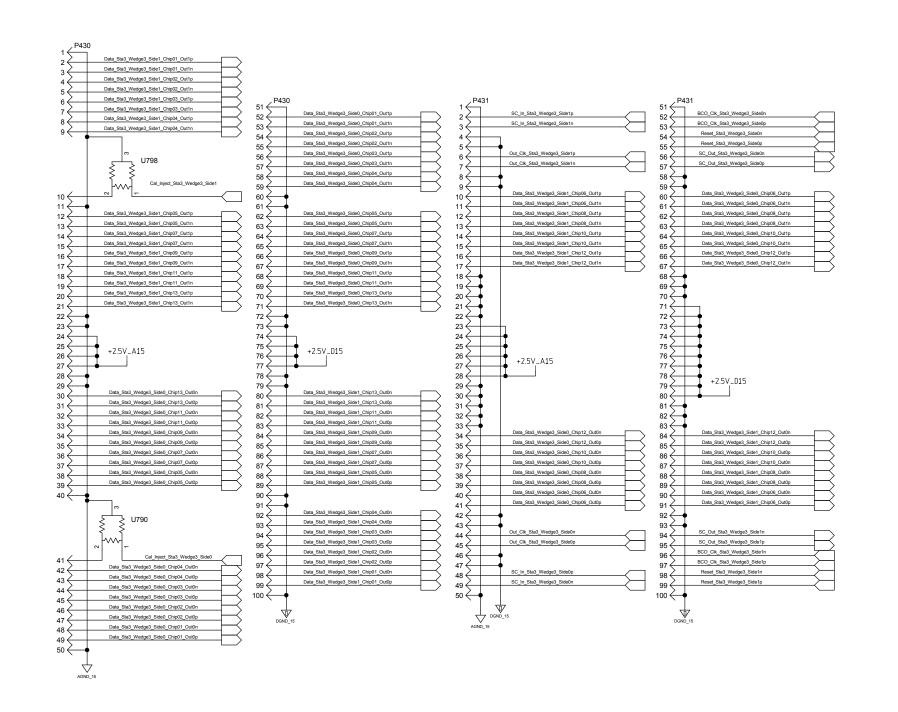
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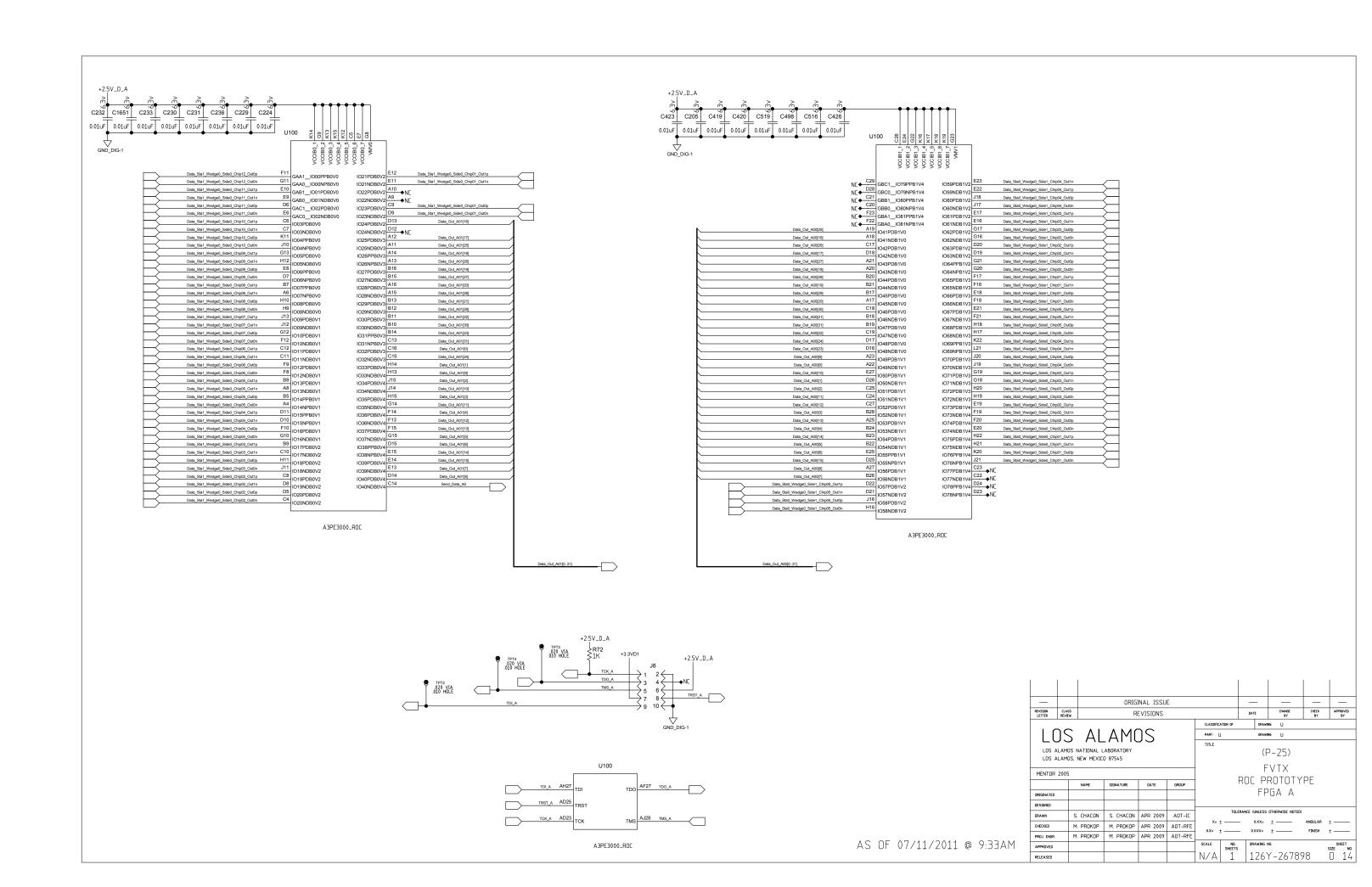
Station 3 Wedge 2

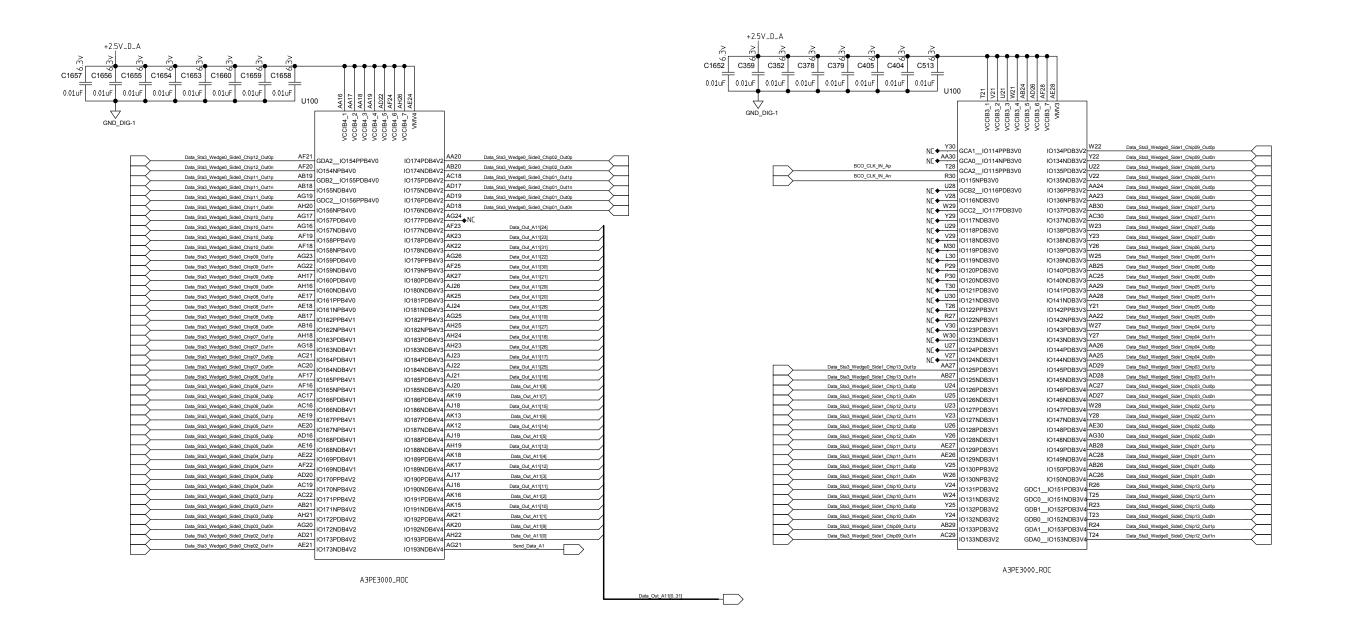
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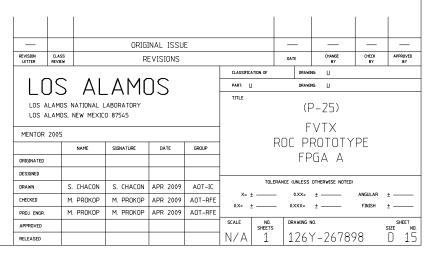


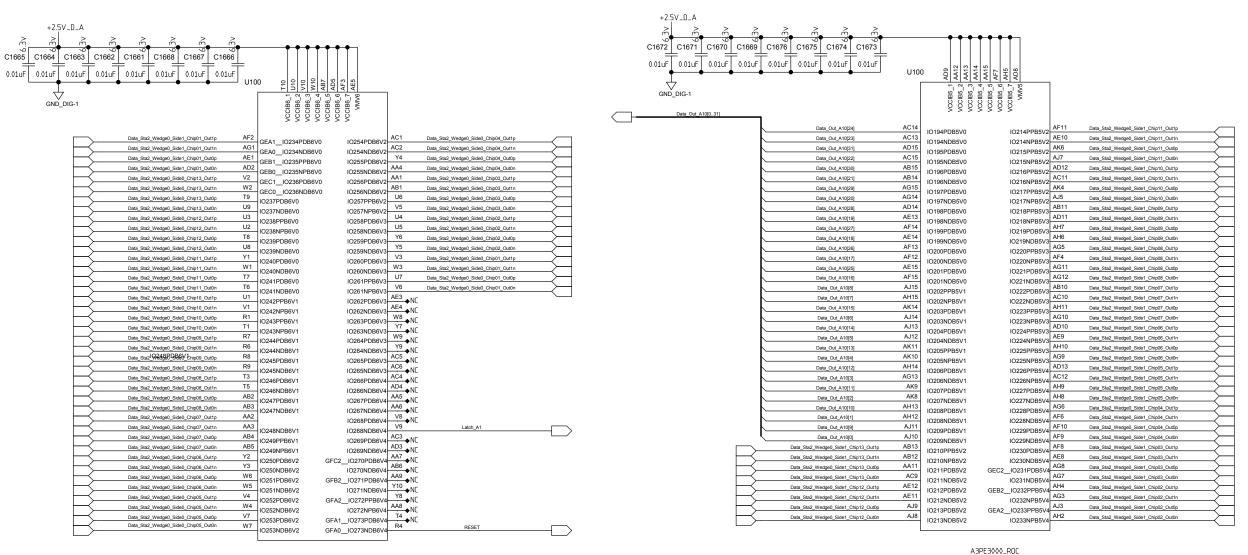
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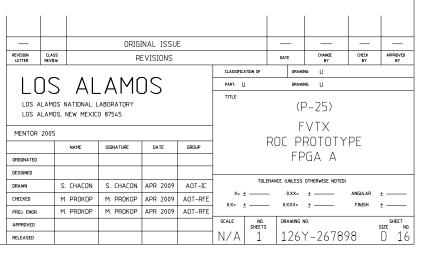
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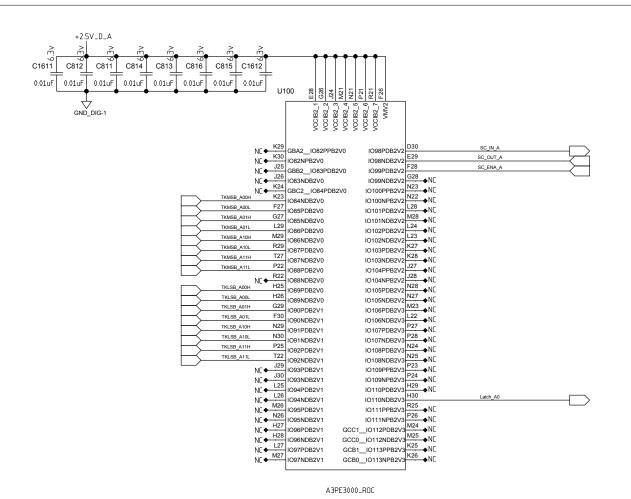


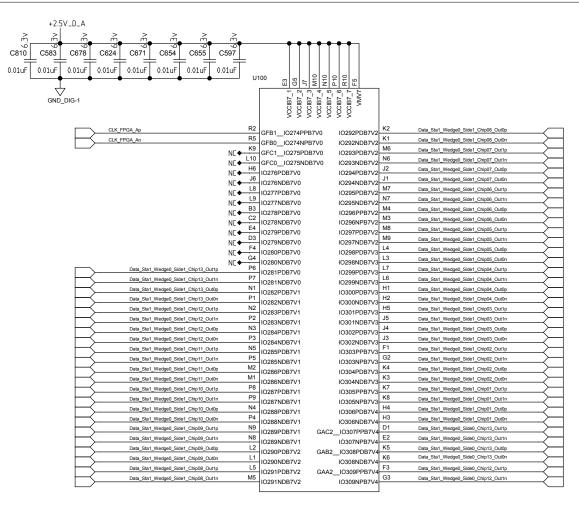


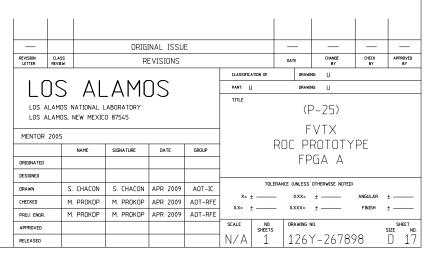


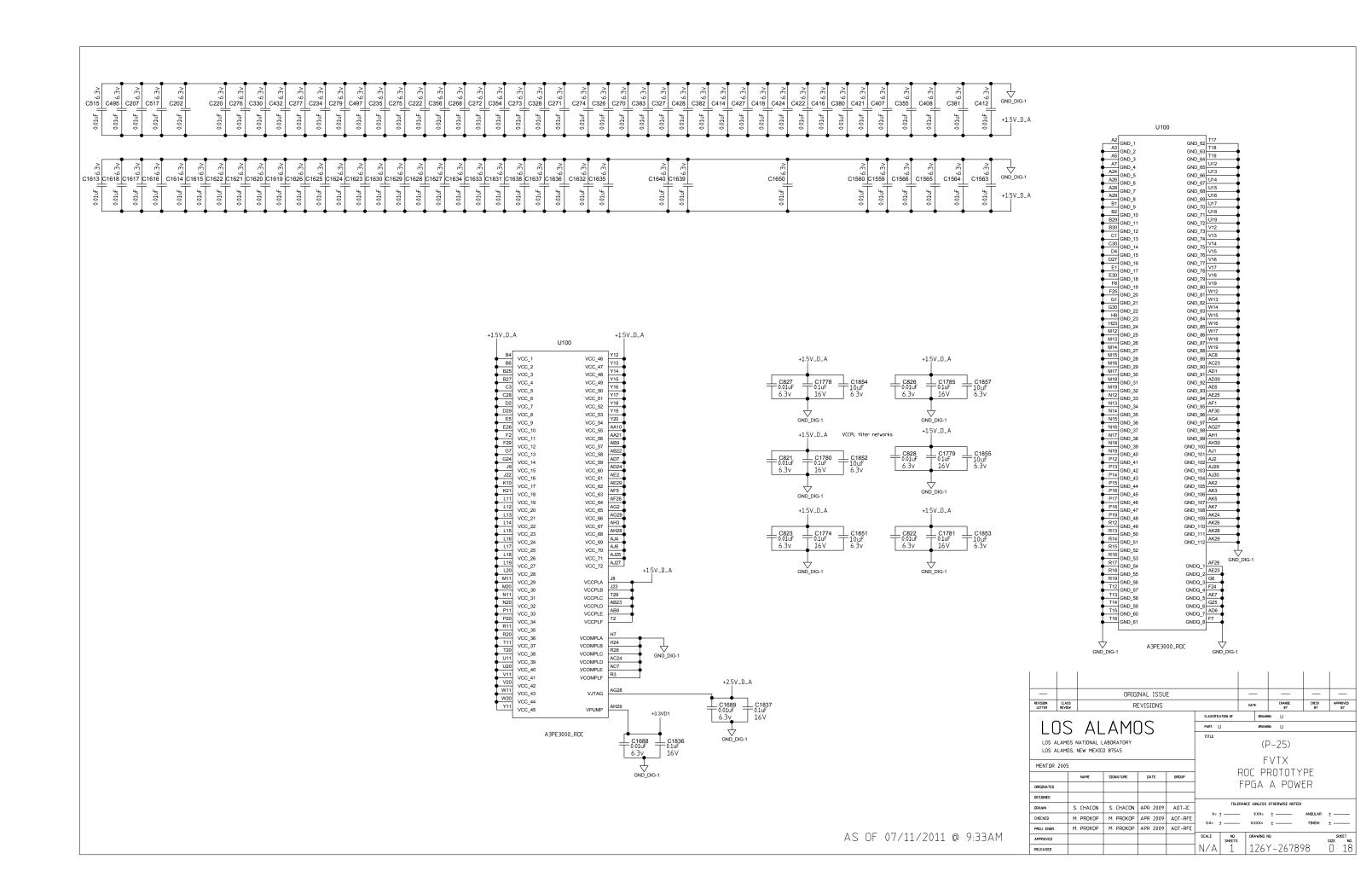


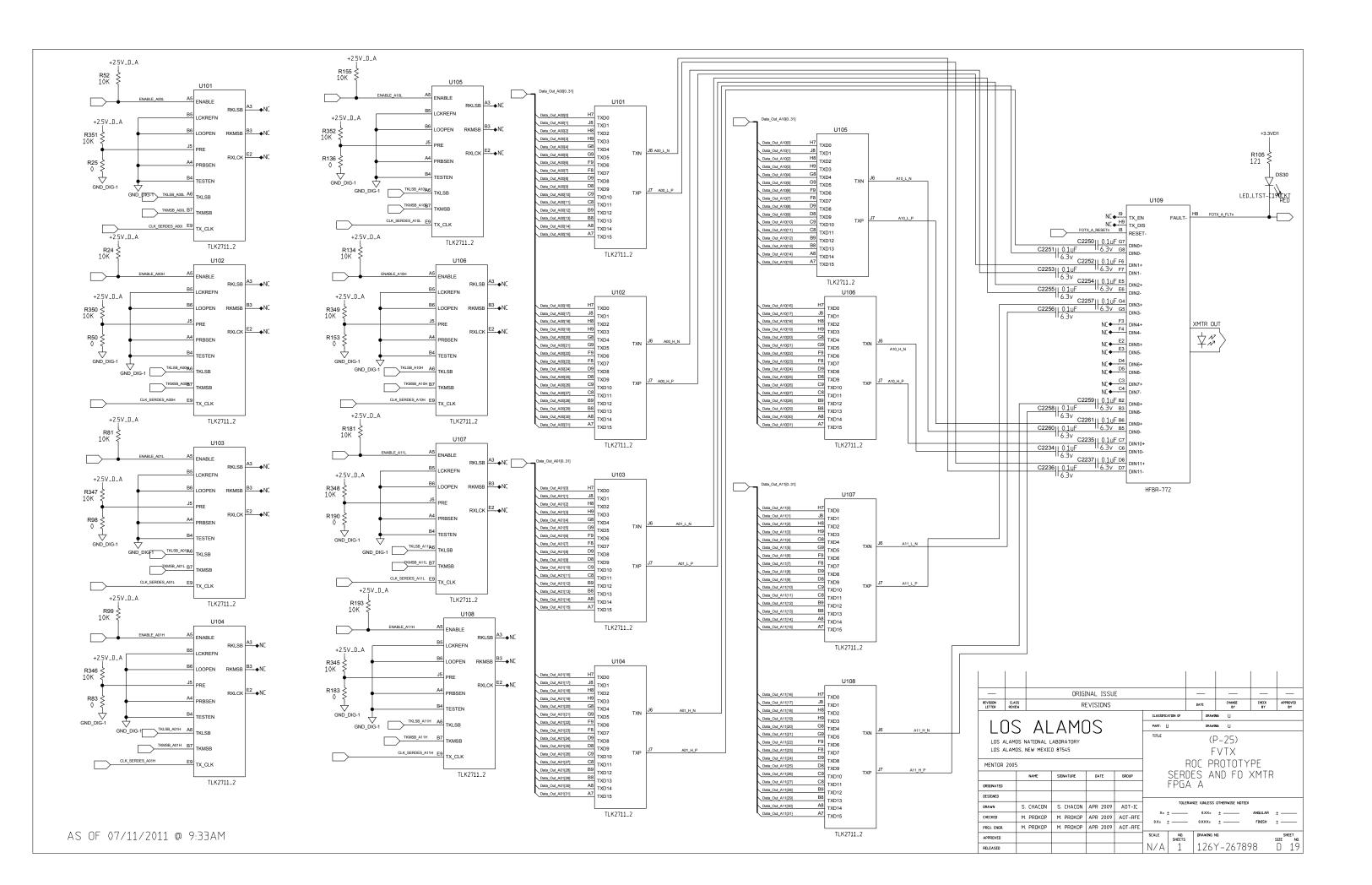


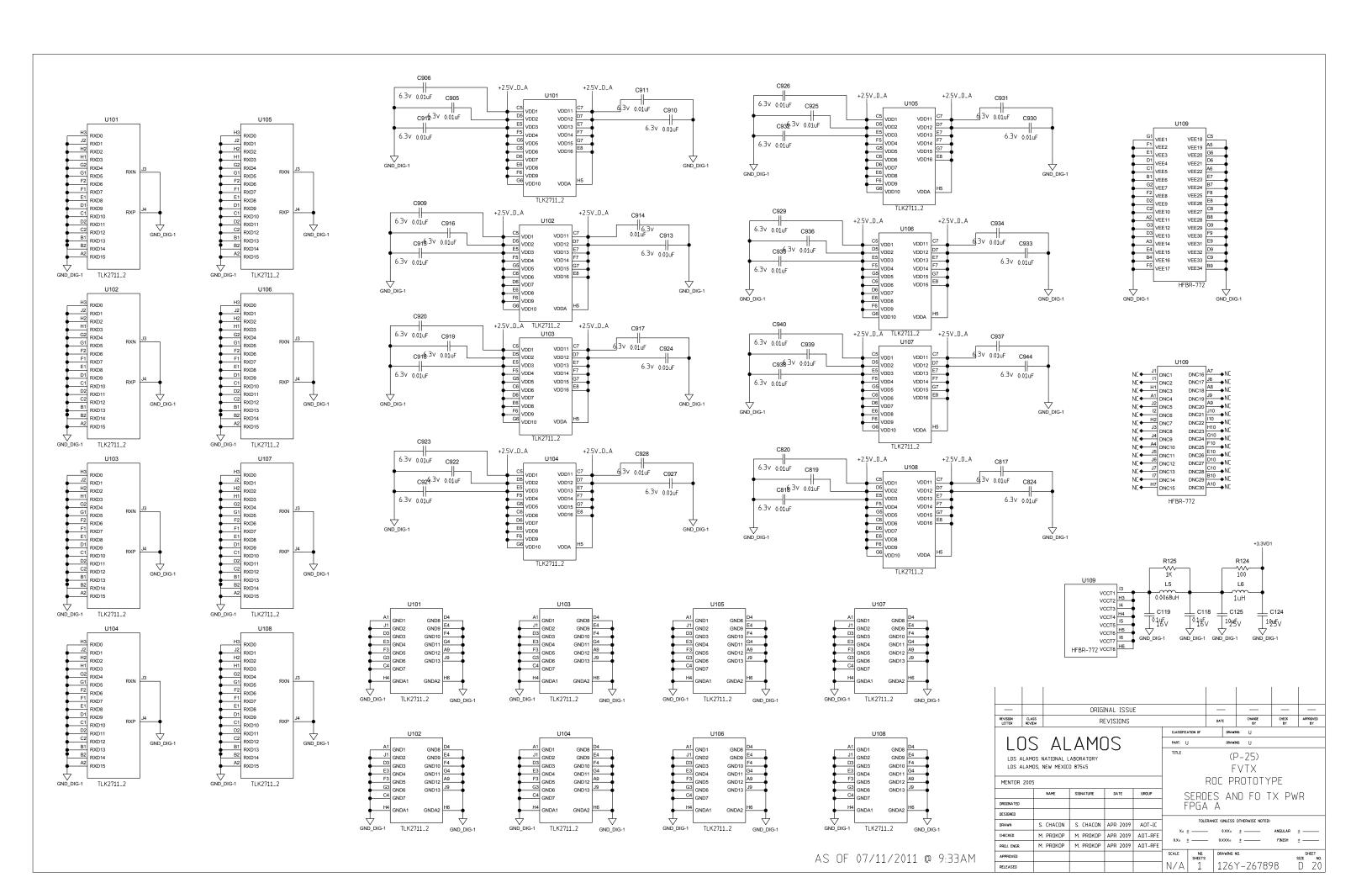


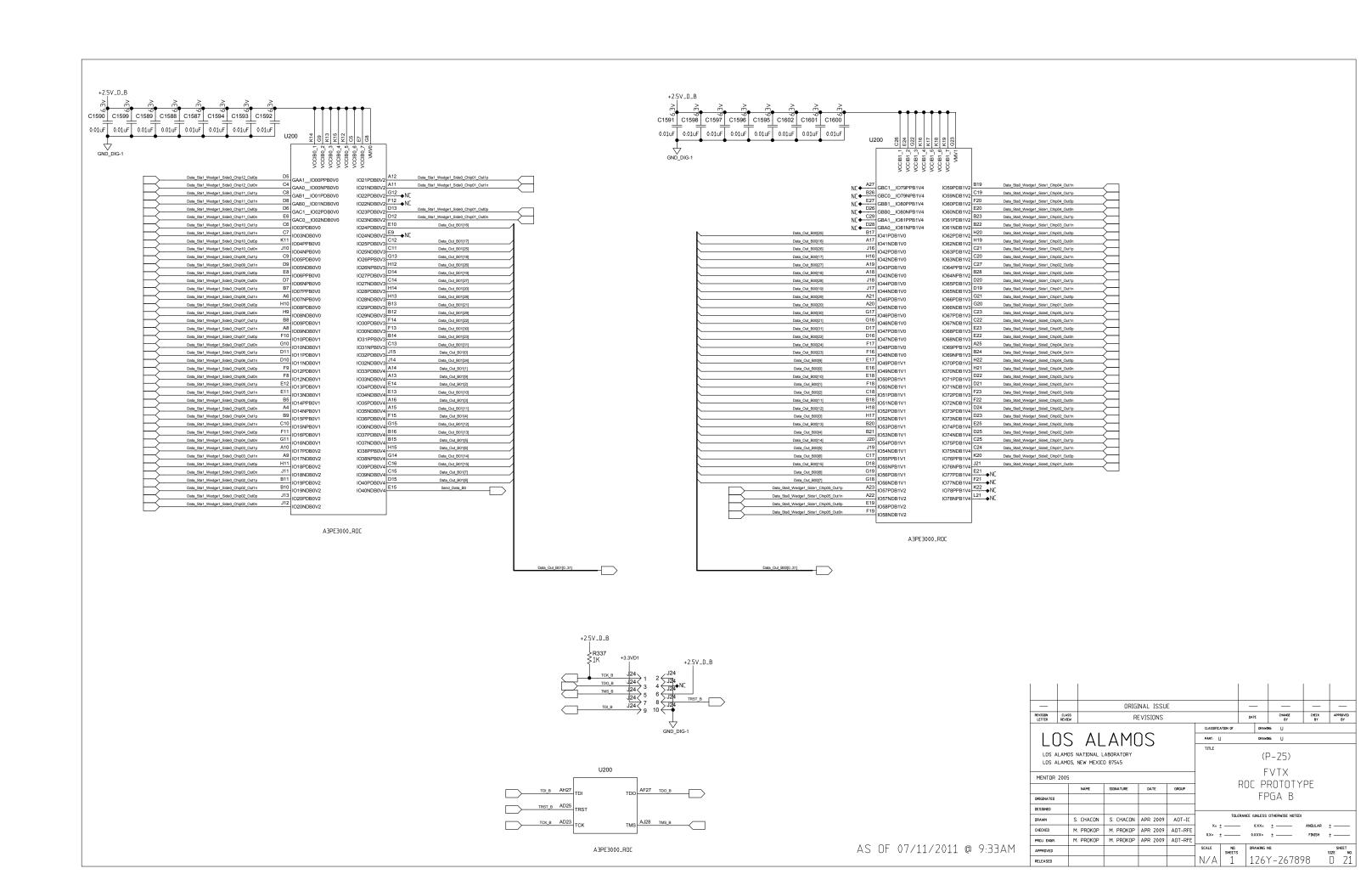


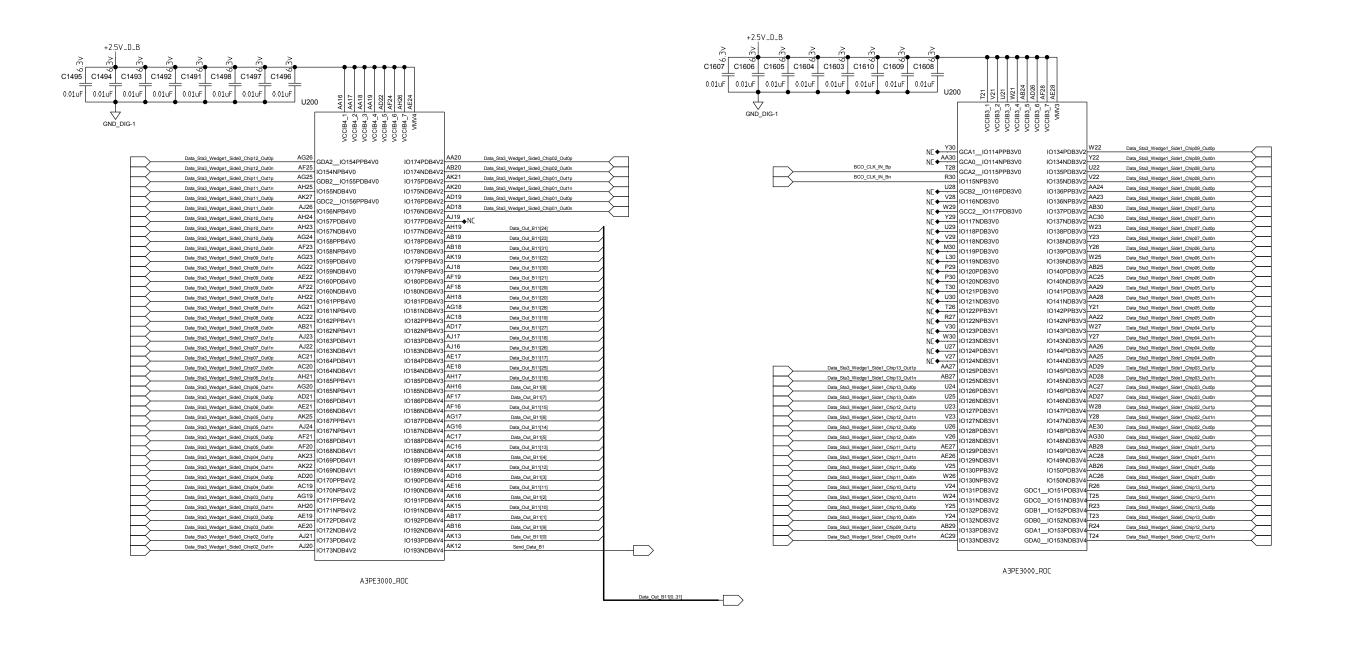


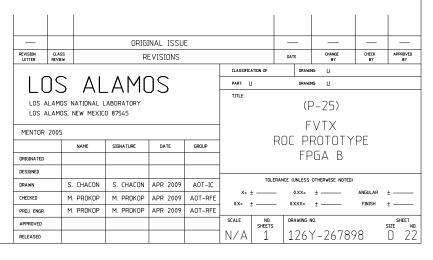


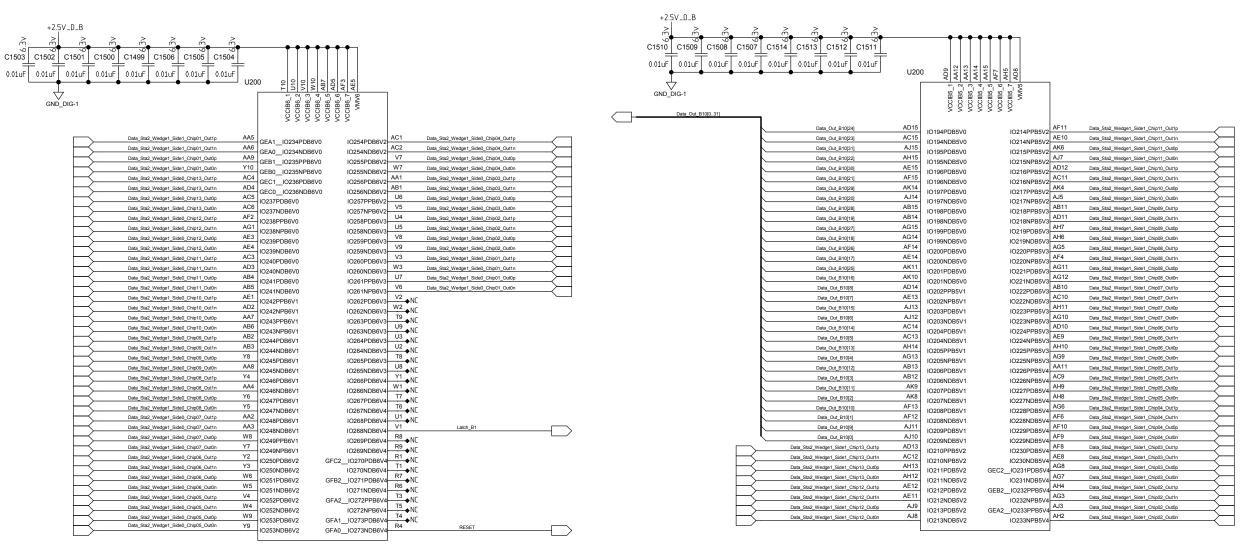




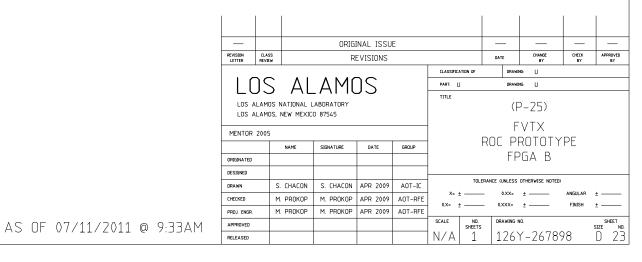


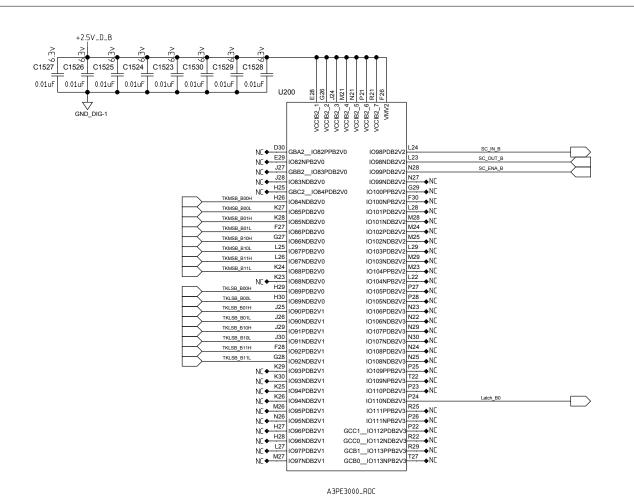


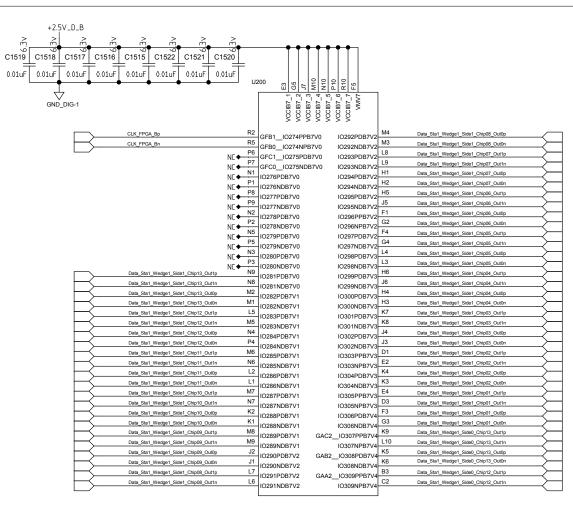


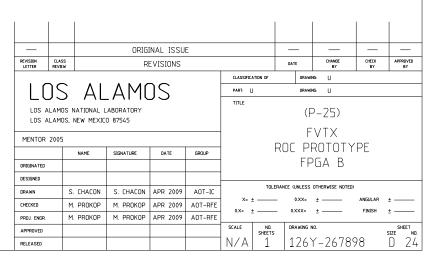


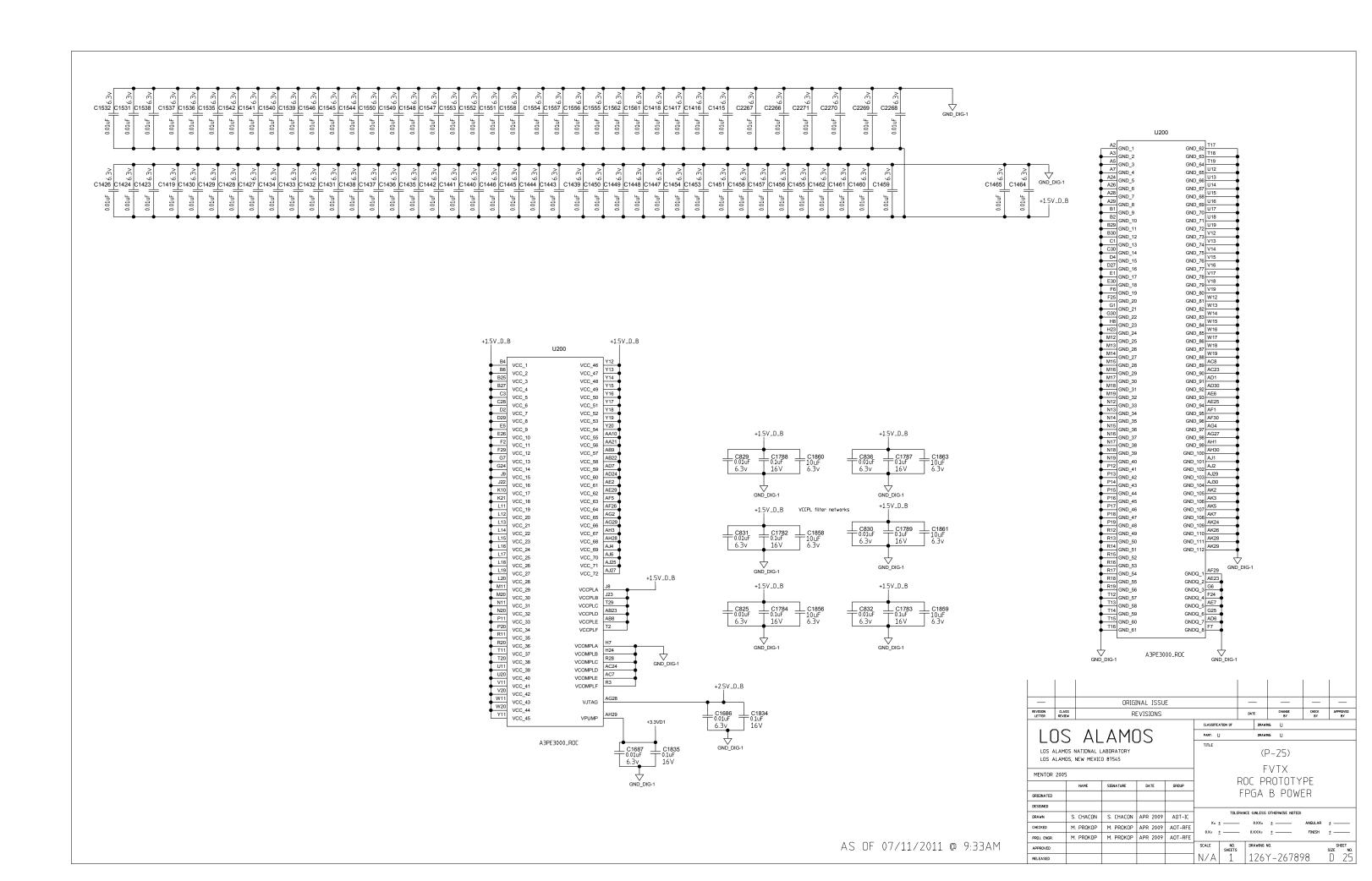
A3PE3000\_ROC

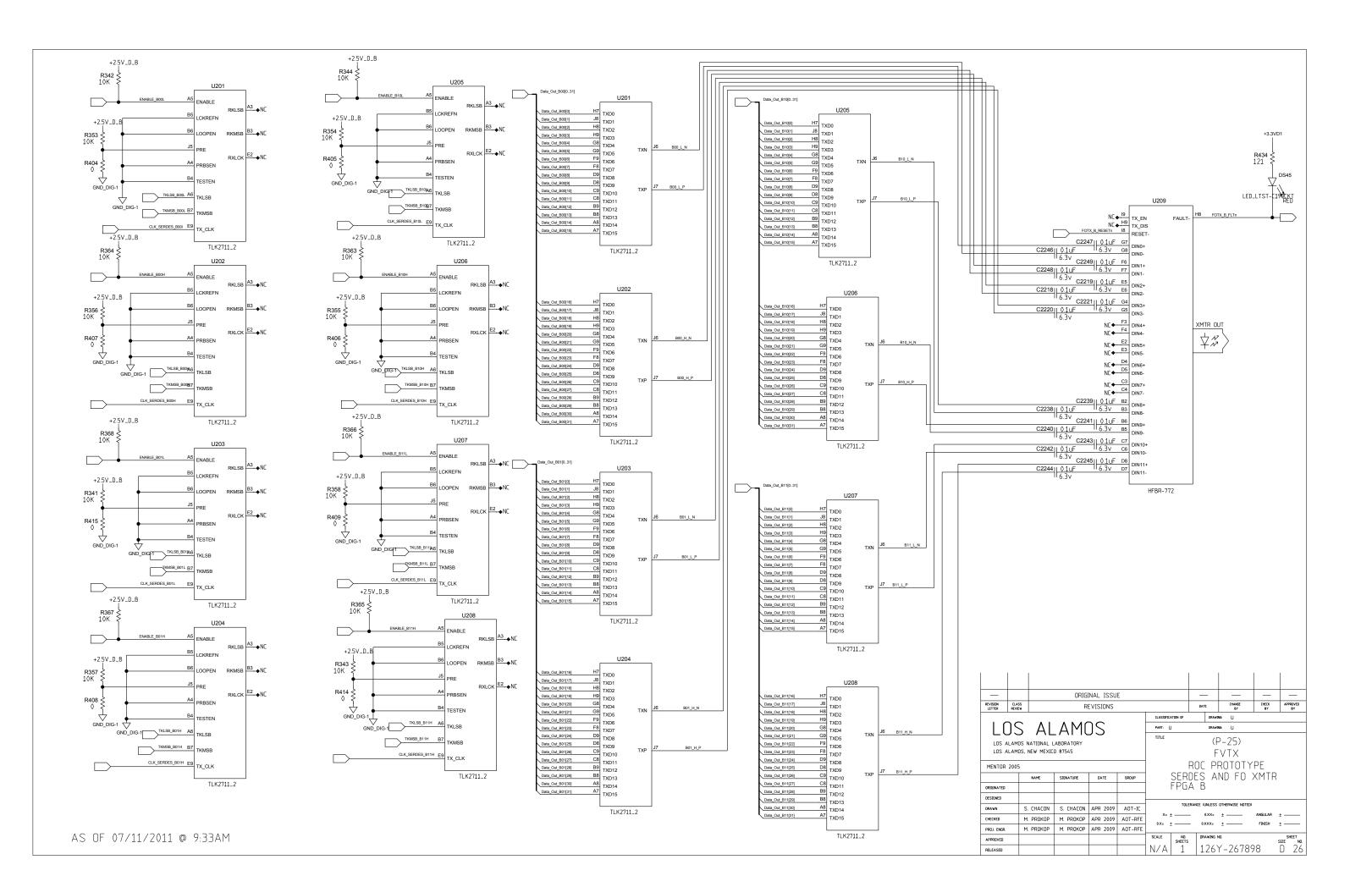


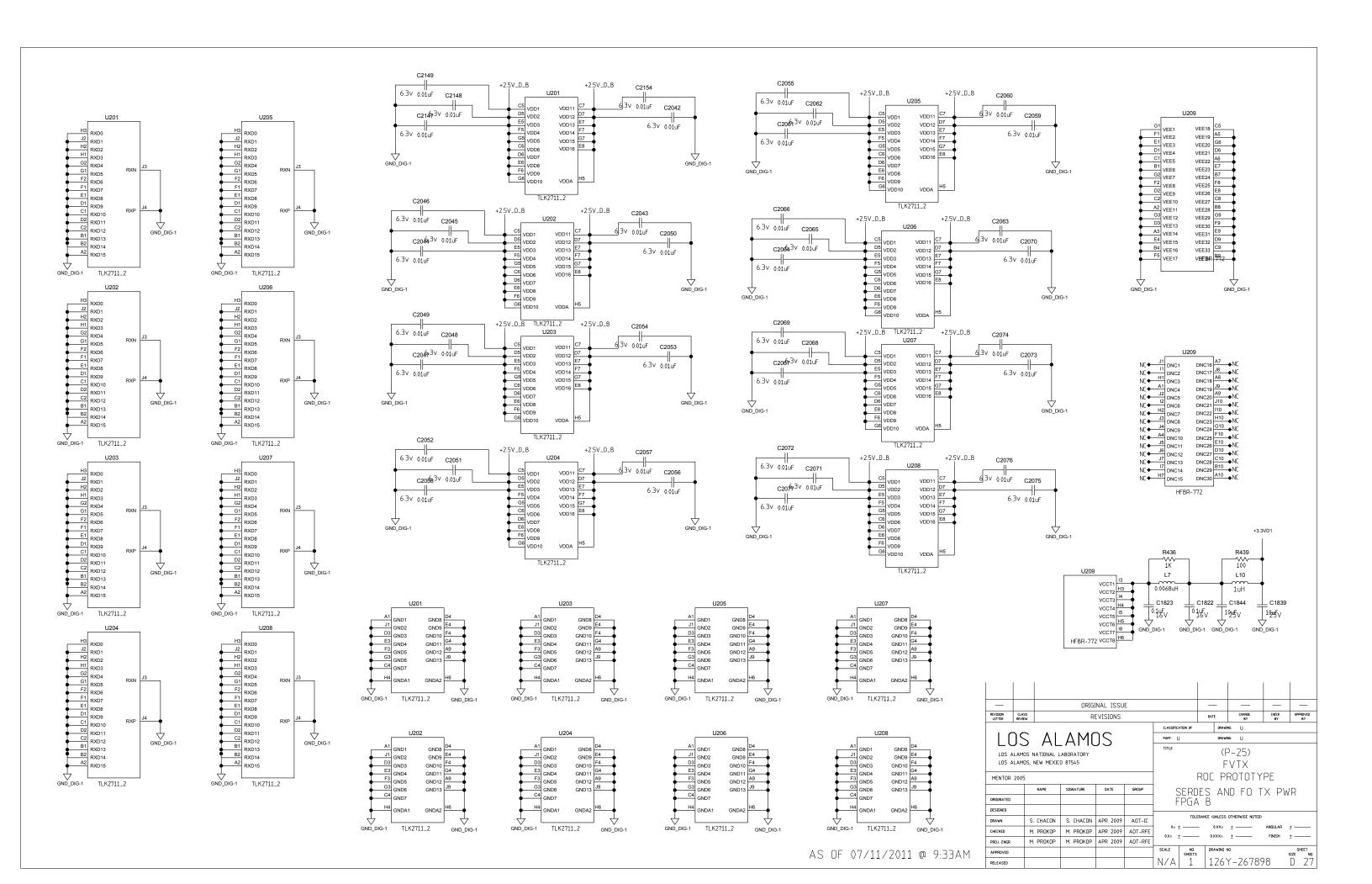


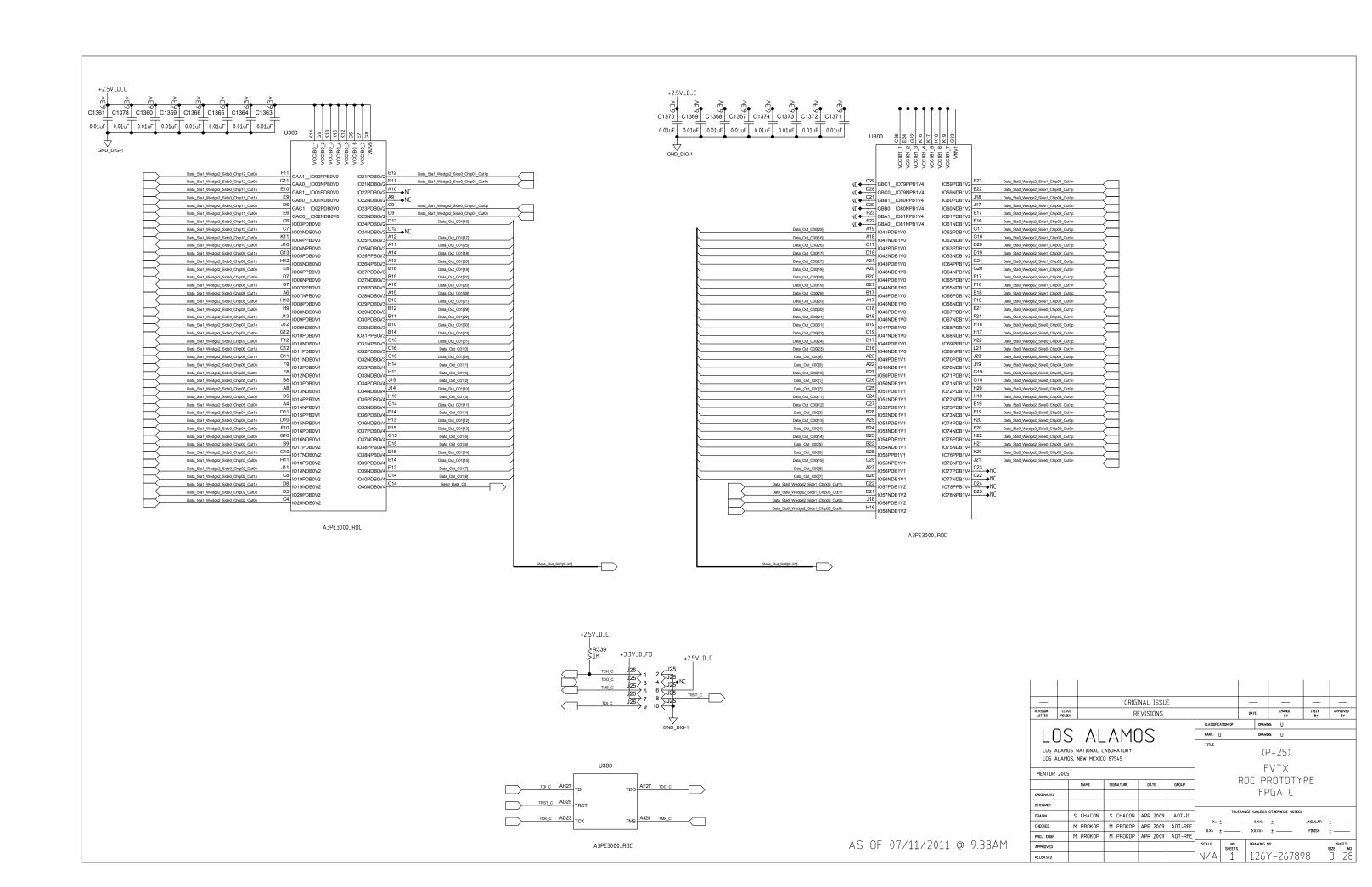


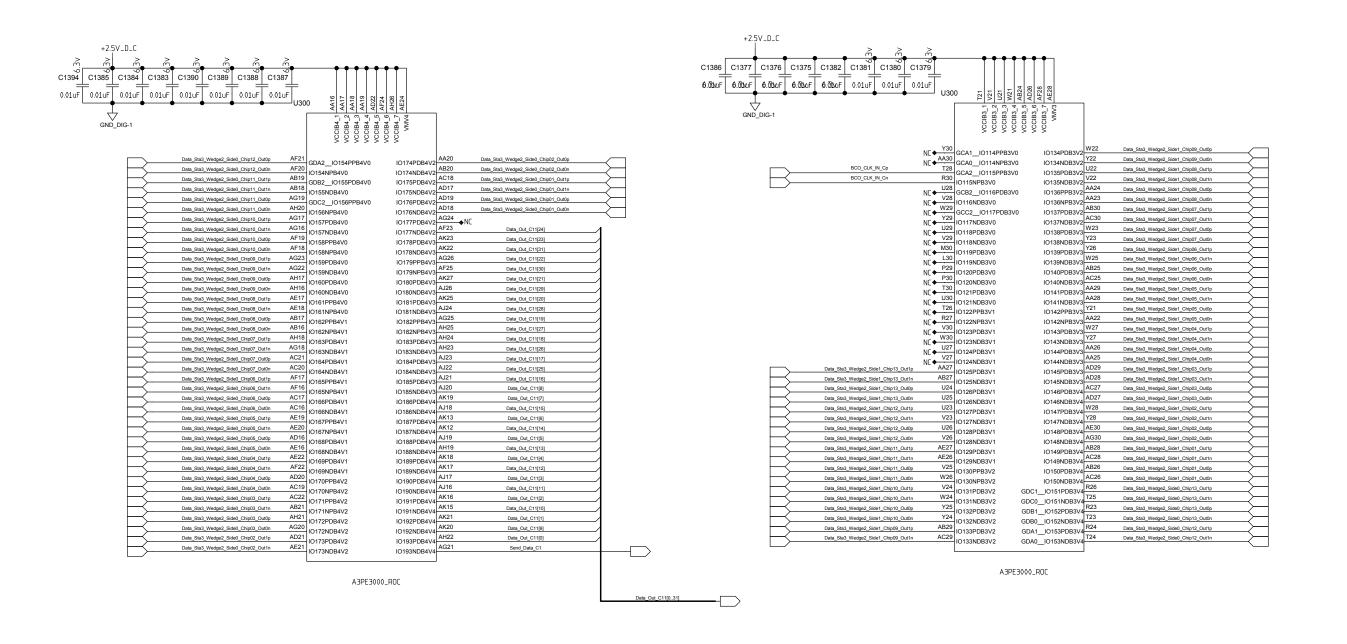


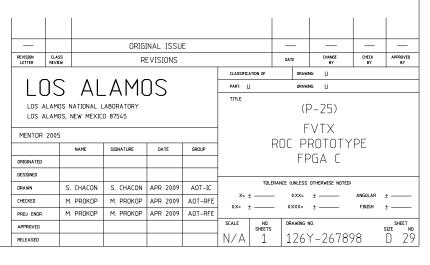


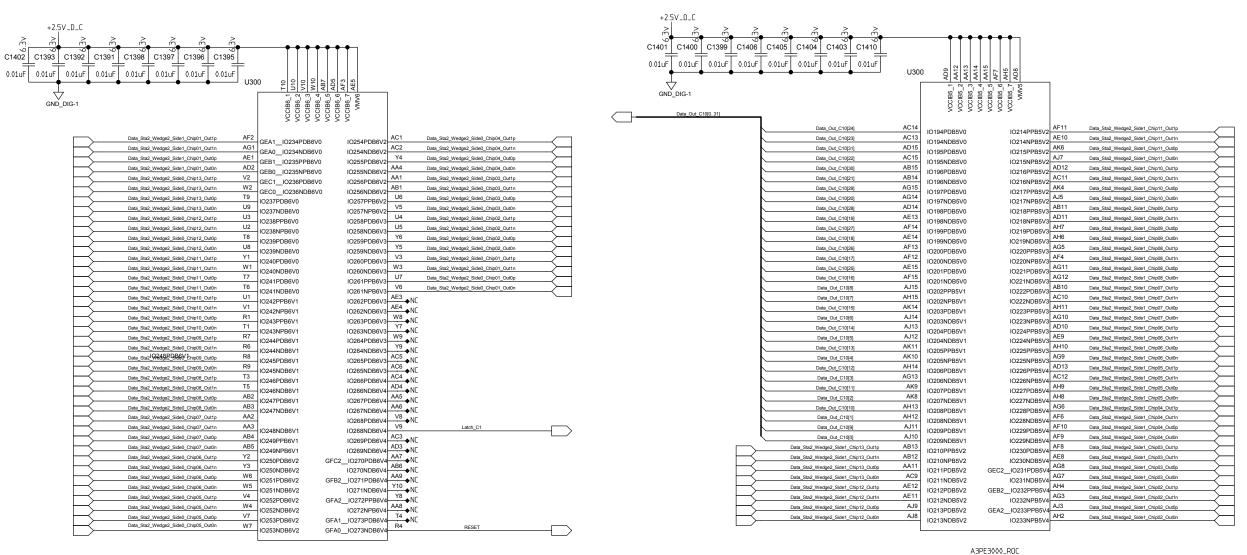


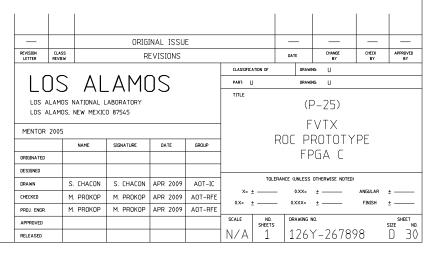


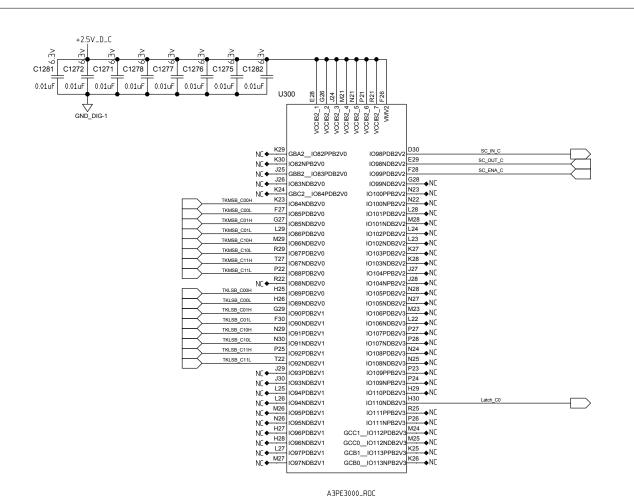


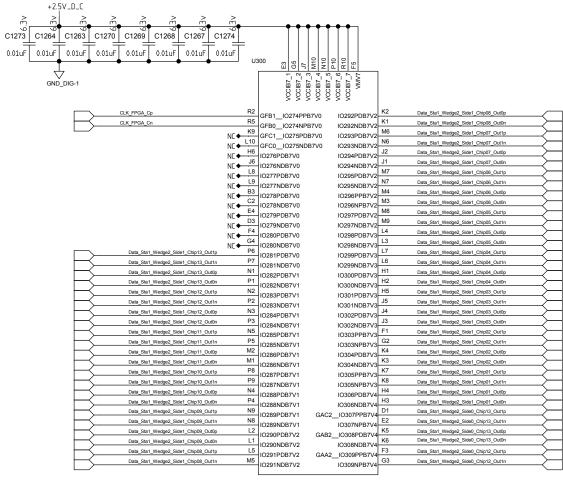


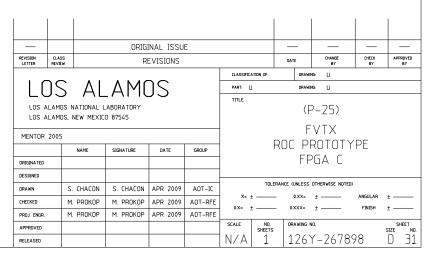


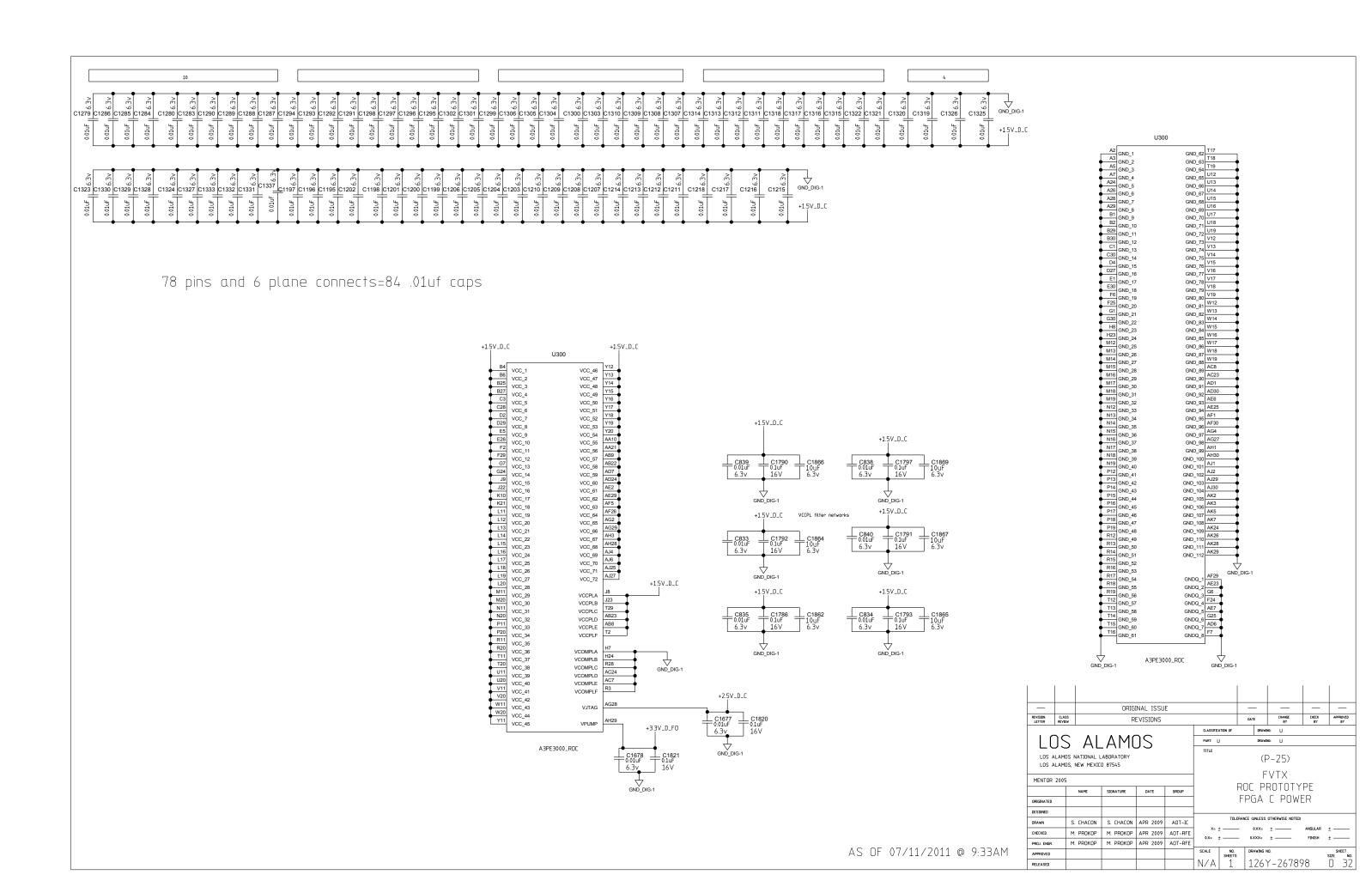


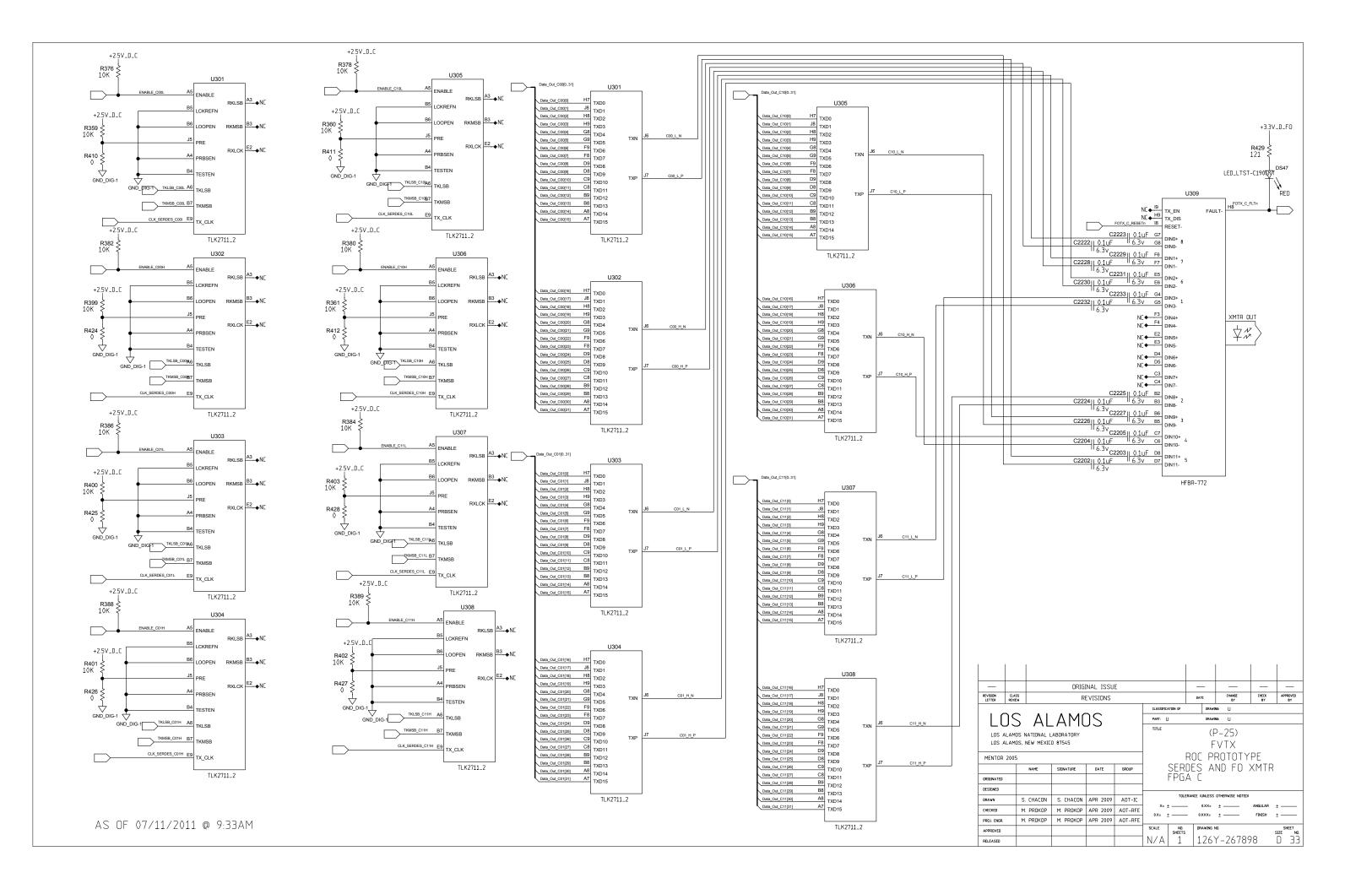


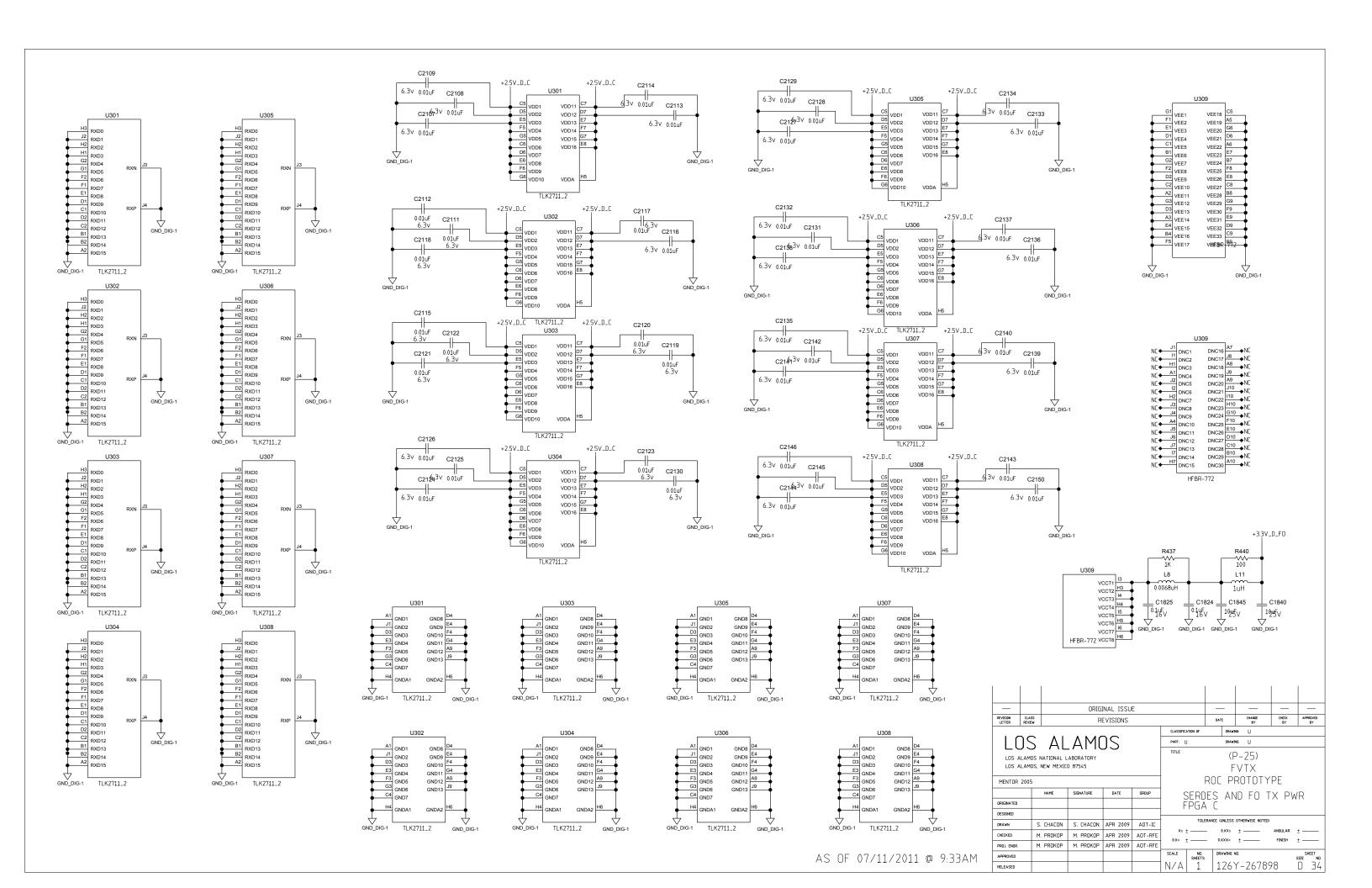


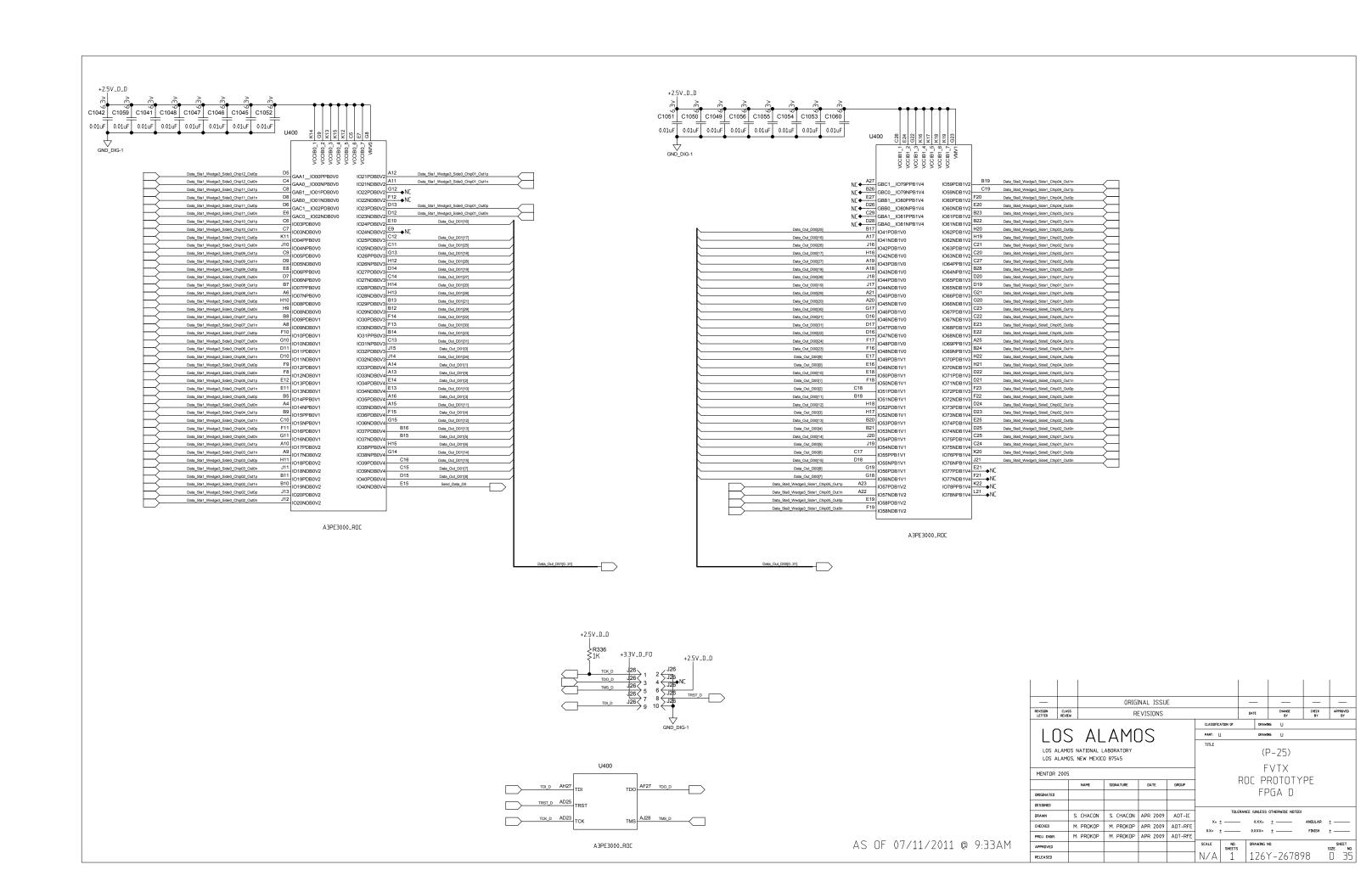


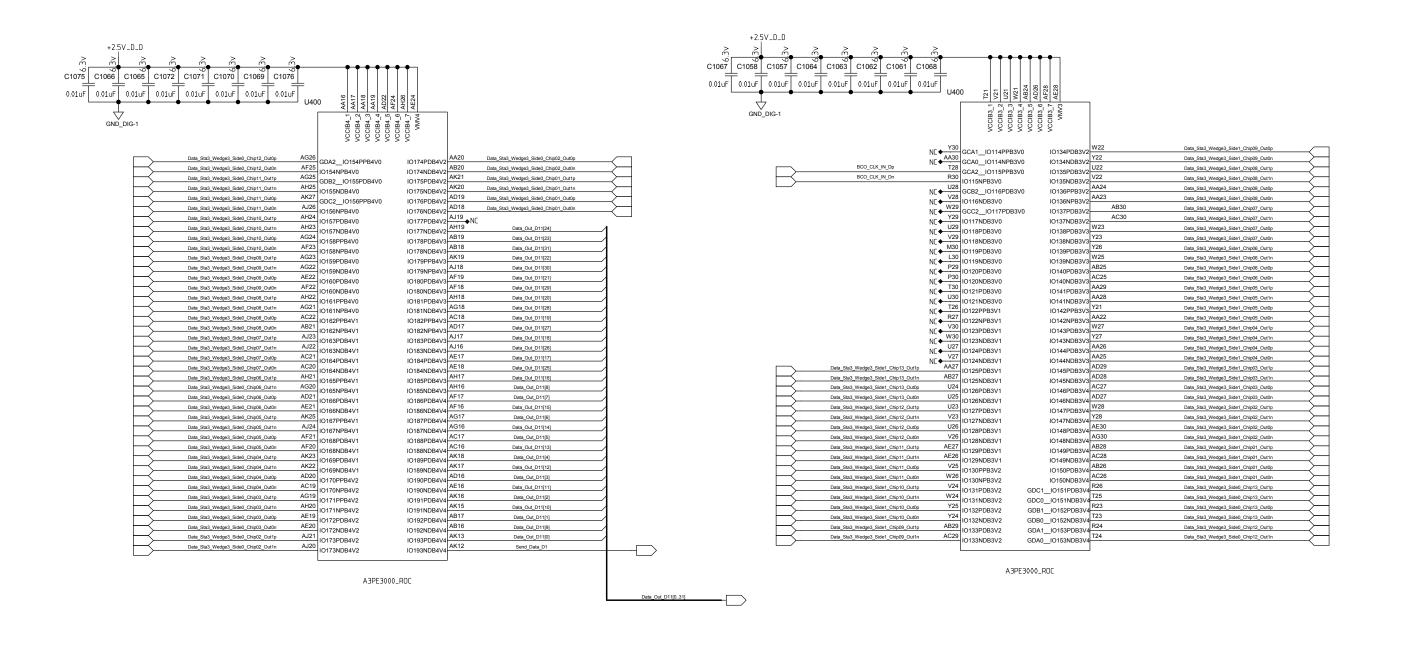


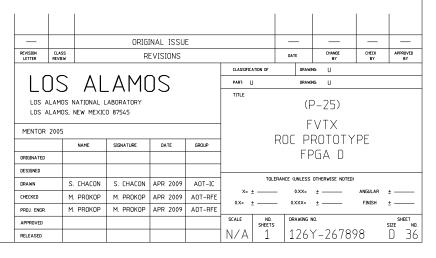


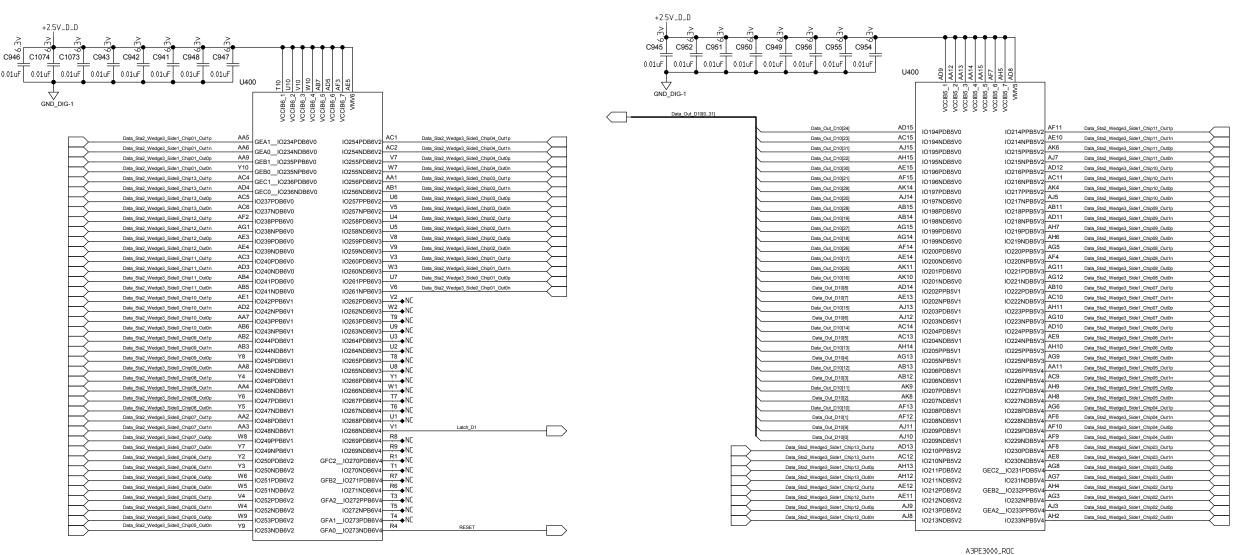




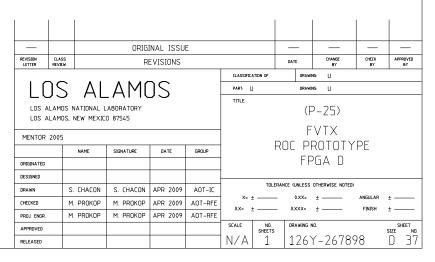


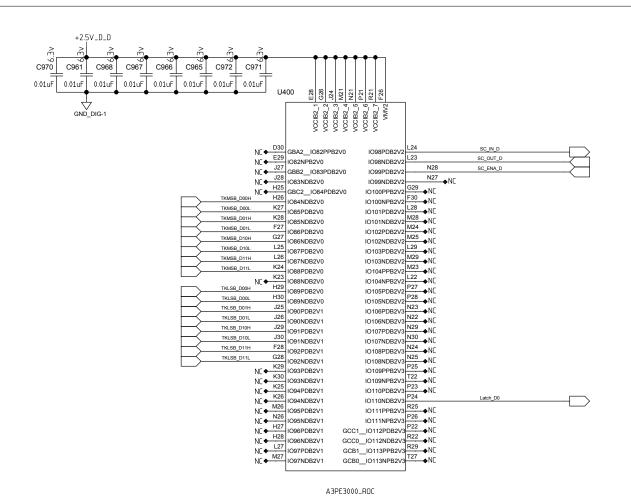


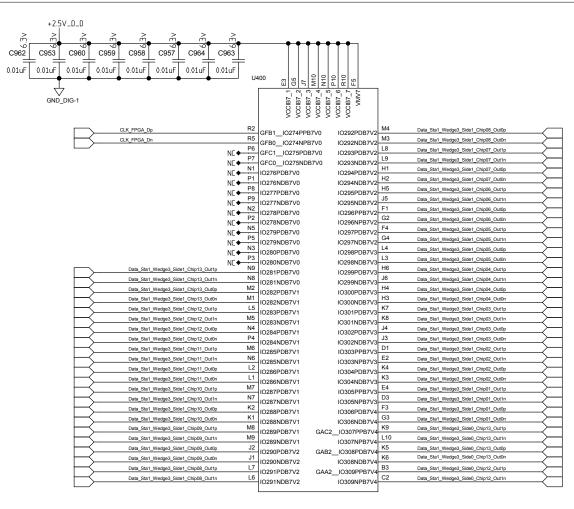




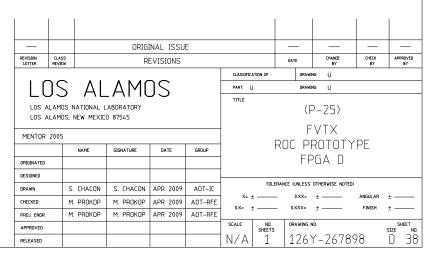
A3PE3000\_ROC

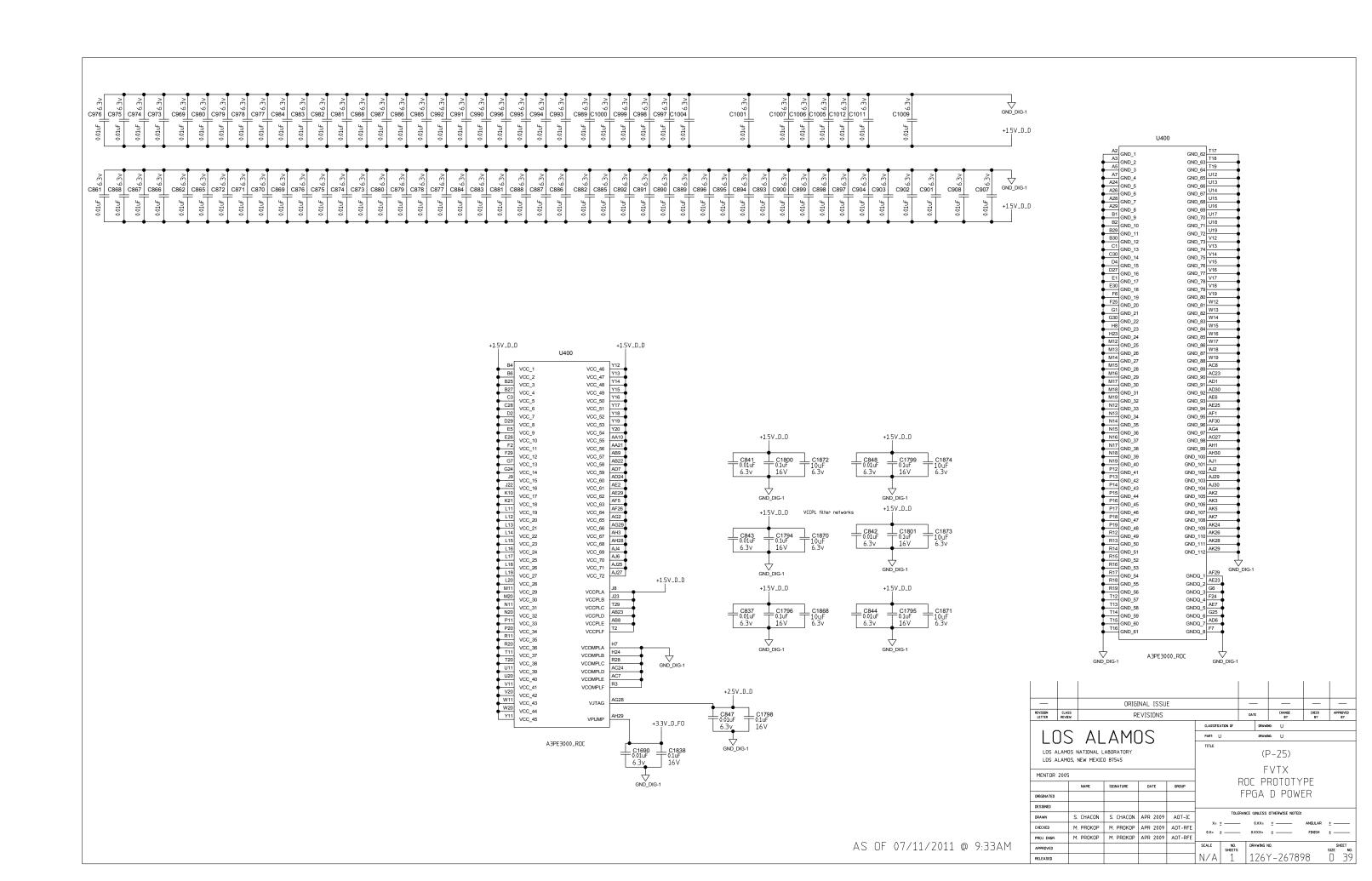


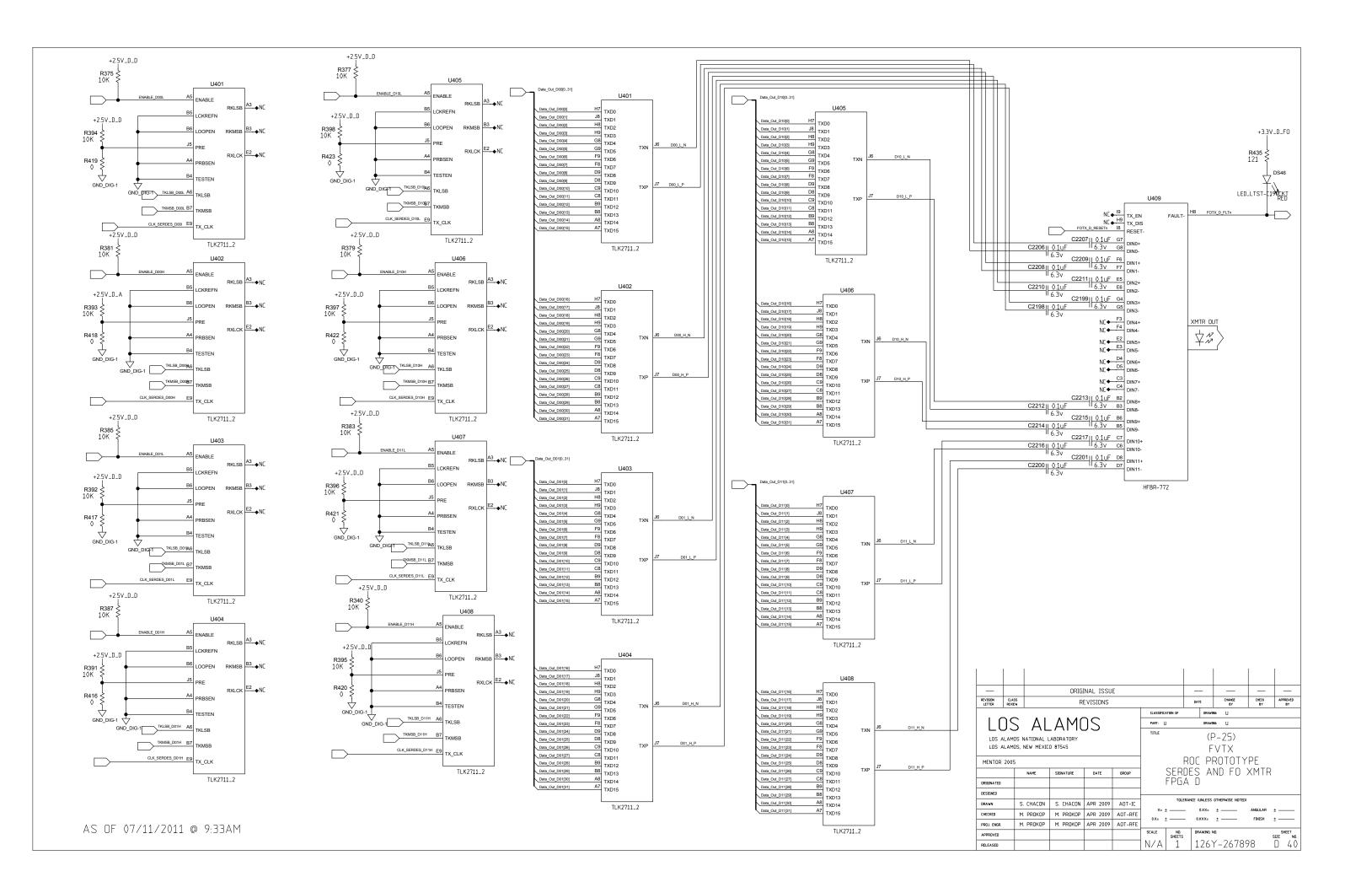


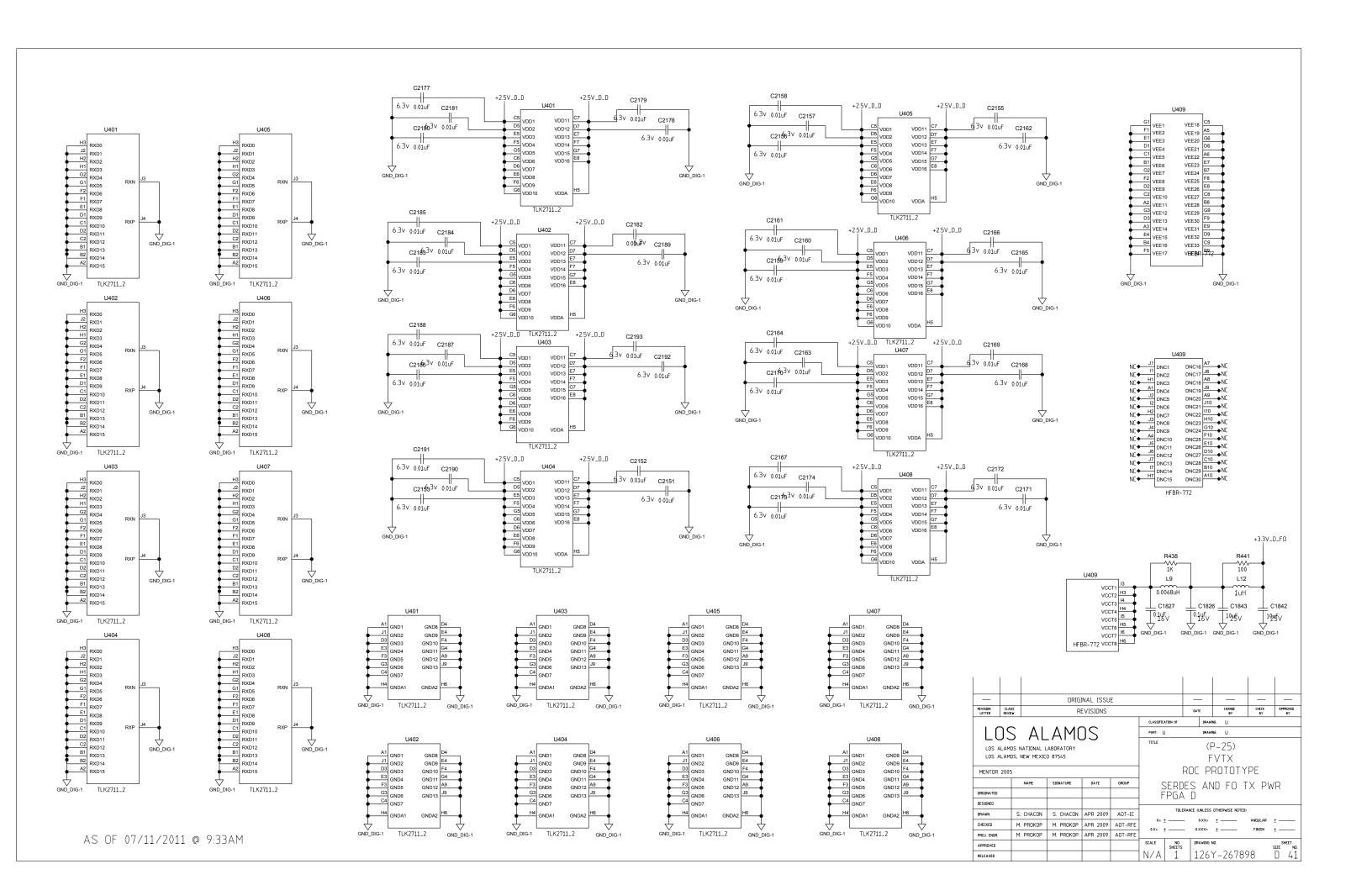


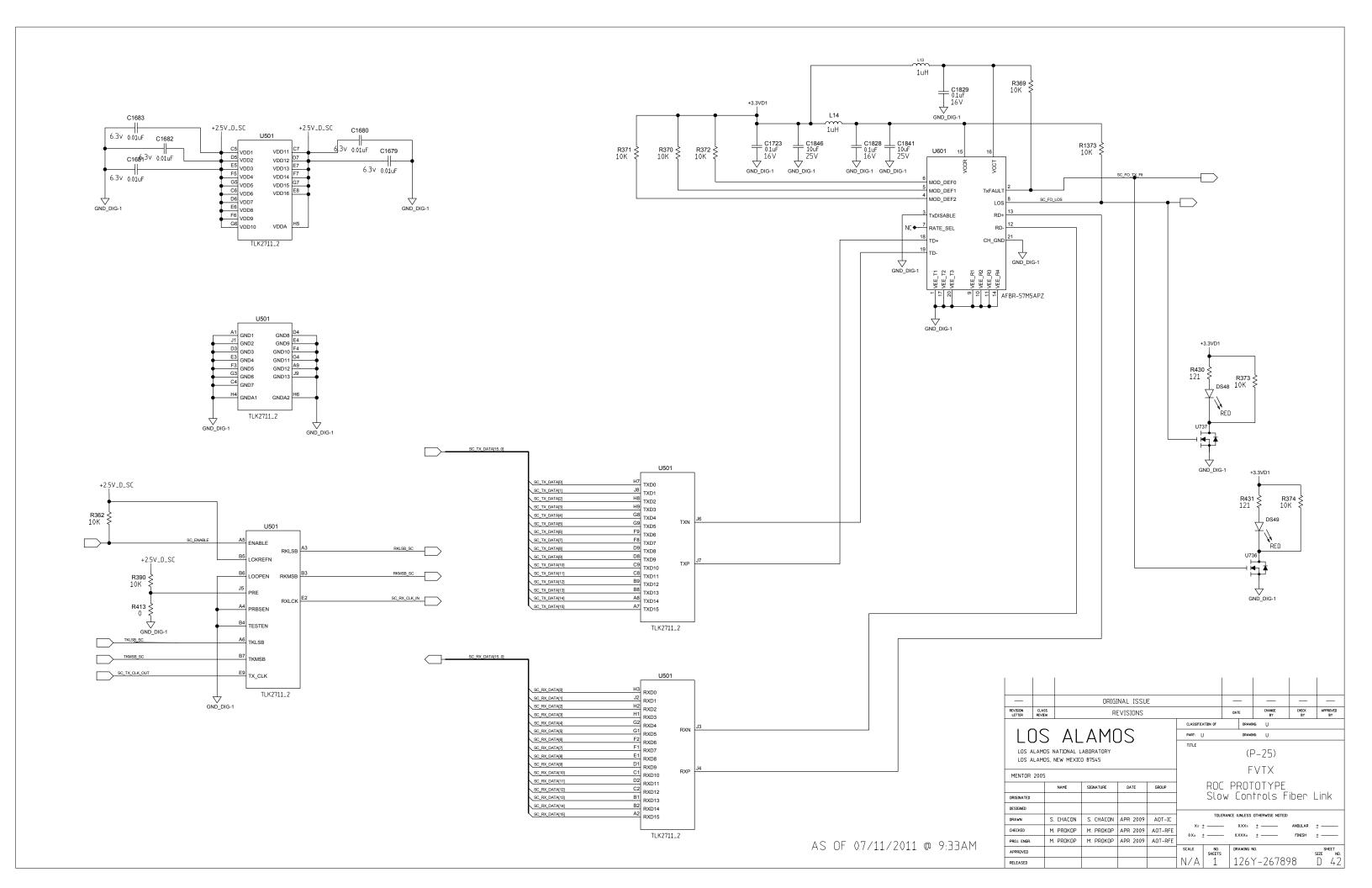
A3PE3000\_ROC

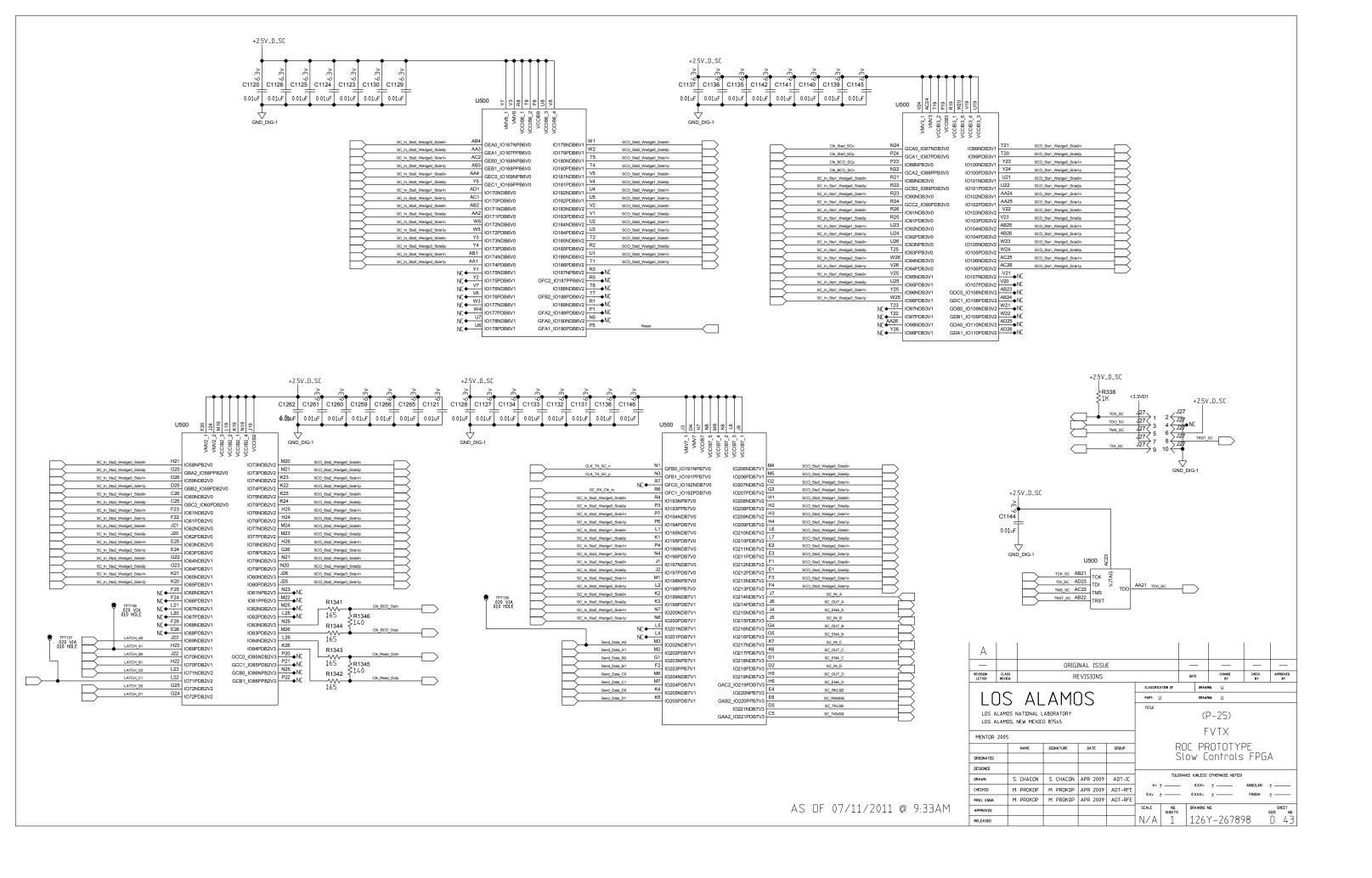


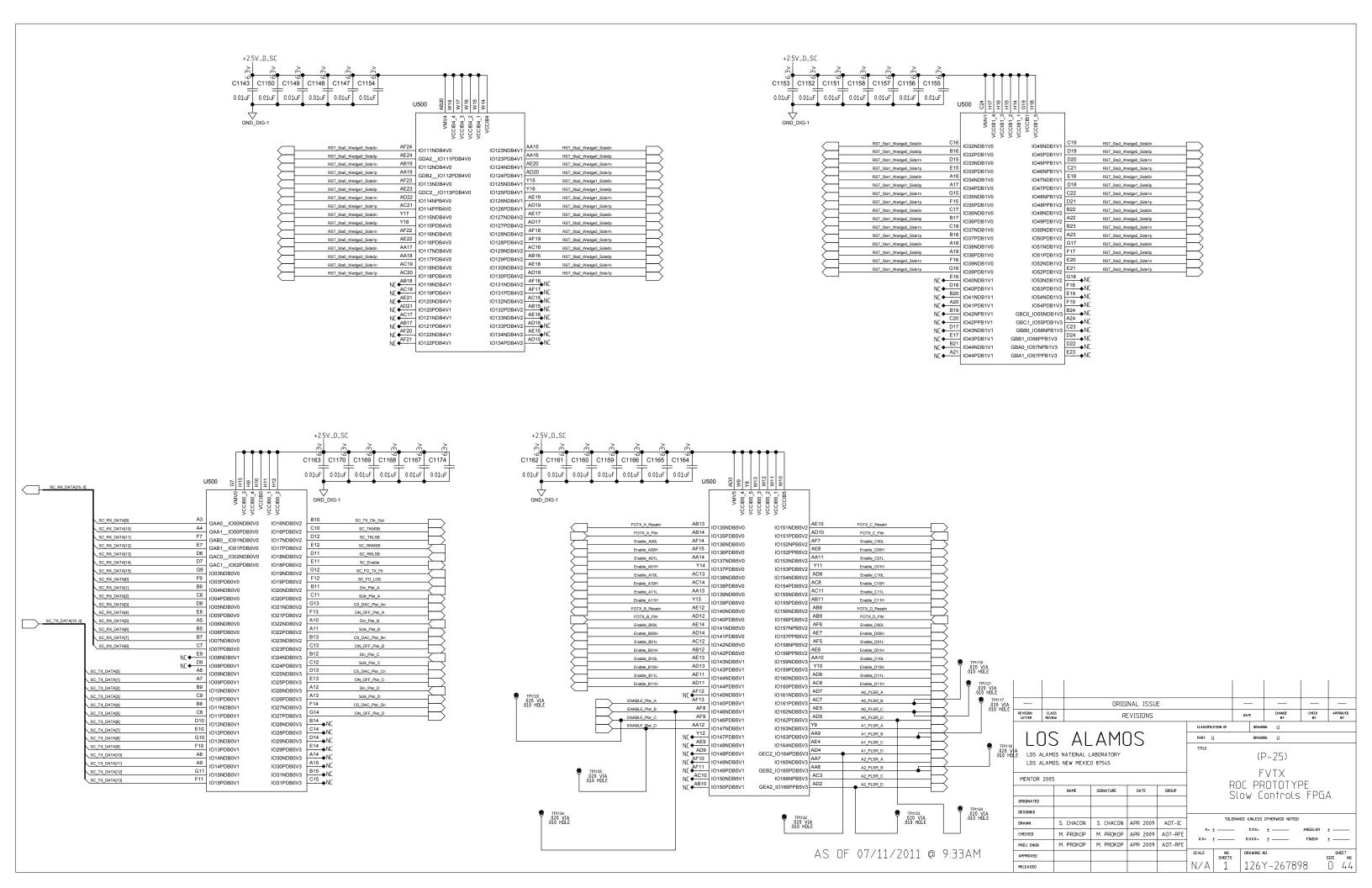


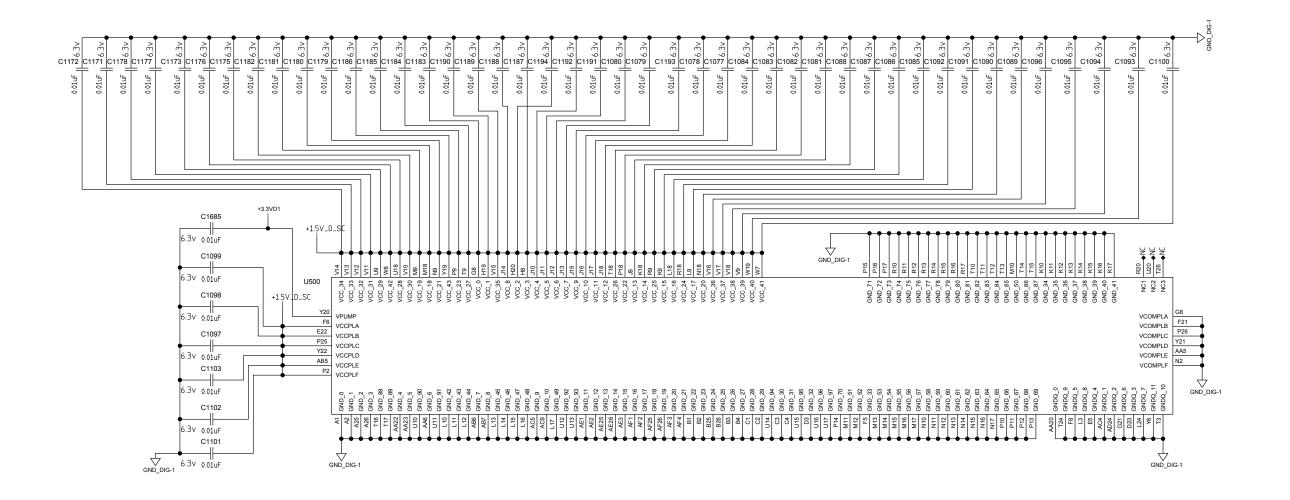


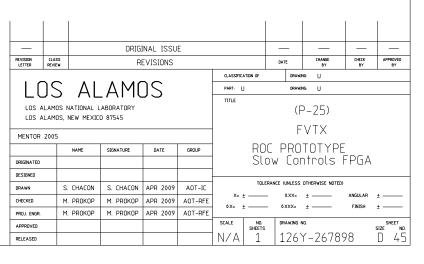


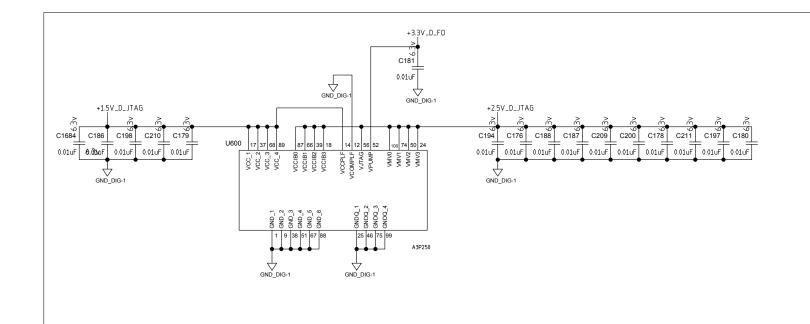


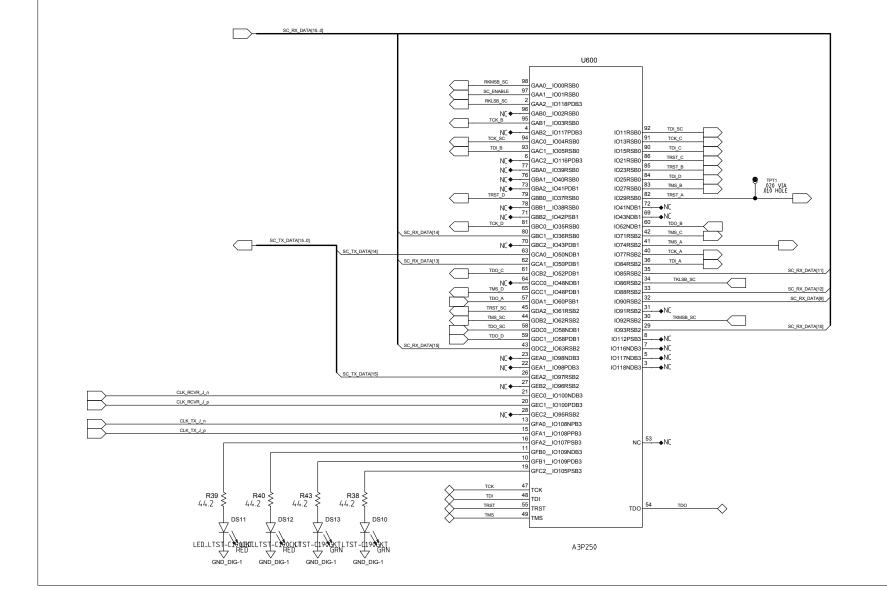


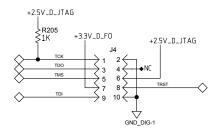




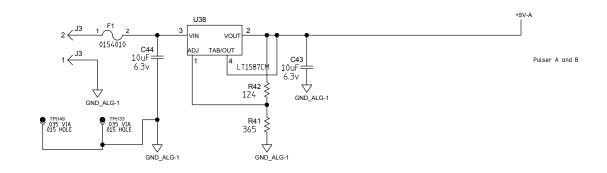


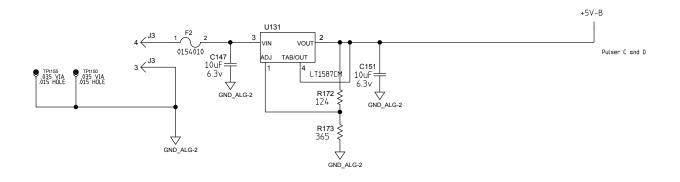


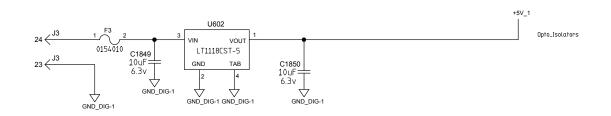


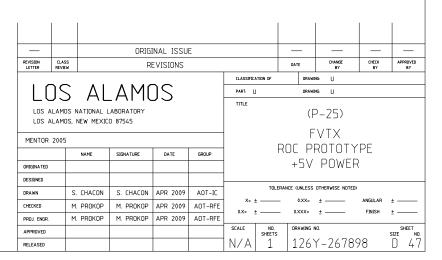


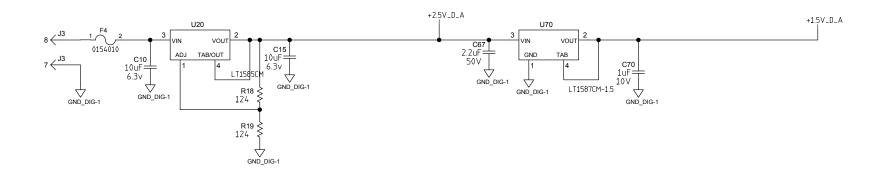
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	REVISION LETTER	CLASS REVIEW		RI	EVISIONS				DAT	E	CHANGE BY	CHECK BY	AF	PROVED BY
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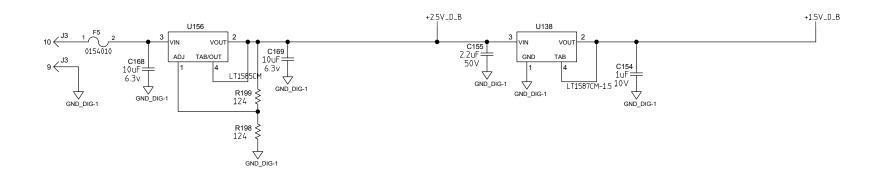


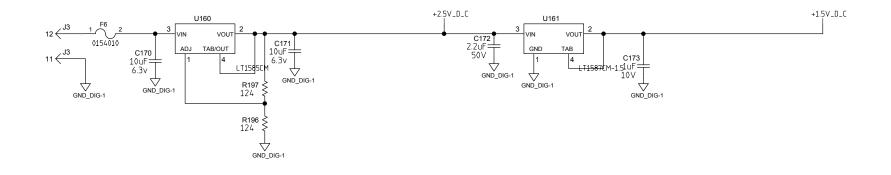


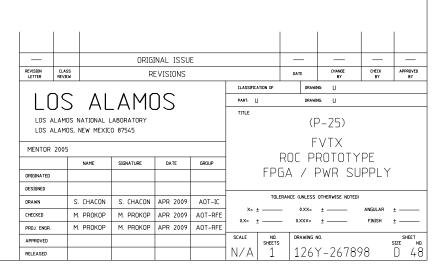


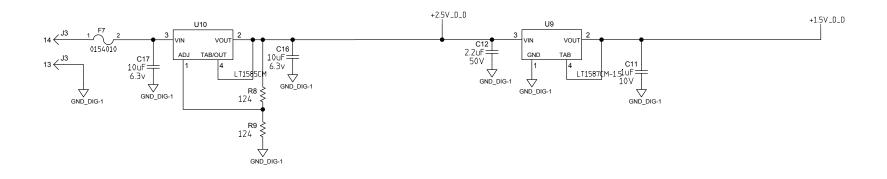


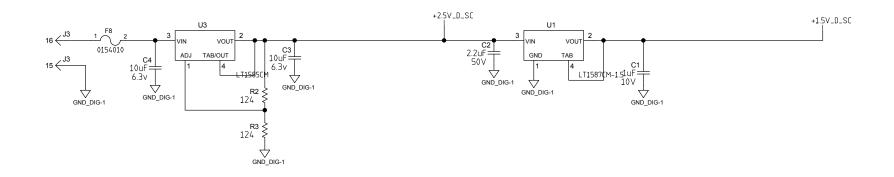


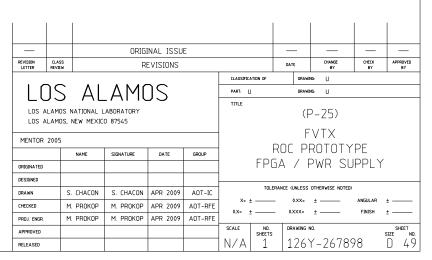


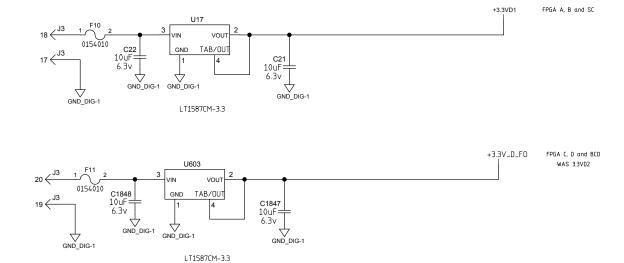


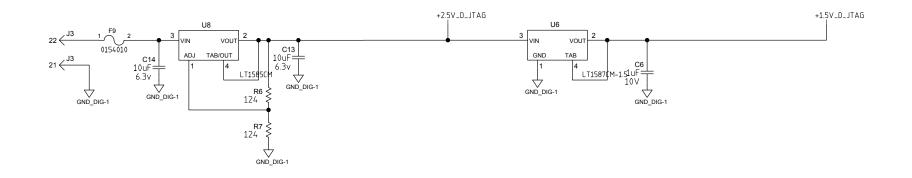


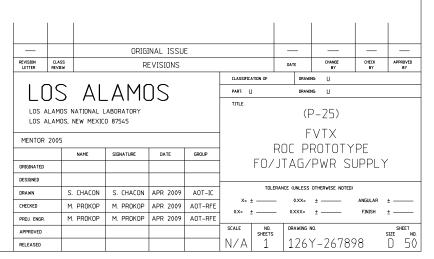


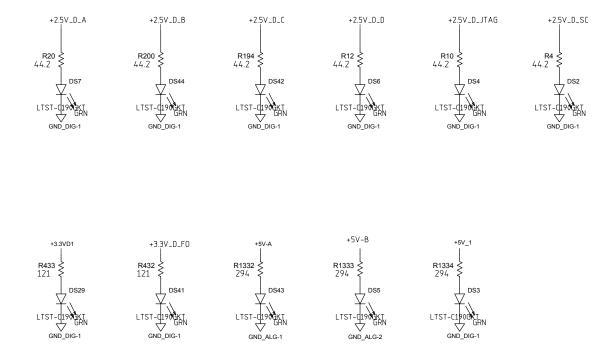


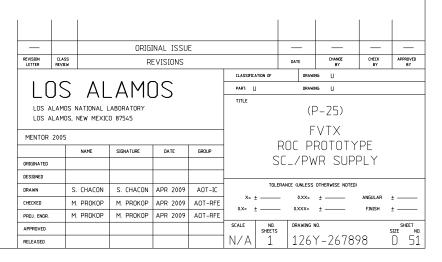


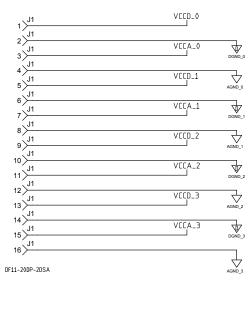


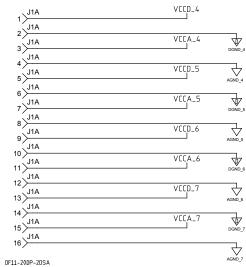


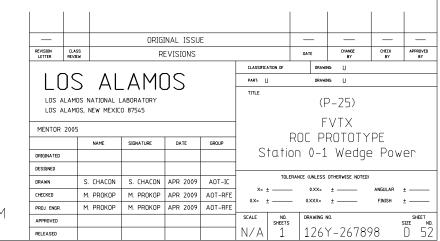


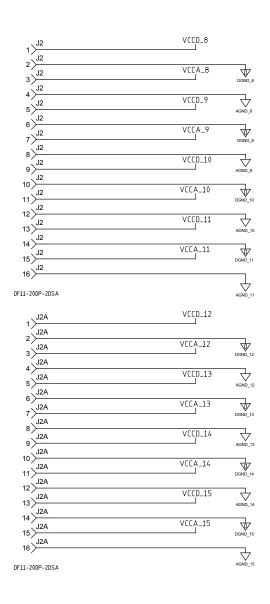


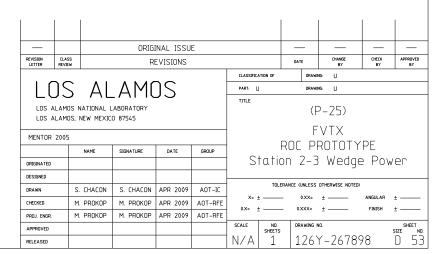


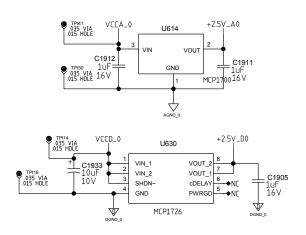


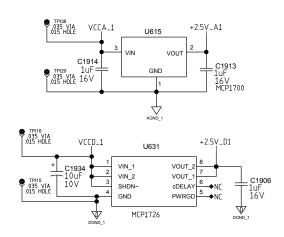


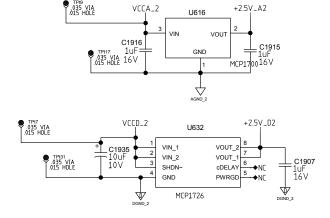


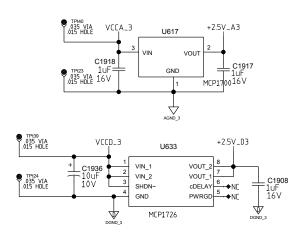


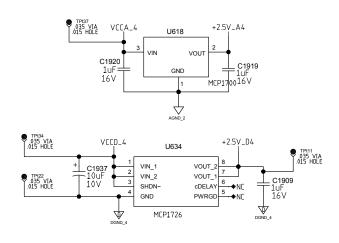


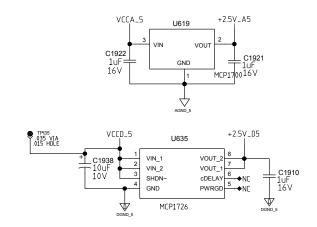


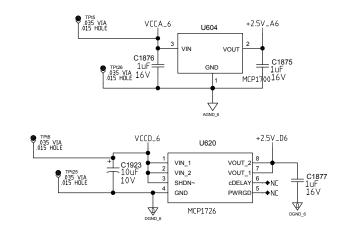


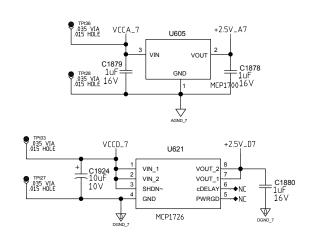


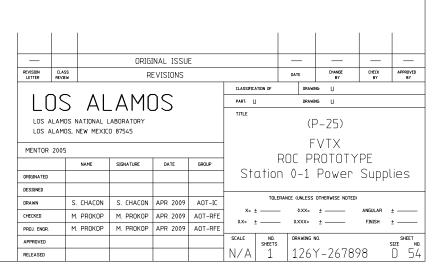


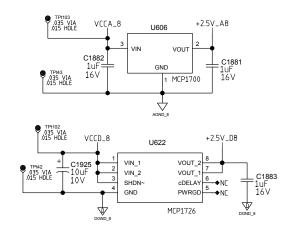


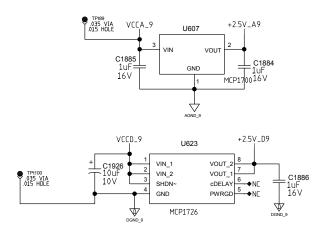


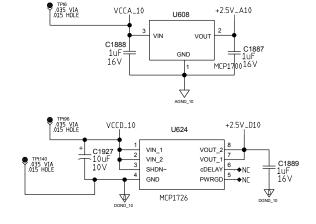


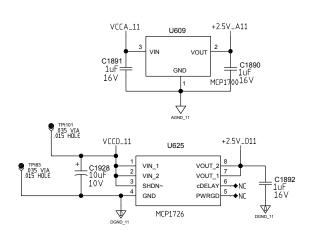


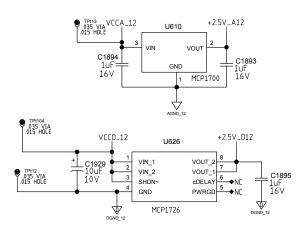


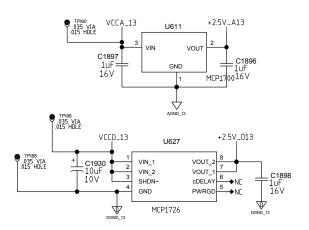


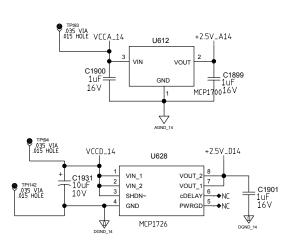


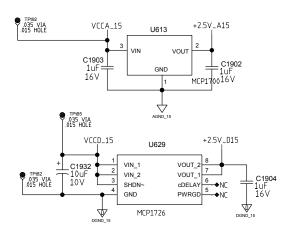


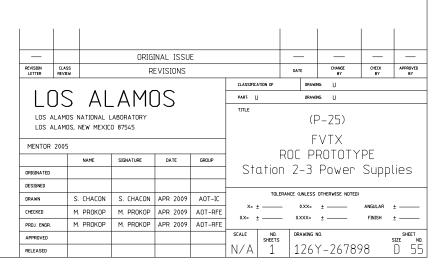


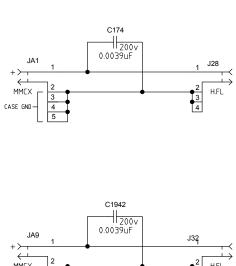


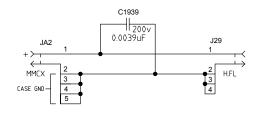


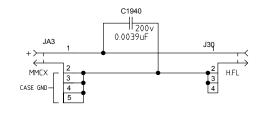


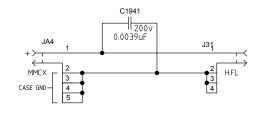


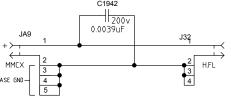


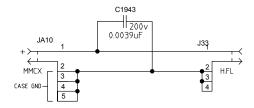


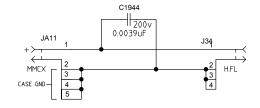


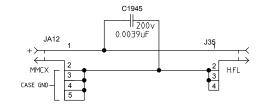


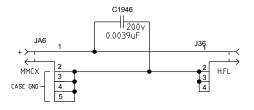


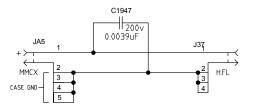


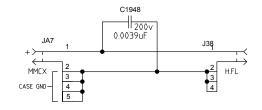


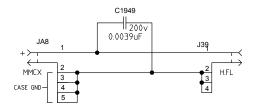


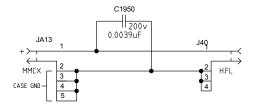


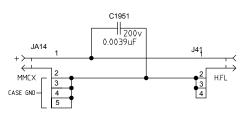


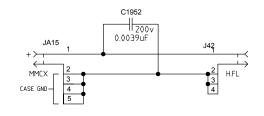


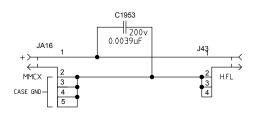




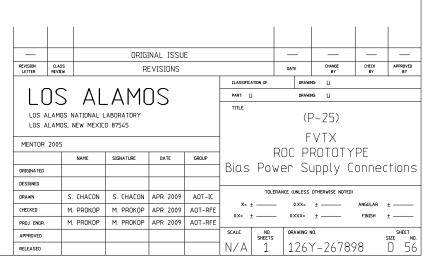


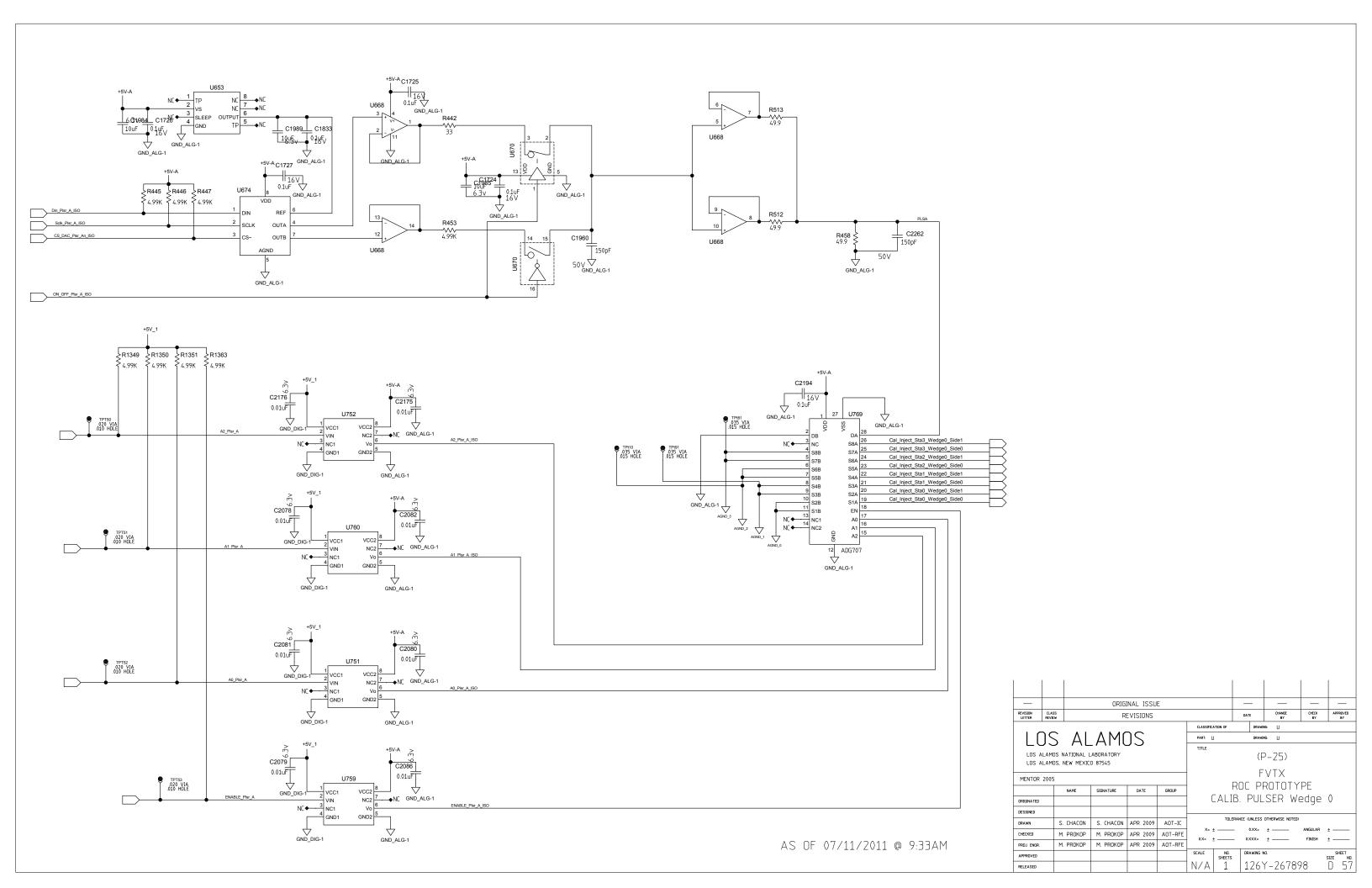


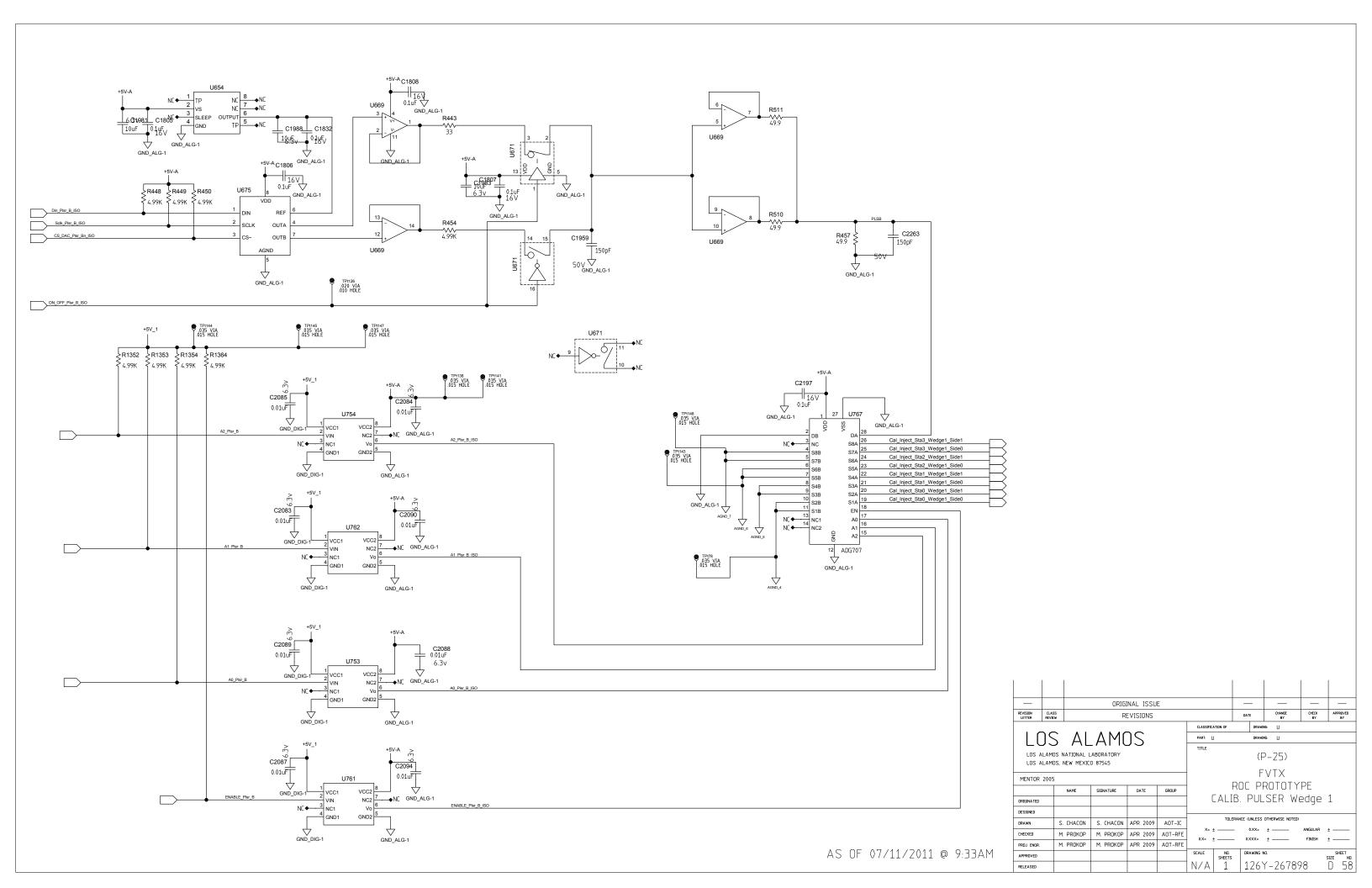


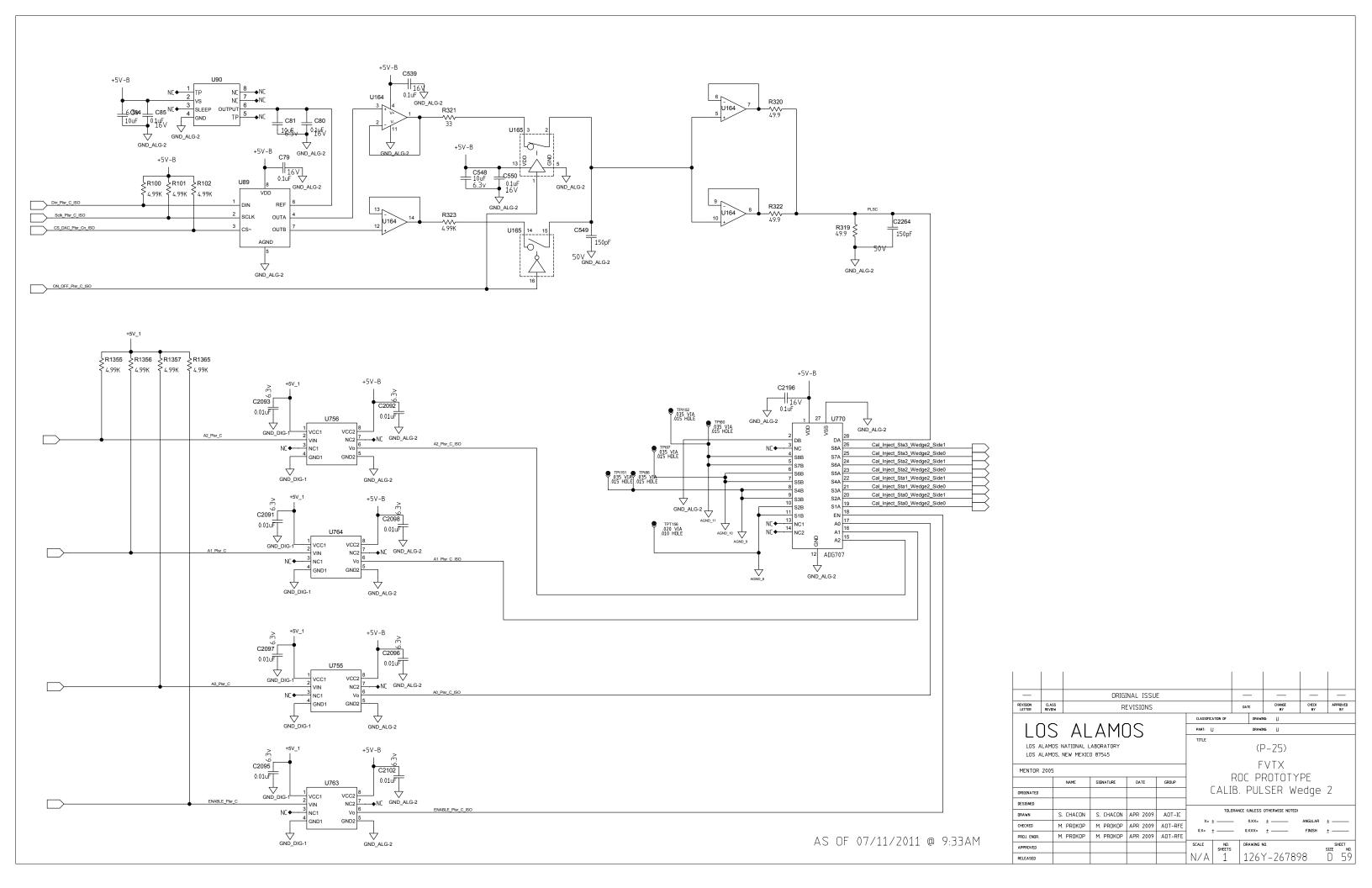


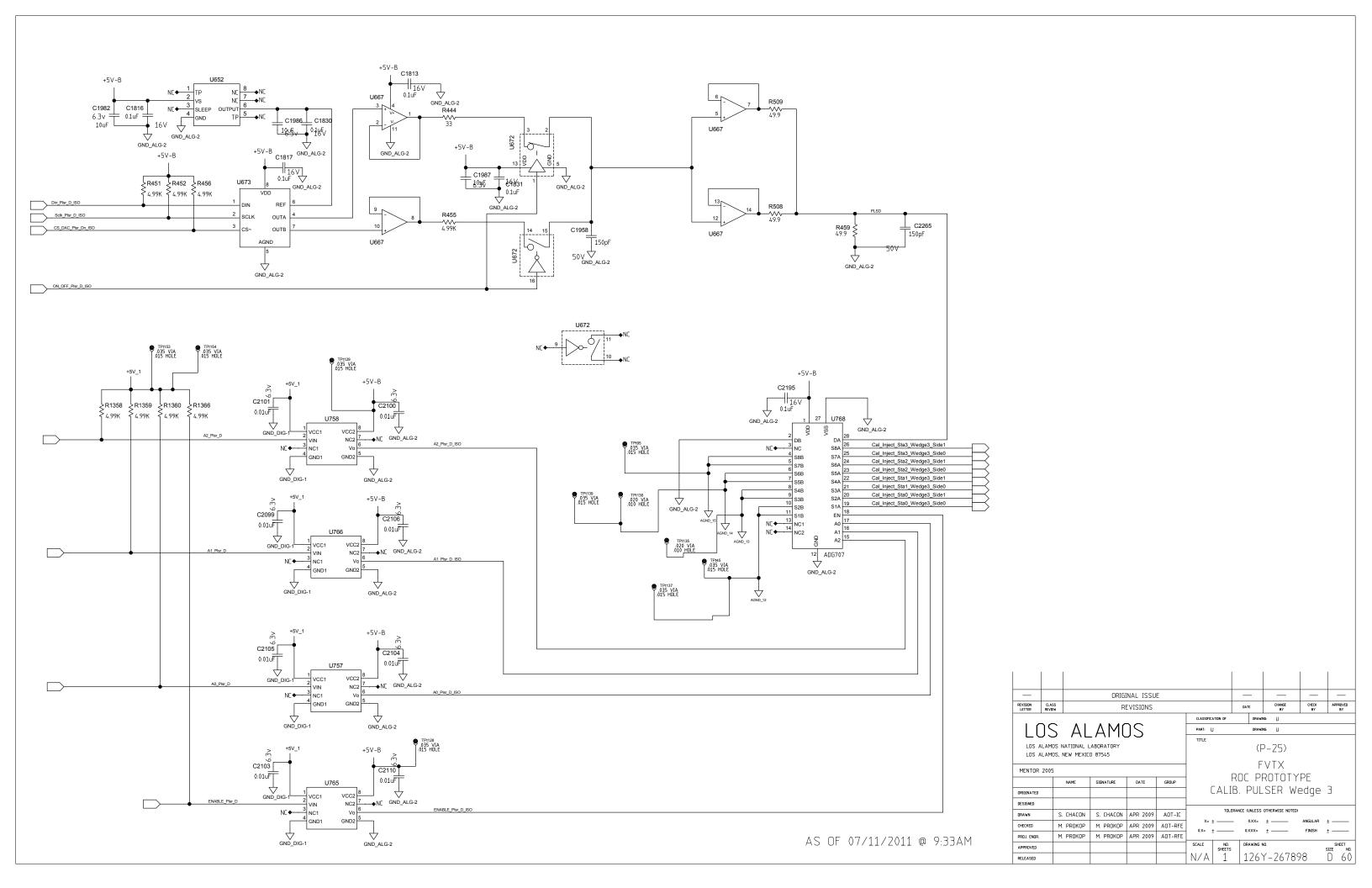
200vdc DETECTOR BIAS INPUT POWER

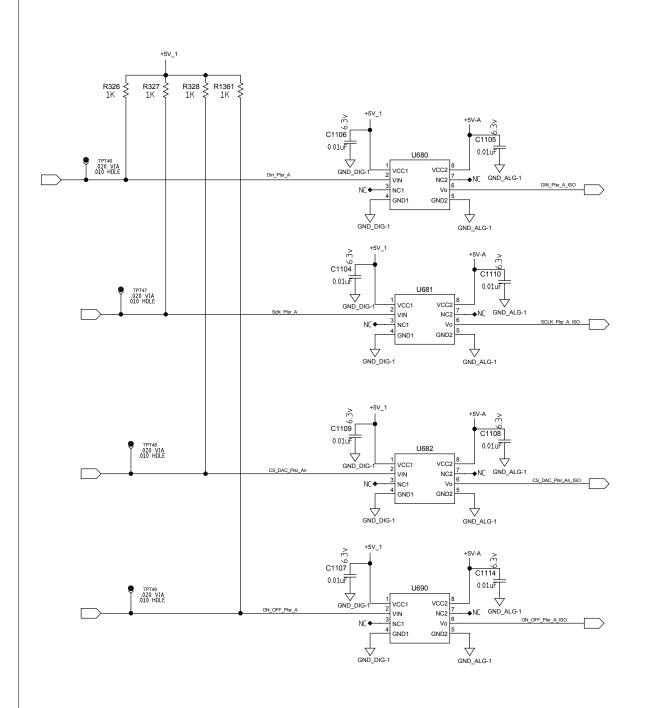


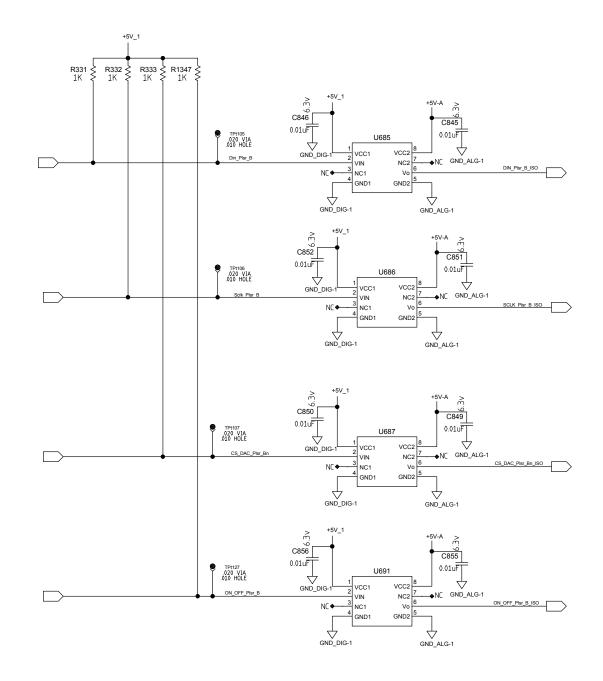


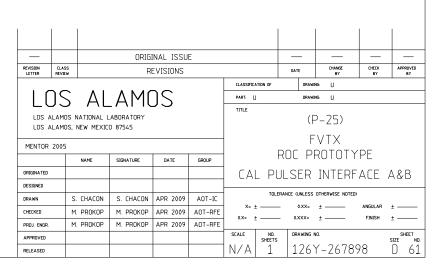


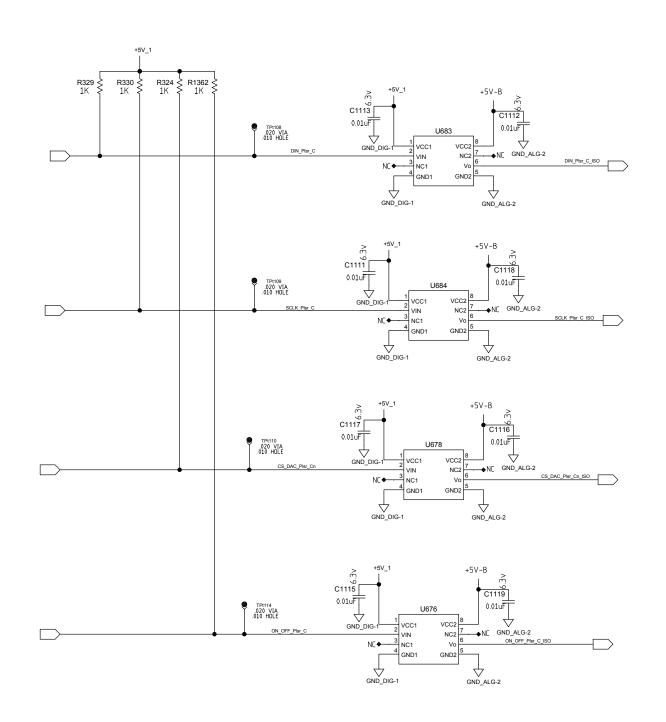


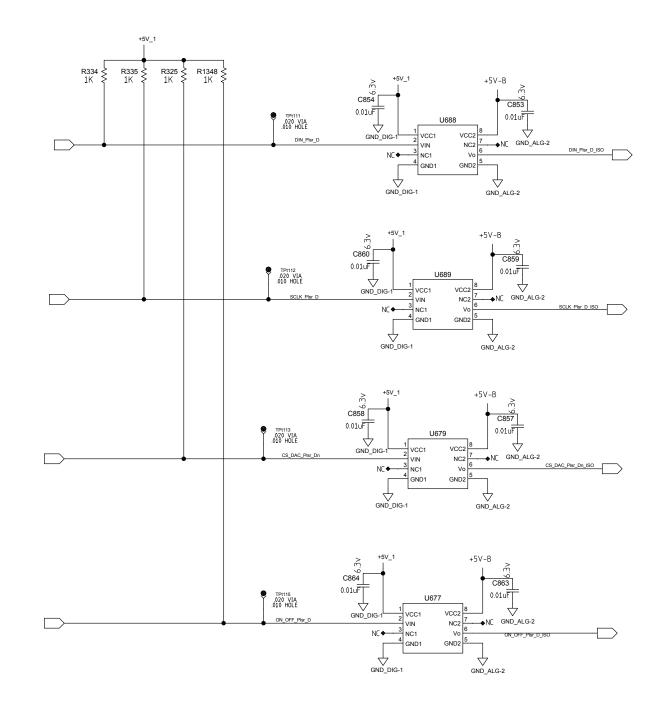


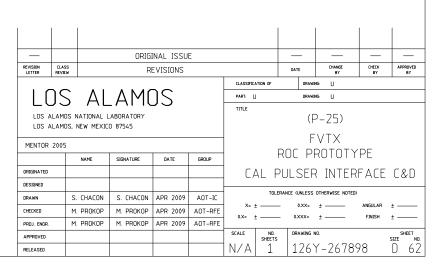


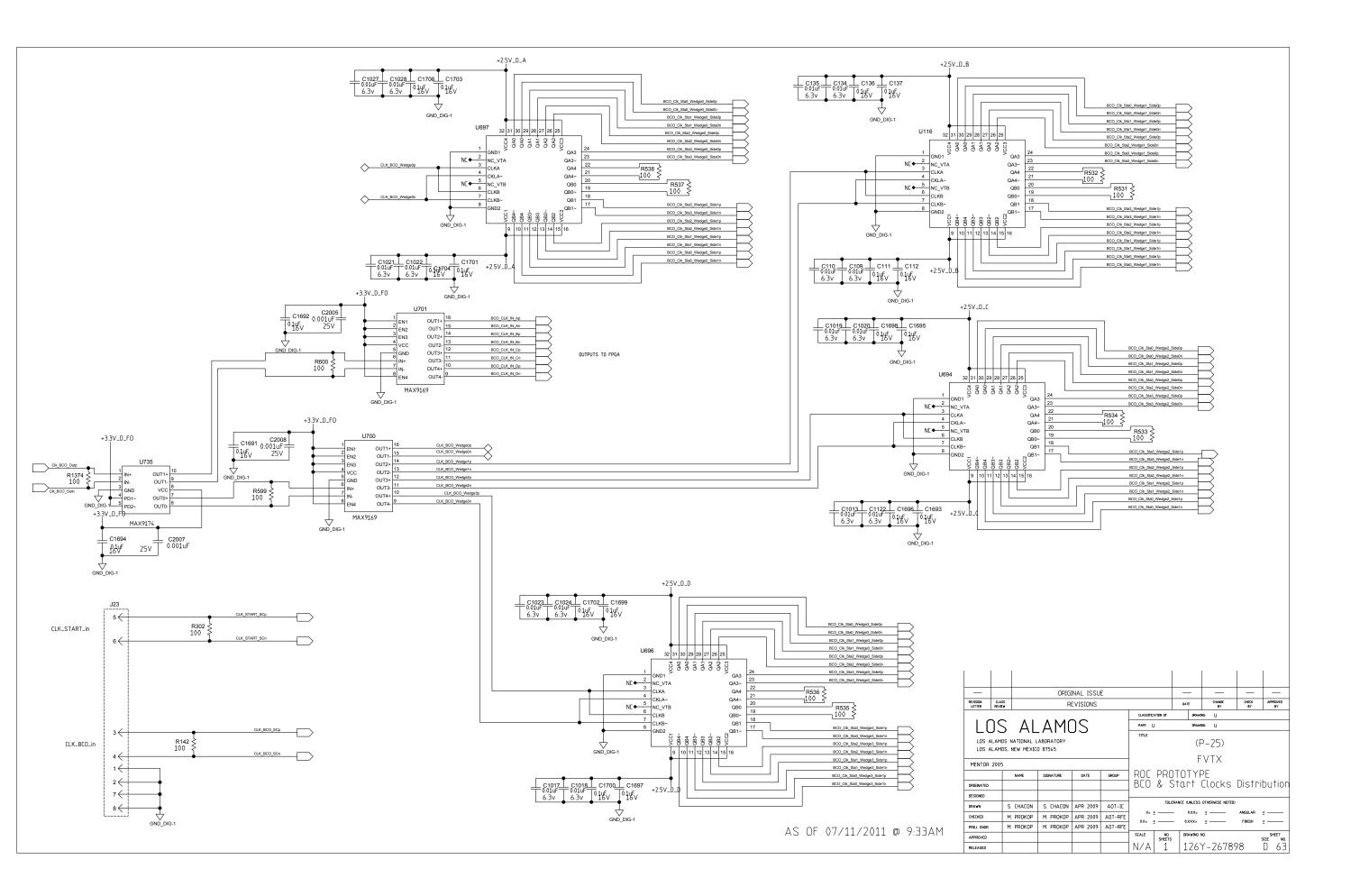


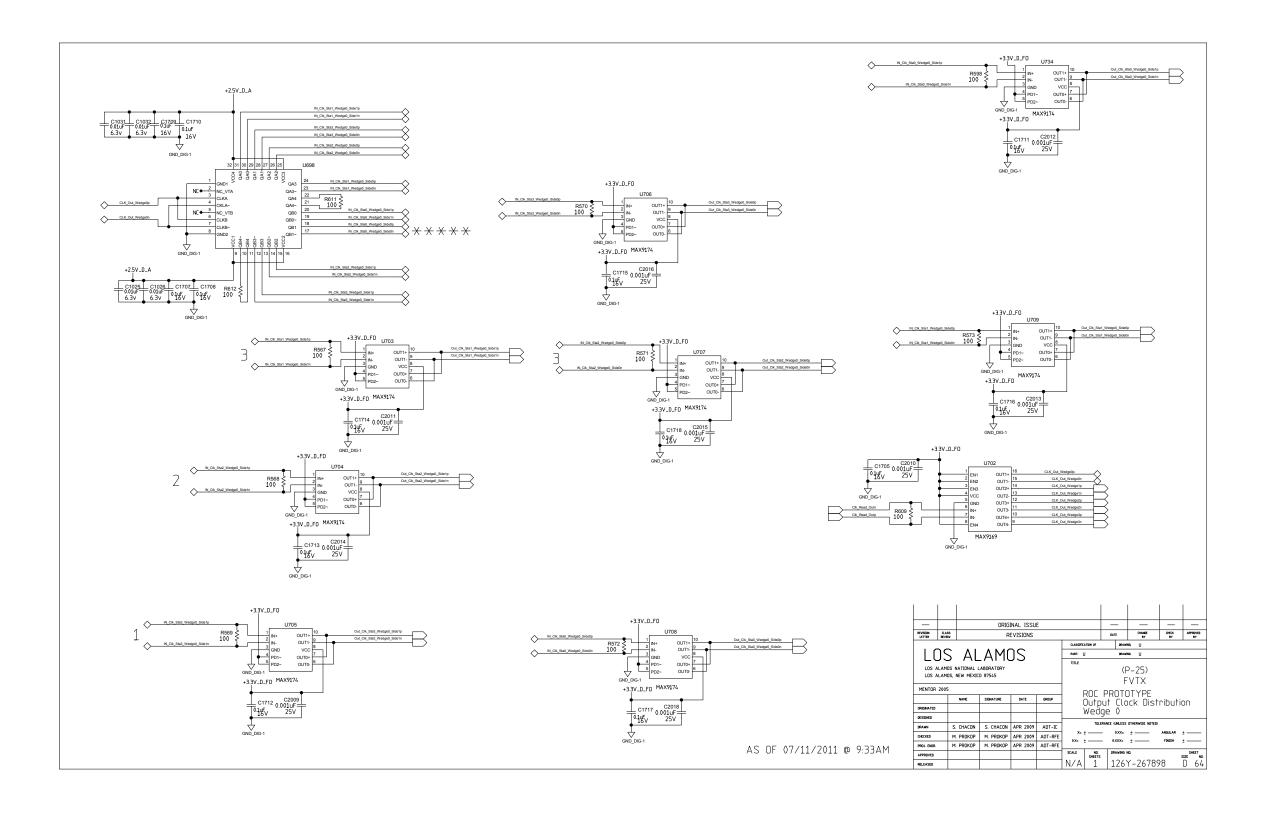


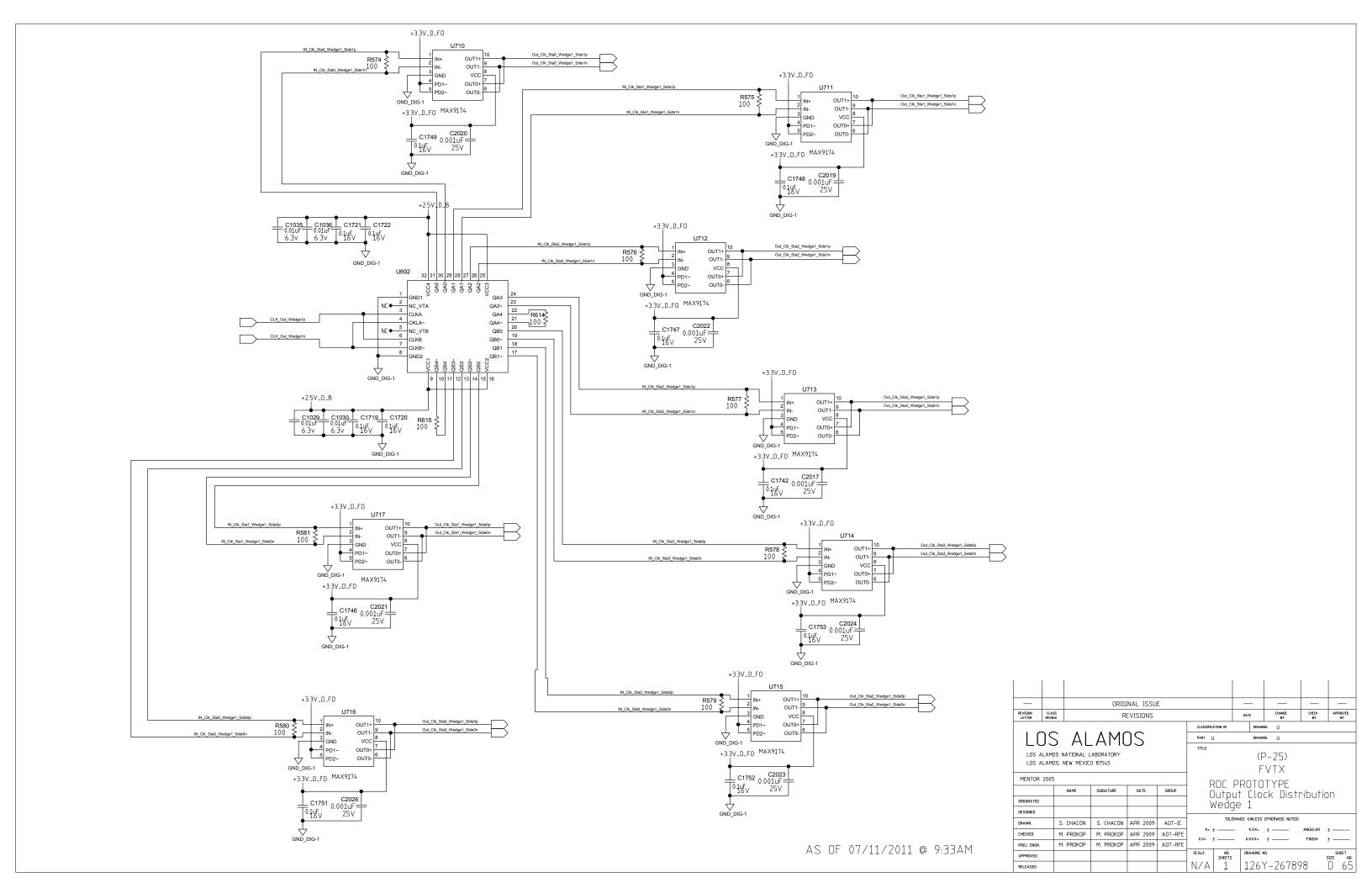


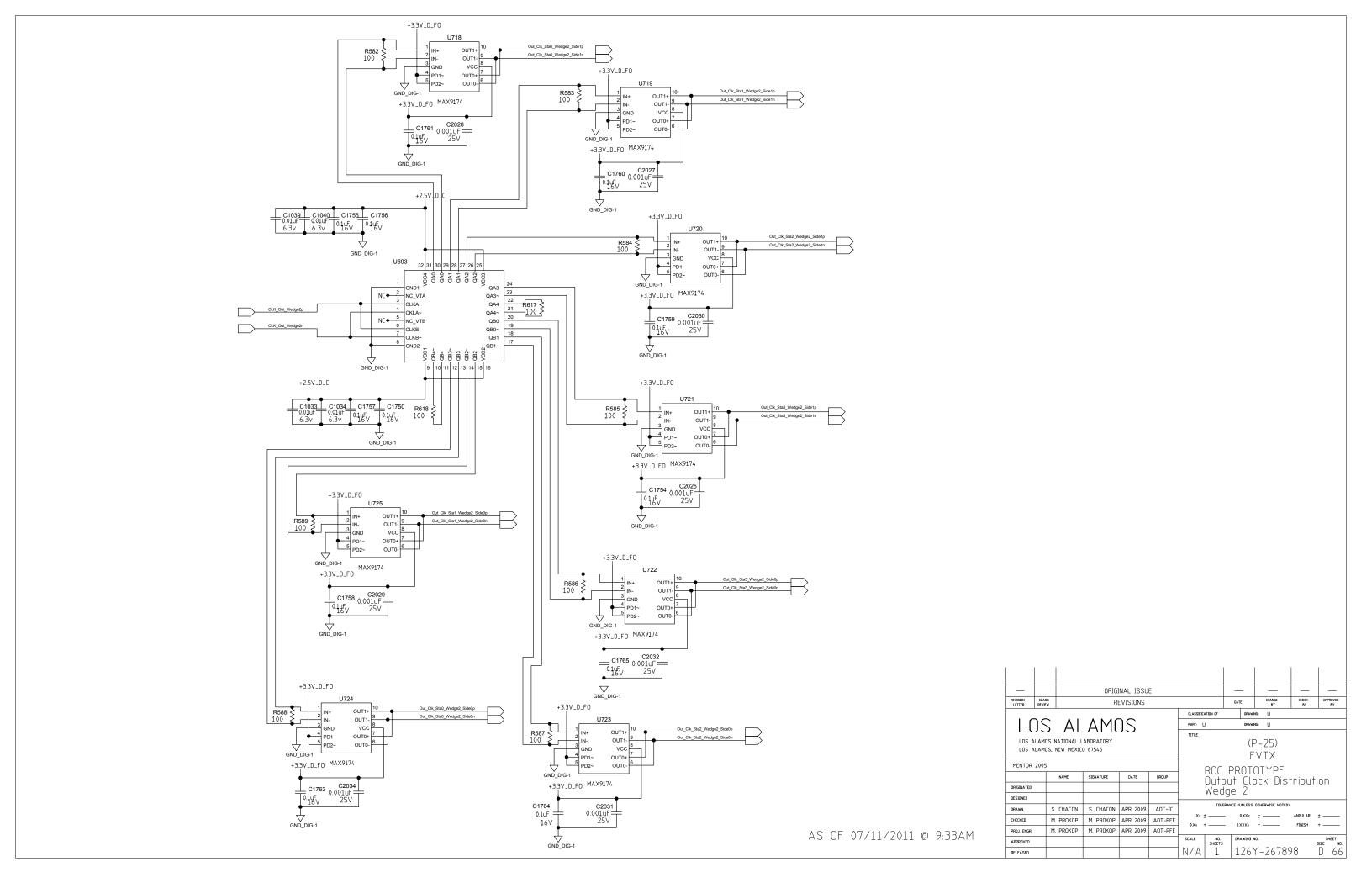


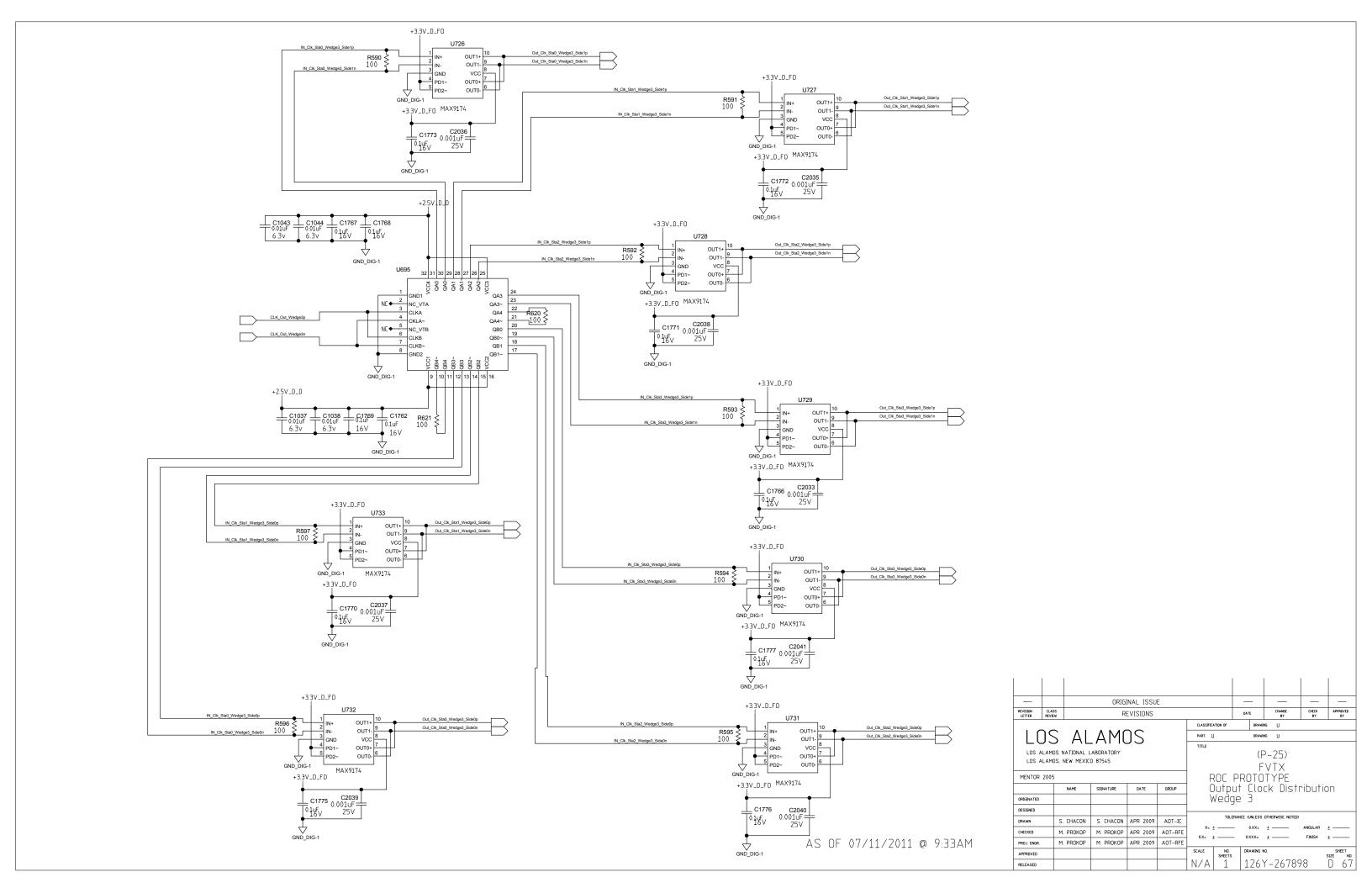


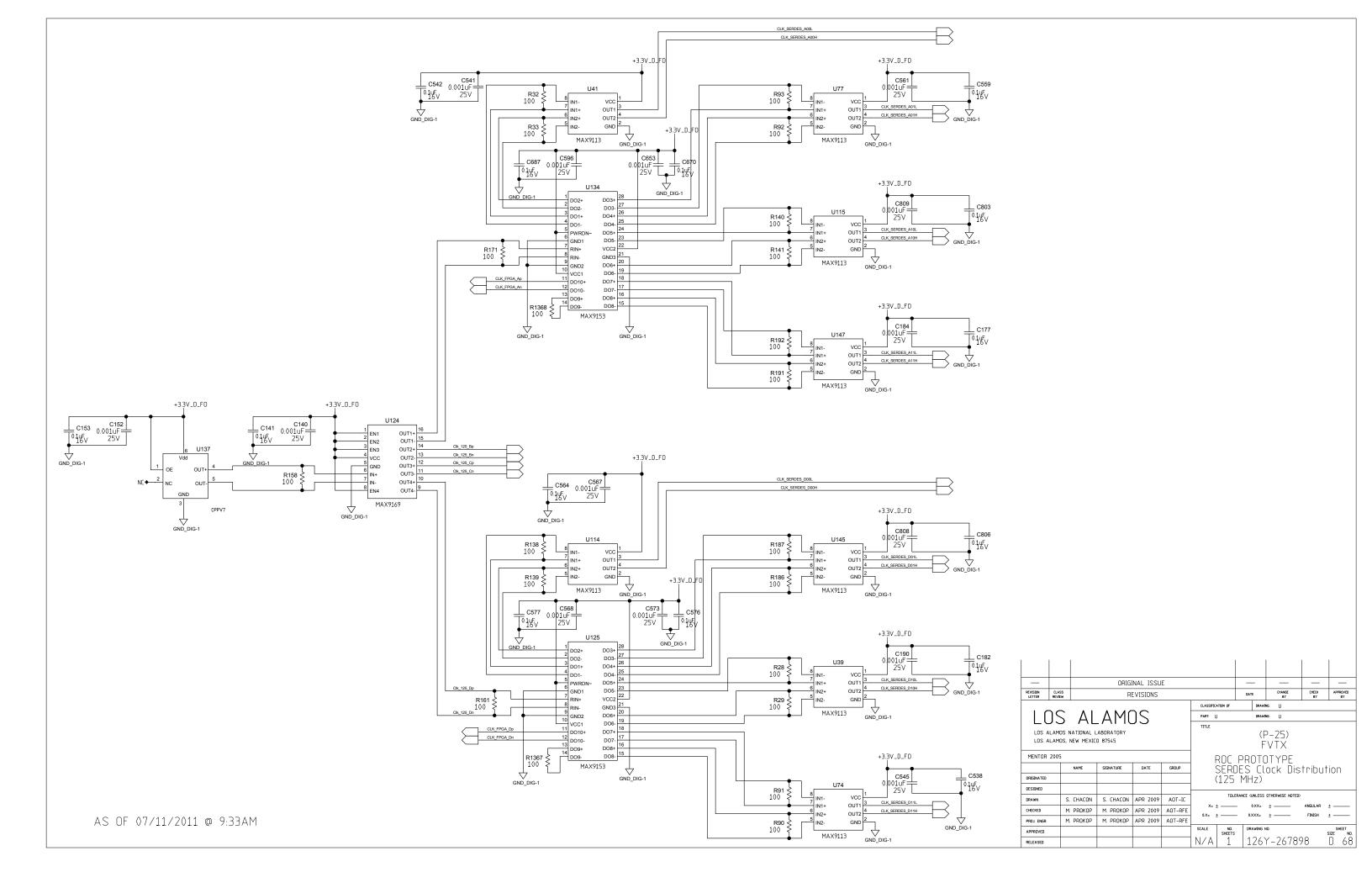


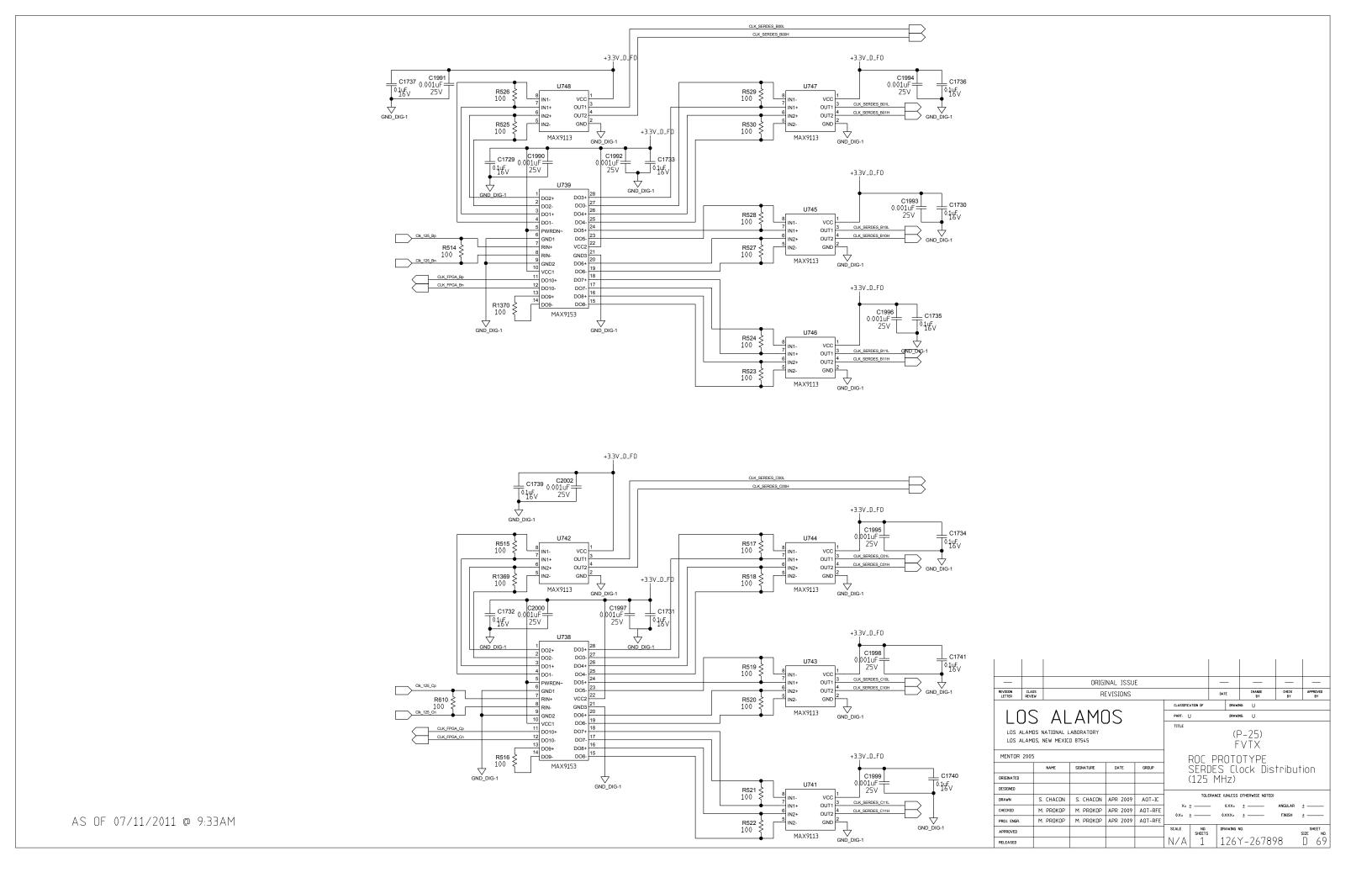


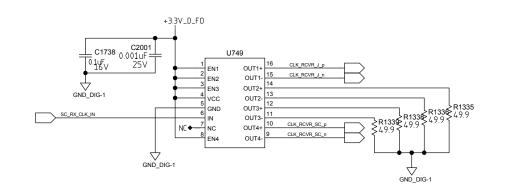


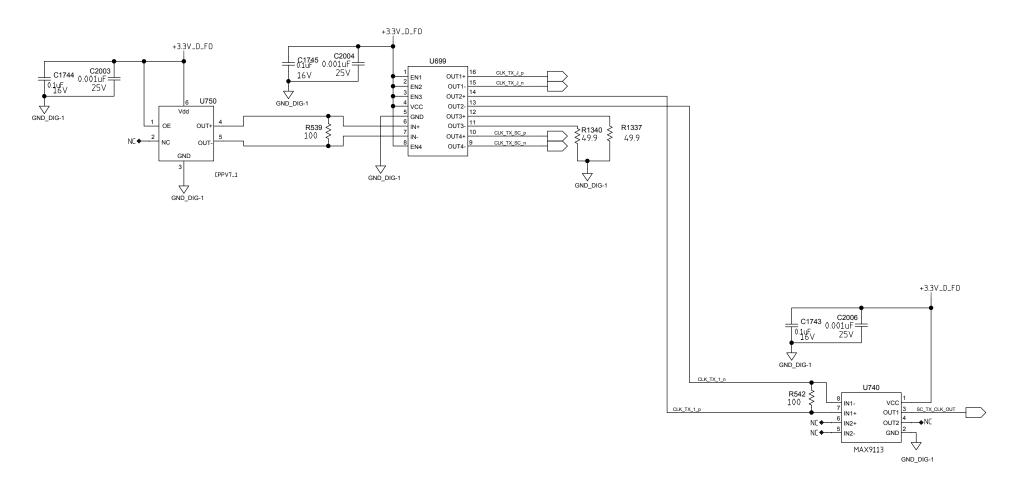


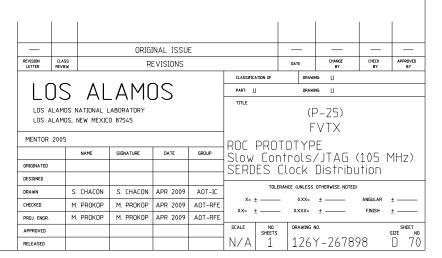












<u> </u>	Data_Sta1_Wedge0_Side0_Chip12_Out0p	R1213	Data_Sta1_Wedge0_Side0_Chip12_Out0n	
$\overline{}$	Data_Sta1_Wedge0_Side0_Chip11_Out1p	R1211 100	Data_Sta1_Wedge0_Side0_Chip11_Out1n	_
	Sata_Sata_mages_saces_snpm_Satap		Bala_ola1_weageo_olaco_olip11_olatiii	$\overline{}$
$\supset$	Data_Sta1_Wedge0_Side0_Chip11_Out0p		Data_Sta1_Wedge0_Side0_Chip11_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge0_Side0_Chip10_Out1p	#9207 	Data_Sta1_Wedge0_Side0_Chip10_Out1n	
_	Data_Sta1_Wedge0_Side0_Chip10_Out0p	R9205	Data_Sta1_Wedge0_Side0_Chip10_Out0n	
_>_	Data_Sta1_Wedges_Stdes_Criip16_Oddsp		Data_Sta1_Wedgeo_Stdeo_Critp10_Outon	$\overline{}$
$\rightarrow$	Data_Sta1_Wedge0_Side0_Chip09_Out1p		Data_Sta1_Wedge0_Side0_Chip09_Out1n	$\overline{}$
<u> </u>	Data_Sta1_Wedge0_Side0_Chip09_Out0p	#9201 	Data_Sta1_Wedge0_Side0_Chip09_Out0n	
_	Data_Sta1_Wedge0_Side0_Chip08_Out1p	£9999	Data_Sta1_Wedge0_Side0_Chip08_Out1n	
_>_	Data_Sta1_Wedge0_Stde0_Chip00_Cdt1p		Data_Sta1_Wedge0_Stde0_Crippo0_Out111	$\overline{}$
$\supset$	Data_Sta1_Wedge0_Side0_Chip08_Out0p		Data_Sta1_Wedge0_Side0_Chip08_Out0n	$\overline{}$
	Data_Sta1_Wedge0_Side0_Chip07_Out1p	#9995 	Data_Sta1_Wedge0_Side0_Chip07_Out1n	
	Date Staf Wedge Cide Chief 7 Outle	₽9193	Date Stat Wadoo Side Chie 7 Outo	
$\rightarrow$	Data_Sta1_Wedge0_Side0_Chip07_Out0p		Data_Sta1_Wedge0_Side0_Chip07_Out0n	$\overline{}$
$\supset$	Data_Sta1_Wedge0_Side0_Chip06_Out1p		Data_Sta1_Wedge0_Side0_Chip06_Out1n	$\overline{}$
	Data_Sta1_Wedge0_Side0_Chip06_Out0p	#9989 	Data_Sta1_Wedge0_Side0_Chip06_Out0n	
		₽9987		
$\supset$	Data_Sta1_Wedge0_Side0_Chip05_Out1p	10085	Data_Sta1_Wedge0_Side0_Chip05_Out1n	$\overline{}$
$\rightarrow$	Data_Sta1_Wedge0_Side0_Chip05_Out0p		Data_Sta1_Wedge0_Side0_Chip05_Out0n	$\overline{}$
_	Data_Sta1_Wedge0_Side0_Chip04_Out1p	£9983	Data_Sta1_Wedge0_Side0_Chip04_Out1n	
$\supset$	Data_Sta1_Wedge0_Side0_Chip04_Out0p		Data_Sta1_Wedge0_Side0_Chip04_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge0_Side0_Chip03_Out1p		Data_Sta1_Wedge0_Side0_Chip03_Out1n	$\overline{}$
_	Data_Sta1_Wedge0_Side0_Chip03_Out0p	£9977	Data_Sta1_Wedge0_Side0_Chip03_Out0n	_
				$\overline{}$
$\supset$ —	Data_Sta1_Wedge0_Side0_Chip02_Out1p		Data_Sta1_Wedge0_Side0_Chip02_Out1n	$\overline{}$
<u> </u>	Data_Sta1_Wedge0_Side0_Chip02_Out0p	#9973 	Data_Sta1_Wedge0_Side0_Chip02_Out0n	
$\overline{}$	Data_Sta1_Wedge0_Side0_Chip01_Out1p	₽9971	Data_Sta1_Wedge0_Side0_Chip01_Out1n	
	Sata_Sata_Treages_Saces_Ships 1_Satap		Bala_oa1_weageo_oaco_onpo1_oatm	$\overline{}$
$\supset$	Data_Sta1_Wedge0_Side0_Chip01_Out0p		Data_Sta1_Wedge0_Side0_Chip01_Out0n	$\overline{}$
	Data_Sta0_Wedge0_Side1_Chip05_Out1p	R9951	Data_Sta0_Wedge0_Side1_Chip05_Out1n	
	Date Class Madage Cided Chines Code	R9967	Date Step Wardens Sided Chieff Order	
_>_	Data_Sta0_Wedge0_Side1_Chip05_Out0p		Data_Sta0_Wedge0_Side1_Chip05_Out0n	$\overline{}$
$\supset$ —	Data_Sta3_Wedge0_Side1_Chip13_Out1p		Data_Sta3_Wedge0_Side1_Chip13_Out1n	$\overline{}$
	Data_Sta3_Wedge0_Side1_Chip13_Out0p	#9963 	Data_Sta3_Wedge0_Side1_Chip13_Out0n	
	Data Sta2 Wadaa0 Sida1 Chin12 Out1n	1000 ₹9161	Data Sta2 Wadaa0 Sida1 Chin12 Out1n	
$\rightarrow$	Data_Sta3_Wedge0_Side1_Chip12_Out1p		Data_Sta3_Wedge0_Side1_Chip12_Out1n	$\overline{}$
$\supset$	Data_Sta3_Wedge0_Side1_Chip12_Out0p		Data_Sta3_Wedge0_Side1_Chip12_Out0n	$\overline{}$
	Data_Sta3_Wedge0_Side1_Chip11_Out1p	#9957 	Data_Sta3_Wedge0_Side1_Chip11_Out1n	
	D	10 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	D	
$\rightarrow$	Data_Sta3_Wedge0_Side1_Chip11_Out0p		Data_Sta3_Wedge0_Side1_Chip11_Out0n	$-\!$
$\supset$	Data_Sta3_Wedge0_Side1_Chip10_Out1p	<del></del>	Data_Sta3_Wedge0_Side1_Chip10_Out1n	$\overline{}$
	Data_Sta3_Wedge0_Side1_Chip10_Out0p	#9325	Data_Sta3_Wedge0_Side1_Chip10_Out0n	
$\supset$	Data_Sta3_Wedge0_Side1_Chip09_Out1p	1000 R9321	Data_Sta3_Wedge0_Side1_Chip09_Out1n	$\overline{}$
<u> </u>	Data_Sta0_Wedge0_Side1_Chip04_Out1p		Data_Sta0_Wedge0_Side1_Chip04_Out1n	
_	Data_Sta0_Wedge0_Side1_Chip04_Out0p	£9819	Data_Sta0_Wedge0_Side1_Chip04_Out0n	_
$\supset$	Data_Sta0_Wedge0_Side1_Chip03_Out1p	<b></b>	Data_Sta0_Wedge0_Side1_Chip03_Out1n	$\overline{}$
<u> </u>	Data_Sta0_Wedge0_Side1_Chip03_Out0p	#9915 	Data_Sta0_Wedge0_Side1_Chip03_Out0n	
	Data_Sta0_Wedge0_Side1_Chip02_Out1p	£9313	Data_Sta0_Wedge0_Side1_Chip02_Out1n	_
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$\supset$	Data_Sta0_Wedge0_Side1_Chip02_Out0p	<del></del>	Data_Sta0_Wedge0_Side1_Chip02_Out0n	$\overline{}$
	Data_Sta0_Wedge0_Side1_Chip01_Out1p	#9309 	Data_Sta0_Wedge0_Side1_Chip01_Out1n	
		#9907		
$\supset$	Data_Sta0_Wedge0_Side1_Chip01_Out0p		Data_Sta0_Wedge0_Side1_Chip01_Out0n	$\overline{}$
$\supset$	Data_Sta0_Wedge0_Side0_Chip05_Out1p		Data_Sta0_Wedge0_Side0_Chip05_Out1n	
	Data_Sta0_Wedge0_Side0_Chip05_Out0p	£9903	Data_Sta0_Wedge0_Side0_Chip05_Out0n	
$\supset$	Data_Sta0_Wedge0_Side0_Chip04_Out1p	<b></b>	Data_Sta0_Wedge0_Side0_Chip04_Out1n	$\overline{}$
<u> </u>	Data_Sta0_Wedge0_Side0_Chip04_Out0p	#9299 	Data_Sta0_Wedge0_Side0_Chip04_Out0n	
		100		<u> </u>

$\longrightarrow$	Data_Sta0_Wedge0_Side0_Chip03_Out1p	R1257	Data_Sta0_Wedge0_Side0_Chip03_Out1n
	Data_Sta0_Wedge0_Side0_Chip03_Out0p	19255	Data_Sta0_Wedge0_Side0_Chip03_Out0n
	Data_otao_rreageo_otaco_ompoo_otatop	#9253	Suit State Wedges Green Stripped Galeti
	Data_Sta0_Wedge0_Side0_Chip02_Out1p	<del></del> ^	Data_Sta0_Wedge0_Side0_Chip02_Out1n
	Data_Sta0_Wedge0_Side0_Chip02_Out0p	R9251	Data_Sta0_Wedge0_Side0_Chip02_Out0n
	D. I. O. O. W. I. O. G. O. O. O. O. I.	100 1249	
	Data_Sta0_Wedge0_Side0_Chip01_Out1p		Data_Sta0_Wedge0_Side0_Chip01_Out1n
	Data_Sta0_Wedge0_Side0_Chip01_Out0p		Data_Sta0_Wedge0_Side0_Chip01_Out0n
	Data_Sta3_Wedge0_Side0_Chip12_Out0p	#9245	Data_Sta3_Wedge0_Side0_Chip12_Out0n
	Data_Sta3_Wedge0_Side0_Chip11_Out1p	₽9243	Data_Sta3_Wedge0_Side0_Chip11_Out1n
	Data_Sta5_Wedge0_Stde0_Critp11_Out1p		Data_Stas_wedged_Stded_Chip11_Out111
	Data_Sta3_Wedge0_Side0_Chip11_Out0p		Data_Sta3_Wedge0_Side0_Chip11_Out0n
$\Box$	Data_Sta3_Wedge0_Side0_Chip10_Out1p	#9239 	Data_Sta3_Wedge0_Side0_Chip10_Out1n
	Data_Sta3_Wedge0_Side0_Chip10_Out0p	₽9237	Data_Sta3_Wedge0_Side0_Chip10_Out0n
	Data_Sta5_wedge0_Stde0_Critp10_Odtop		Data_Stas_wedged_Stded_Chip10_Outuri
	Data_Sta3_Wedge0_Side0_Chip09_Out1p		Data_Sta3_Wedge0_Side0_Chip09_Out1n
$\overline{}$	Data_Sta3_Wedge0_Side0_Chip09_Out0p	#9233 	Data_Sta3_Wedge0_Side0_Chip09_Out0n
	Data_Sta3_Wedge0_Side0_Chip08_Out1p	1000 1€9231	Data_Sta3_Wedge0_Side0_Chip08_Out1n
	Data_otab_vvedgeo_otato_otispoo_otatip		Stat_Stat_Treages_cross_Cripto_Cat.iii
$\longrightarrow$	Data_Sta3_Wedge0_Side0_Chip08_Out0p		Data_Sta3_Wedge0_Side0_Chip08_Out0n
$\longrightarrow$	Data_Sta3_Wedge0_Side0_Chip07_Out1p	#9227 	Data_Sta3_Wedge0_Side0_Chip07_Out1n
	Data_Sta3_Wedge0_Side0_Chip07_Out0p	100 1225	Data_Sta3_Wedge0_Side0_Chip07_Out0n
	Data_Sta3_Wedge0_Side0_Chip06_Out1p	<b></b> ^	Data_Sta3_Wedge0_Side0_Chip06_Out1n
$\longrightarrow$	Data_Sta3_Wedge0_Side0_Chip06_Out0p	#9221 	Data_Sta3_Wedge0_Side0_Chip06_Out0n
	Data_Sta3_Wedge0_Side0_Chip05_Out1p	19219	Data_Sta3_Wedge0_Side0_Chip05_Out1n
$\Box$	Data_Sta3_Wedge0_Side0_Chip05_Out0p	<del></del>	Data_Sta3_Wedge0_Side0_Chip05_Out0n
$\longrightarrow$	Data_Sta3_Wedge0_Side0_Chip04_Out1p	#1215 	Data_Sta3_Wedge0_Side0_Chip04_Out1n
	Data_Sta3_Wedge0_Side0_Chip04_Out0p	#9216 	Data_Sta3_Wedge0_Side0_Chip04_Out0n
		#9228	
$\Box$	Data_Sta3_Wedge0_Side0_Chip03_Out1p	100 100 189218	Data_Sta3_Wedge0_Side0_Chip03_Out1n
	Data_Sta3_Wedge0_Side0_Chip03_Out0p		Data_Sta3_Wedge0_Side0_Chip03_Out0n
	Data_Sta3_Wedge0_Side0_Chip02_Out1p	R9220	Data_Sta3_Wedge0_Side0_Chip02_Out1n
		100 R9222	
$\Box$	Data_Sta3_Wedge0_Side0_Chip02_Out0p		Data_Sta3_Wedge0_Side0_Chip02_Out0n
$\Box$	Data_Sta3_Wedge0_Side0_Chip01_Out1p		Data_Sta3_Wedge0_Side0_Chip01_Out1n
	Data_Sta3_Wedge0_Side0_Chip01_Out0p	#9226 	Data_Sta3_Wedge0_Side0_Chip01_Out0n
	CLK_FPGA_Ap	₽9327	CLK_FPGA_An
	CLN_FFGM_AP	100	CLI_FFOA_AII

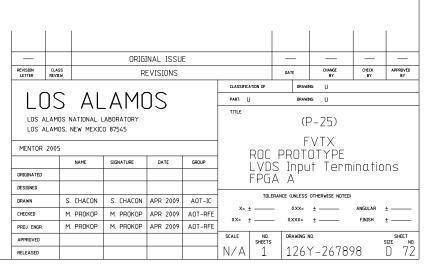
		R1297	
$\supset$ —	Data_Sta3_Wedge0_Side1_Chip09_Out0p		Data_Sta3_Wedge0_Side1_Chip09_Out0n
_	Data Sta3 Wedge0 Side1 Chip08 Out1p	R9295	Data Sta3 Wedge0 Side1 Chip08 Out1n
<b>&gt;</b> —	Data_Sta3_Wedge0_Side1_Chip08_Out0p		Data_Sta3_Wedge0_Side1_Chip08_Out0n
_	Data Sta3 Wedge0 Side1 Chip07 Out1p	R9291	Data Sta3 Wedge0 Side1 Chip07 Out1n
$\rightarrow$	Data_StaS_eveogeo_Stde1_Cnipt/1_Cdt1p		bala_stas_vveugeo_stde1_chipo1_cdutiii
<b>&gt;</b>	Data_Sta3_Wedge0_Side1_Chip07_Out0p		Data_Sta3_Wedge0_Side1_Chip07_Out0n
_		100 R9287	
$\rightarrow$	Data_Sta3_Wedge0_Side1_Chip06_Out1p		Data_Sta3_Wedge0_Side1_Chip06_Out1n
<u> </u>	Data_Sta3_Wedge0_Side1_Chip06_Out0p	#1285 	Data_Sta3_Wedge0_Side1_Chip06_Out0n
_		£9283	
$\rightarrow$	Data_Sta3_Wedge0_Side1_Chip05_Out1p	<u> </u>	Data_Sta3_Wedge0_Side1_Chip05_Out1n
_	Data_Sta3_Wedge0_Side1_Chip05_Out0p	#9281 	Data_Sta3_Wedge0_Side1_Chip05_Out0n
		19979	
$\rightarrow$	Data_Sta3_Wedge0_Side1_Chip04_Out1p		Data_Sta3_Wedge0_Side1_Chip04_Out1n
_	Data Sta3 Wedge0 Side1 Chip04 Out0p	R9277	Data Sta3 Wedge0 Side1 Chip04 Out0n
>	Data_Sta3_Wedge0_Side1_Chip03_Out1p		Data_Sta3_Wedge0_Side1_Chip03_Out1n
_	Data Sta3 Wedge0 Side1 Chip03 Out0p	R9273	Data Sta3 Wedge0 Side1 Chip03 Out0n
$\rightarrow$	Data_StaS_wedgeo_Stde1_Cripto3_Gatop		bala_stas_wedgeo_stde1_chipos_cdto11
>	Data_Sta3_Wedge0_Side1_Chip02_Out1p		Data_Sta3_Wedge0_Side1_Chip02_Out1n
_	Date Charles Western Court Charles	R9269	
$\rightarrow$	Data_Sta3_Wedge0_Side1_Chip02_Out0p		Data_Sta3_Wedge0_Side1_Chip02_Out0n
<u> </u>	Data_Sta3_Wedge0_Side1_Chip01_Out1p	R1267 	Data_Sta3_Wedge0_Side1_Chip01_Out1n
_		192 <sub>65</sub>	
$\rightarrow$	Data_Sta3_Wedge0_Side1_Chip01_Out0p	<b>\</b> \\\-	Data_Sta3_Wedge0_Side1_Chip01_Out0n
<u> </u>	Data_Sta3_Wedge0_Side0_Chip13_Out1p	#1263 	Data_Sta3_Wedge0_Side0_Chip13_Out1n
_		100 100 100 100 100 100 100 100 100 100	
$\rightarrow$	Data_Sta3_Wedge0_Side0_Chip13_Out0p		Data_Sta3_Wedge0_Side0_Chip13_Out0n
_	Data_Sta3_Wedge0_Side0_Chip12_Out1p	R9259	Data_Sta3_Wedge0_Side0_Chip12_Out1n

_			ORIGINAL ISSUE					_	_	_	$\vdash$
EVISION LETTER	CLASS	REVISIONS						DATE	CHANGE BY	CHECK BY	APPROVED BY
							ATION OF	DRAWII	NG: U		
I f	LOS ALAMOS						ı	DRAWI	NG: U		
LOS AI	LOS ALAMOS NATIONAL LABORATORY LOS ALAMOS, NEW MEXICO 87545					TITLE			P-25)		
MENTOR :	2005					FVTX ROC PROTOTYPE					
		NAME	SIGNATURE	DATE	GROUP		KUL 1 LVDS	Indr	ut Term	inatio	ns
RIGINATED							FPGA	Α			-
ESIGNED											
RAWN	5	. CHACON	S. CHACON	APR 2009	AOT-IC				OTHERWISE NOTED)		
HECKED	١	1. PROKOP	M. PROKOP	APR 2009	AOT-RFE	X= ± 0.XX= ± ANGULAR ±				±	
PROJ. ENGR.	١	1. PROKOP	M. PROKOP	APR 2009	AOT-RFE	0.X= ±		0.XXX=	±	FINISH	±
APPROVED						SCALE	NO. SHEETS	DRAWING N	10.		SHEET SIZE NO.
RELEASED						N/A	1	126`	Y-26789	8	D 71

	Data_Sta1_Wedge0_Side1_Chip13_Out1p	R1230	Data_Sta1_Wedge0_Side1_Chip13_Out1n	
		1000 1000 1000 1000 1000 1000 1000 100		
	Data_Sta1_Wedge0_Side1_Chip13_Out0p	100 100 101	Data_Sta1_Wedge0_Side1_Chip13_Out0n	$\prec$
$\Box$	Data_Sta1_Wedge0_Side1_Chip12_Out1p		Data_Sta1_Wedge0_Side1_Chip12_Out1n	
$\longrightarrow$	Data_Sta1_Wedge0_Side1_Chip12_Out0p	#9236 	Data_Sta1_Wedge0_Side1_Chip12_Out0n	$\overline{}$
	Data_Sta1_Wedge0_Side1_Chip11_Out1p	100 R1238	Data_Sta1_Wedge0_Side1_Chip11_Out1n	_
	Data_Sta1_Wedge0_Side1_Chip11_Out0p	₽9240	Data_Sta1_Wedge0_Side1_Chip11_Out0n	
				$\leftarrow$
$\Box$	Data_Sta1_Wedge0_Side1_Chip10_Out1p		Data_Sta1_Wedge0_Side1_Chip10_Out1n	$\overline{}$
	Data_Sta1_Wedge0_Side1_Chip10_Out0p		Data_Sta1_Wedge0_Side1_Chip10_Out0n	
$\longrightarrow$	Data_Sta1_Wedge0_Side1_Chip09_Out1p	#9246 	Data_Sta1_Wedge0_Side1_Chip09_Out1n	$\overline{}$
	Data_Sta1_Wedge0_Side1_Chip09_Out0p	#9248	Data_Sta1_Wedge0_Side1_Chip09_Out0n	_
	Data_Sta1_Wedge0_Side1_Chip08_Out1p	100 R1250	Data_Sta1_Wedge0_Side1_Chip08_Out1n	
				$\leftarrow$
$\overline{}$	Data_Sta1_Wedge0_Side1_Chip08_Out0p	100 100 100 100	Data_Sta1_Wedge0_Side1_Chip08_Out0n	$\leftarrow$
$\longrightarrow$	Data_Sta1_Wedge0_Side1_Chip07_Out1p		Data_Sta1_Wedge0_Side1_Chip07_Out1n	
$\longrightarrow$	Data_Sta1_Wedge0_Side1_Chip07_Out0p	#9256 	Data_Sta1_Wedge0_Side1_Chip07_Out0n	$\leftarrow$
$\overline{}$	Data_Sta1_Wedge0_Side1_Chip06_Out1p	#9258 	Data_Sta1_Wedge0_Side1_Chip06_Out1n	_
	Data_Sta1_Wedge0_Side1_Chip06_Out0p	₽9260	Data_Sta1_Wedge0_Side1_Chip06_Out0n	
		1000 R9262		
	Data_Sta1_Wedge0_Side1_Chip05_Out1p	10064	Data_Sta1_Wedge0_Side1_Chip05_Out1n	$\leftarrow$
$\longrightarrow$	Data_Sta1_Wedge0_Side1_Chip05_Out0p	100 100 100 100 100 100 100 100	Data_Sta1_Wedge0_Side1_Chip05_Out0n	
$\Box$	Data_Sta1_Wedge0_Side1_Chip04_Out1p		Data_Sta1_Wedge0_Side1_Chip04_Out1n	
$\longrightarrow$	Data_Sta1_Wedge0_Side1_Chip04_Out0p	#9268 	Data_Sta1_Wedge0_Side1_Chip04_Out0n	$\overline{}$
	Data_Sta1_Wedge0_Side1_Chip03_Out1p	£9270	Data_Sta1_Wedge0_Side1_Chip03_Out1n	
	Data_Sta1_Wedge0_Side1_Chip03_Out0p	₽9272	Data_Sta1_Wedge0_Side1_Chip03_Out0n	
				$\leftarrow$
$\Box$	Data_Sta1_Wedge0_Side1_Chip02_Out1p	100	Data_Sta1_Wedge0_Side1_Chip02_Out1n	$\leftarrow$
$\Box$	Data_Sta1_Wedge0_Side1_Chip02_Out0p		Data_Sta1_Wedge0_Side1_Chip02_Out0n	
	Data_Sta1_Wedge0_Side1_Chip01_Out1p	100 1276 	Data_Sta1_Wedge0_Side1_Chip01_Out1n	$\overline{}$
$\longrightarrow$		100		
	Data_Sta1_Wedge0_Side1_Chip01_Out0p	#9278 	Data_Sta1_Wedge0_Side1_Chip01_Out0n	
		#Y278 		
	Data_Sta1_Wedge0_Side0_Chip13_Out1p		Data_Sta1_Wedge0_Side0_Chip13_Out1n	
		#9280		

$\longrightarrow$	Data_Sta2_Wedge0_Side1_Chip01_Out1p	R1288	Data_Sta2_Wedge0_Side1_Chip01_Out1n	$\overline{}$
	Data Sta2 Wedge0 Side1 Chip01 Out0p	#9290	Data Sta2 Wedge0 Side1 Chip01 Out0n	
		1000 1000 1000 1000 1000		$\overline{}$
$\overline{}$	Data_Sta2_Wedge0_Side0_Chip13_Out1p	100 100 100 100	Data_Sta2_Wedge0_Side0_Chip13_Out1n	$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip13_Out0p		Data_Sta2_Wedge0_Side0_Chip13_Out0n	
	Data_Sta2_Wedge0_Side0_Chip12_Out1p	#9296 	Data_Sta2_Wedge0_Side0_Chip12_Out1n	
	Data_Sta2_Wedge0_Side0_Chip12_Out0p	100 R9298	Data_Sta2_Wedge0_Side0_Chip12_Out0n	
				$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip11_Out1p	1000 1000 1000	Data_Sta2_Wedge0_Side0_Chip11_Out1n	$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip11_Out0p		Data_Sta2_Wedge0_Side0_Chip11_Out0n	
ightharpoons	Data_Sta2_Wedge0_Side0_Chip10_Out1p	#9304 	Data_Sta2_Wedge0_Side0_Chip10_Out1n	$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip10_Out0p	1000 1000	Data_Sta2_Wedge0_Side0_Chip10_Out0n	
				$\overline{}$
$\overline{}$	Data_Sta2_Wedge0_Side0_Chip09_Out1p	100 R1310	Data_Sta2_Wedge0_Side0_Chip09_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta2_Wedge0_Side0_Chip09_Out0p		Data_Sta2_Wedge0_Side0_Chip09_Out0n	
$\Box$	Data_Sta2_Wedge0_Side0_Chip08_Out1p	#19912 	Data_Sta2_Wedge0_Side0_Chip08_Out1n	
	Data_Sta2_Wedge0_Side0_Chip08_Out0p	<del>1</del> 9914	Data_Sta2_Wedge0_Side0_Chip08_Out0n	
				$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip07_Out1p	100 100 181318	Data_Sta2_Wedge0_Side0_Chip07_Out1n	$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip07_Out0p		Data_Sta2_Wedge0_Side0_Chip07_Out0n	$\overline{}$
$\Box$	Data_Sta2_Wedge0_Side0_Chip06_Out1p	100 189320	Data_Sta2_Wedge0_Side0_Chip06_Out1n	
	Data_Sta2_Wedge0_Side0_Chip06_Out0p	100 R1322	Data_Sta2_Wedge0_Side0_Chip06_Out0n	
				$\overline{}$
$\overline{}$	Data_Sta2_Wedge0_Side0_Chip05_Out1p	199 <sub>26</sub>	Data_Sta2_Wedge0_Side0_Chip05_Out1n	$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip05_Out0p	<b>─</b> ─ <b></b>	Data_Sta2_Wedge0_Side0_Chip05_Out0n	
	Data_Sta2_Wedge0_Side0_Chip04_Out1p	100 152	Data_Sta2_Wedge0_Side0_Chip04_Out1n	
	Data Sta2 Wedge0 Side0 Chip04 Out0p	£9954	Data Sta2 Wedge0 Side0 Chip04 Out0n	
		10056		$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip03_Out1p	10074	Data_Sta2_Wedge0_Side0_Chip03_Out1n	$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip03_Out0p		Data_Sta2_Wedge0_Side0_Chip03_Out0n	
$\overline{}$	Data_Sta2_Wedge0_Side0_Chip02_Out1p	#9958 	Data_Sta2_Wedge0_Side0_Chip02_Out1n	
	Data_Sta2_Wedge0_Side0_Chip02_Out0p	#9160	Data Sta2 Wedge0 Side0 Chip02 Out0n	
				$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip01_Out1p	100 100 100 100	Data_Sta2_Wedge0_Side0_Chip01_Out1n	$\overline{}$
	Data_Sta2_Wedge0_Side0_Chip01_Out0p		Data_Sta2_Wedge0_Side0_Chip01_Out0n	
ightharpoonup	Data_Sta2_Wedge0_Side1_Chip13_Out1p	#9966 	Data_Sta2_Wedge0_Side1_Chip13_Out1n	
	Data Sta2 Wedge0 Side1 Chip13 Out0p	£9968	Data Sta2 Wedge0 Side1 Chip13 Out0n	
				$\overline{}$
$\Box$	Data_Sta2_Wedge0_Side1_Chip12_Out1p	10072	Data_Sta2_Wedge0_Side1_Chip12_Out1n	
	Data_Sta2_Wedge0_Side1_Chip12_Out0p		Data_Sta2_Wedge0_Side1_Chip12_Out0n	
		100		_

		R1176	
	Data_Sta2_Wedge0_Side1_Chip11_Out1p	10078	Data_Sta2_Wedge0_Side1_Chip11_Out1n
$\longrightarrow$	Data_Sta2_Wedge0_Side1_Chip11_Out0p		Data_Sta2_Wedge0_Side1_Chip11_Out0n
	Data Sta2 Wedge0 Side1 Chip10 Out1p	£9980	Data Sta2 Wedge0 Side1 Chip10 Out1n
	Bud_out_Treages_out 1_onip1o_out1p		bud_out_reages_coer_onpre_out.ii
$\Box$	Data_Sta2_Wedge0_Side1_Chip10_Out0p		Data_Sta2_Wedge0_Side1_Chip10_Out0n
	Data_Sta2_Wedge0_Side1_Chip09_Out1p	19984 	Data_Sta2_Wedge0_Side1_Chip09_Out1n
	Data Sta2 Wedge0 Side1 Chip09 Out0p	19986	Data Sta2 Wedge0 Side1 Chip09 Out0n
	Data_staz_weogeo_stoe1_chipoa_outop		Data_Sta2_wedge0_Stde1_Criip09_Outori
$\longrightarrow$	Data_Sta2_Wedge0_Side1_Chip08_Out1p		Data_Sta2_Wedge0_Side1_Chip08_Out1n
	Data_Sta2_Wedge0_Side1_Chip08_Out0p	£9990	Data_Sta2_Wedge0_Side1_Chip08_Out0n
		10 992	
$\Box$	Data_Sta2_Wedge0_Side1_Chip07_Out1p	10094	Data_Sta2_Wedge0_Side1_Chip07_Out1n
$\longrightarrow$	Data_Sta2_Wedge0_Side1_Chip07_Out0p	#Y1194 	Data_Sta2_Wedge0_Side1_Chip07_Out0n
	Data Sta2 Wedge0 Side1 Chip06 Out1p	R9996	Data Sta2 Wedge0 Side1 Chip06 Out1n
$\Box$	Data_Sta2_Wedge0_Side1_Chip06_Out0p		Data_Sta2_Wedge0_Side1_Chip06_Out0n
$\longrightarrow$	Data_Sta2_Wedge0_Side1_Chip05_Out1p	#1200 	Data_Sta2_Wedge0_Side1_Chip05_Out1n
	Data Sta2 Wedge0 Side1 Chip05 Out0p	A9202	Data Sta2 Wedge0 Side1 Chip05 Out0n
		19904	
$\Box$	Data_Sta2_Wedge0_Side1_Chip04_Out1p		Data_Sta2_Wedge0_Side1_Chip04_Out1n
$\Box$	Data_Sta2_Wedge0_Side1_Chip04_Out0p	R9206	Data_Sta2_Wedge0_Side1_Chip04_Out0n
	Data_Sta2_Wedge0_Side1_Chip03_Out1p	R9208	Data_Sta2_Wedge0_Side1_Chip03_Out1n
	Saas_stass_reages_stass_critipos_cuttp		Sub_cut_rreages_stoci_criipos_cut.ii
$\Box$	Data_Sta2_Wedge0_Side1_Chip03_Out0p	<del></del>	Data_Sta2_Wedge0_Side1_Chip03_Out0n
	Data_Sta2_Wedge0_Side1_Chip02_Out1p	#9212 	Data_Sta2_Wedge0_Side1_Chip02_Out1n
	D	100 14 14	
$\Box$	Data_Sta2_Wedge0_Side1_Chip02_Out0p	100	Data_Sta2_Wedge0_Side1_Chip02_Out0n



<u> </u>	Data_Sta1_Wedge1_Side0_Chip12_Out0p	R1023	Data_Sta1_Wedge1_Side0_Chip12_Out0n	
$\overline{}$	Data_Sta1_Wedge1_Side0_Chip11_Out1p	₽9821 	Data_Sta1_Wedge1_Side0_Chip11_Out1n	_
		#9019		
$\supset$ —	Data_Sta1_Wedge1_Side0_Chip11_Out0p		Data_Sta1_Wedge1_Side0_Chip11_Out0n	$\overline{}$
$\supset$	Data_Sta1_Wedge1_Side0_Chip10_Out1p		Data_Sta1_Wedge1_Side0_Chip10_Out1n	$\overline{}$
	Data_Sta1_Wedge1_Side0_Chip10_Out0p	#9815 	Data_Sta1_Wedge1_Side0_Chip10_Out0n	
	Data Stat Wadaat Sidao Chinon Outto	₽9013	Data Stat Wadaat Sidaa Chinaa Outta	
_>_	Data_Sta1_Wedge1_Side0_Chip09_Out1p		Data_Sta1_Wedge1_Side0_Chip09_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge1_Side0_Chip09_Out0p	<u> </u>	Data_Sta1_Wedge1_Side0_Chip09_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge1_Side0_Chip08_Out1p	#9809 	Data_Sta1_Wedge1_Side0_Chip08_Out1n	
_	Data_Sta1_Wedge1_Side0_Chip08_Out0p	£9807	Data_Sta1_Wedge1_Side0_Chip08_Out0n	
		£9805		
$\supset$	Data_Sta1_Wedge1_Side0_Chip07_Out1p	19803	Data_Sta1_Wedge1_Side0_Chip07_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge1_Side0_Chip07_Out0p		Data_Sta1_Wedge1_Side0_Chip07_Out0n	$\overline{}$
	Data_Sta1_Wedge1_Side0_Chip06_Out1p	#9801 	Data_Sta1_Wedge1_Side0_Chip06_Out1n	
	Date Staf Wedged Side Ochiel Ochiel	1 <del>0</del> 099	Date Chat Wadant Cided Chinds Outle	
$\rightarrow$	Data_Sta1_Wedge1_Side0_Chip06_Out0p		Data_Sta1_Wedge1_Side0_Chip06_Out0n	$\overline{}$
$\supset$ —	Data_Sta1_Wedge1_Side0_Chip05_Out1p		Data_Sta1_Wedge1_Side0_Chip05_Out1n	$\overline{}$
<u> </u>	Data_Sta1_Wedge1_Side0_Chip05_Out0p	<del>1</del> <del>2</del> 995	Data_Sta1_Wedge1_Side0_Chip05_Out0n	
_	Data_Sta1_Wedge1_Side0_Chip04_Out1p	<del>1</del> 8993	Data_Sta1_Wedge1_Side0_Chip04_Out1n	_
$\supset$	Data_Sta1_Wedge1_Side0_Chip04_Out0p	100 -	Data_Sta1_Wedge1_Side0_Chip04_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge1_Side0_Chip03_Out1p	<del>1</del> 8989 	Data_Sta1_Wedge1_Side0_Chip03_Out1n	
	Data_Sta1_Wedge1_Side0_Chip03_Out0p	1 <del>2</del> 987	Data_Sta1_Wedge1_Side0_Chip03_Out0n	
	D	1 <del>000</del> 5	D	
$\rightarrow$	Data_Sta1_Wedge1_Side0_Chip02_Out1p		Data_Sta1_Wedge1_Side0_Chip02_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge1_Side0_Chip02_Out0p		Data_Sta1_Wedge1_Side0_Chip02_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge1_Side0_Chip01_Out1p	100 17981 	Data_Sta1_Wedge1_Side0_Chip01_Out1n	
_	Data_Sta1_Wedge1_Side0_Chip01_Out0p	<del>1</del> R979	Data_Sta1_Wedge1_Side0_Chip01_Out0n	
	Data_Sta1_Wedge1_Stde0_Cripto1_Cdd0p		Data_sta1_vveuge1_state0_stripto1_state1	$\overline{}$
$\supset$	Data_Sta0_Wedge1_Side1_Chip05_Out1p		Data_Sta0_Wedge1_Side1_Chip05_Out1n	$\overline{}$
<u> </u>	Data_Sta0_Wedge1_Side1_Chip05_Out0p	<del>1897</del> 7 	Data_Sta0_Wedge1_Side1_Chip05_Out0n	
_	Data_Sta3_Wedge1_Side1_Chip13_Out1p	1 <del>R97</del> 5	Data_Sta3_Wedge1_Side1_Chip13_Out1n	_
$\supset$ —	Data_Sta3_Wedge1_Side1_Chip13_Out0p	100 100 135	Data_Sta3_Wedge1_Side1_Chip13_Out0n	$\overline{}$
$\supset$ —	Data_Sta3_Wedge1_Side1_Chip12_Out1p		Data_Sta3_Wedge1_Side1_Chip12_Out1n	$\overline{}$
<u> </u>	Data_Sta3_Wedge1_Side1_Chip12_Out0p	#9933 	Data_Sta3_Wedge1_Side1_Chip12_Out0n	
	Data Sto2 Wadaa4 Sida4 Chin14 Outla	R9931	Data_Sta3_Wedge1_Side1_Chip11_Out1n	
_>_	Data_Sta3_Wedge1_Side1_Chip11_Out1p		Data_Stas_vveuge1_State1_Chilp11_Cut.fil	$\overline{}$
$\supset$	Data_Sta3_Wedge1_Side1_Chip11_Out0p		Data_Sta3_Wedge1_Side1_Chip11_Out0n	$\overline{}$
$\rightarrow$	Data_Sta3_Wedge1_Side1_Chip10_Out1p	100 	Data_Sta3_Wedge1_Side1_Chip10_Out1n	
_	Data_Sta3_Wedge1_Side1_Chip10_Out0p	R9023	Data_Sta3_Wedge1_Side1_Chip10_Out0n	_
$\supset$	Data_Sta3_Wedge1_Side1_Chip09_Out1p	100 100 100 100 100 100	Data_Sta3_Wedge1_Side1_Chip09_Out1n	$\overline{}$
$\supset$ —	Data_Sta0_Wedge1_Side1_Chip04_Out1p		Data_Sta0_Wedge1_Side1_Chip04_Out1n	
	Data_Sta0_Wedge1_Side1_Chip04_Out0p	#9917 	Data_Sta0_Wedge1_Side1_Chip04_Out0n	
	Data Shall Wadané Sidaé Silinas Silina	R9915	Data Stati Wednest Sidest Street State	_
_>_	Data_Sta0_Wedge1_Side1_Chip03_Out1p	10013	Data_Sta0_Wedge1_Side1_Chip03_Out1n	$\overline{}$
$\supset$	Data_Sta0_Wedge1_Side1_Chip03_Out0p		Data_Sta0_Wedge1_Side1_Chip03_Out0n	$\overline{}$
<u> </u>	Data_Sta0_Wedge1_Side1_Chip02_Out1p	#9911 	Data_Sta0_Wedge1_Side1_Chip02_Out1n	
_	Data Stati Wednet Sidot Chin03 Out?~	R9909	Data Staft Warland Sided Chings Outlo-	_
	Data_Sta0_Wedge1_Side1_Chip02_Out0p		Data_Sta0_Wedge1_Side1_Chip02_Out0n	$\overline{}$
$\rightarrow$	Data_Sta0_Wedge1_Side1_Chip01_Out1p		Data_Sta0_Wedge1_Side1_Chip01_Out1n	$\overline{}$
<u> </u>	Data_Sta0_Wedge1_Side1_Chip01_Out0p	£9905	Data_Sta0_Wedge1_Side1_Chip01_Out0n	
_	Data_Sta0_Wedge1_Side0_Chip05_Out1p	<del>1</del> 9903	Data_Sta0_Wedge1_Side0_Chip05_Out1n	_
_>_		#9P01		
$\supset$	Data_Sta0_Wedge1_Side0_Chip05_Out0p	<del></del>	Data_Sta0_Wedge1_Side0_Chip05_Out0n	$\overline{}$
<u> </u>	Data_Sta0_Wedge1_Side0_Chip04_Out1p	#9899 	Data_Sta0_Wedge1_Side0_Chip04_Out1n	
_	Data_Sta0_Wedge1_Side0_Chip04_Out0p	k9897	Data_Sta0_Wedge1_Side0_Chip04_Out0n	_
_>				$\overline{}$

		R1055		
$\longrightarrow$	Data_Sta0_Wedge1_Side0_Chip03_Out1p		Data_Sta0_Wedge1_Side0_Chip03_Out1n	$\overline{}$
	Data_Sta0_Wedge1_Side0_Chip03_Out0p	₽9053	Data_Sta0_Wedge1_Side0_Chip03_Out0n	
		10051		
	Data_Sta0_Wedge1_Side0_Chip02_Out1p		Data_Sta0_Wedge1_Side0_Chip02_Out1n	$\overline{}$
	Data_Sta0_Wedge1_Side0_Chip02_Out0p	#9849 	Data_Sta0_Wedge1_Side0_Chip02_Out0n	
	D	R9847	D	
	Data_Sta0_Wedge1_Side0_Chip01_Out1p	10845	Data_Sta0_Wedge1_Side0_Chip01_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta0_Wedge1_Side0_Chip01_Out0p		Data_Sta0_Wedge1_Side0_Chip01_Out0n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip12_Out0p	R9843	Data_Sta3_Wedge1_Side0_Chip12_Out0n	
				$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip11_Out1p		Data_Sta3_Wedge1_Side0_Chip11_Out1n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip11_Out0p	£9839	Data_Sta3_Wedge1_Side0_Chip11_Out0n	
		10037		
$\Box$	Data_Sta3_Wedge1_Side0_Chip10_Out1p		Data_Sta3_Wedge1_Side0_Chip10_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta3_Wedge1_Side0_Chip10_Out0p	#9835 	Data_Sta3_Wedge1_Side0_Chip10_Out0n	$\overline{}$
	Date Chea Wednest Cideo Chicoo Outle	R9033	Date Class Wardard Cideo Chicoo Cuda	
	Data_Sta3_Wedge1_Side0_Chip09_Out1p		Data_Sta3_Wedge1_Side0_Chip09_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta3_Wedge1_Side0_Chip09_Out0p		Data_Sta3_Wedge1_Side0_Chip09_Out0n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip08_Out1p	R9029	Data_Sta3_Wedge1_Side0_Chip08_Out1n	
				$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip08_Out0p		Data_Sta3_Wedge1_Side0_Chip08_Out0n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip07_Out1p	£9025	Data_Sta3_Wedge1_Side0_Chip07_Out1n	
		R9054		
	Data_Sta3_Wedge1_Side0_Chip07_Out0p	10056	Data_Sta3_Wedge1_Side0_Chip07_Out0n	$\overline{}$
$\longrightarrow$	Data_Sta3_Wedge1_Side0_Chip06_Out1p		Data_Sta3_Wedge1_Side0_Chip06_Out1n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip06_Out0p	R9058	Data_Sta3_Wedge1_Side0_Chip06_Out0n	
		#9860		$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip05_Out1p	<del></del>	Data_Sta3_Wedge1_Side0_Chip05_Out1n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip05_Out0p	#9862 	Data_Sta3_Wedge1_Side0_Chip05_Out0n	
		19064		
$\longrightarrow$	Data_Sta3_Wedge1_Side0_Chip04_Out1p	<b>─</b> ─ <b>──</b>	Data_Sta3_Wedge1_Side0_Chip04_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta3_Wedge1_Side0_Chip04_Out0p	#9866 	Data_Sta3_Wedge1_Side0_Chip04_Out0n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip03_Out1p	₽9078	Data_Sta3_Wedge1_Side0_Chip03_Out1n	
	_uu_ouo_mouge i_oideo_oiiipoo_out ip	1068		$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip03_Out0p	<b></b>	Data_Sta3_Wedge1_Side0_Chip03_Out0n	
	Data_Sta3_Wedge1_Side0_Chip02_Out1p	£9870	Data_Sta3_Wedge1_Side0_Chip02_Out1n	
		#9872		
$\longrightarrow$	Data_Sta3_Wedge1_Side0_Chip02_Out0p	<del></del>	Data_Sta3_Wedge1_Side0_Chip02_Out0n	$\overline{}$
	Data_Sta3_Wedge1_Side0_Chip01_Out1p	19874 	Data_Sta3_Wedge1_Side0_Chip01_Out1n	$\overline{}$
	D	₽9076	Date Class West and Class Class Co.	
$\Box$	Data_Sta3_Wedge1_Side0_Chip01_Out0p		Data_Sta3_Wedge1_Side0_Chip01_Out0n	$\overline{}$
$\longrightarrow$	CLK_FPGA_Bp	<del></del>	CLK_FPGA_Bn	
		100		$\overline{}$

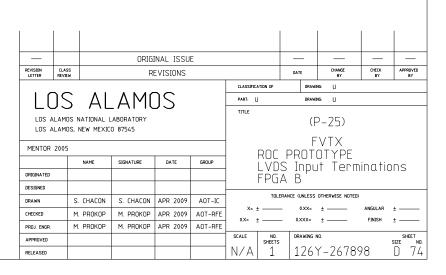
		R1095	
$\supset$ —	Data_Sta3_Wedge1_Side1_Chip09_Out0p		Data_Sta3_Wedge1_Side1_Chip09_Out0n
_	Data Sta3 Wedge1 Side1 Chip08 Out1p	£9093	Data Sta3 Wedge1 Side1 Chip08 Out1n
<u> </u>	Data_Sta3_Wedge1_Side1_Chip08_Out0p	——VV	Data_Sta3_Wedge1_Side1_Chip08_Out0n
_		k9889	
$\rightarrow$	Data_Sta3_Wedge1_Side1_Chip07_Out1p		Data_Sta3_Wedge1_Side1_Chip07_Out1n
<u> </u>	Data_Sta3_Wedge1_Side1_Chip07_Out0p	#9887 	Data_Sta3_Wedge1_Side1_Chip07_Out0n
		19085	
$\supset$ —	Data_Sta3_Wedge1_Side1_Chip06_Out1p		Data_Sta3_Wedge1_Side1_Chip06_Out1n
$\overline{}$	Data Sta3 Wedge1 Side1 Chip06 Out0p	R1083	Data Sta3 Wedge1 Side1 Chip06 Out0n
$\rightarrow$			
<b>&gt;</b>	Data_Sta3_Wedge1_Side1_Chip05_Out1p		Data_Sta3_Wedge1_Side1_Chip05_Out1n
_		k9879	
$\rightarrow$	Data_Sta3_Wedge1_Side1_Chip05_Out0p		Data_Sta3_Wedge1_Side1_Chip05_Out0n
	Data_Sta3_Wedge1_Side1_Chip04_Out1p	£9877	Data_Sta3_Wedge1_Side1_Chip04_Out1n
		19875	
$\rightarrow$	Data_Sta3_Wedge1_Side1_Chip04_Out0p	<del></del>	Data_Sta3_Wedge1_Side1_Chip04_Out0n
_	Data Sta3 Wedge1 Side1 Chip03 Out1p	R9073	Data Sta3 Wedge1 Side1 Chip03 Out1n
<u> </u>	Data_Sta3_Wedge1_Side1_Chip03_Out0p		Data_Sta3_Wedge1_Side1_Chip03_Out0n
_		k9869	
$\rightarrow$	Data_Sta3_Wedge1_Side1_Chip02_Out1p		Data_Sta3_Wedge1_Side1_Chip02_Out1n
	Data_Sta3_Wedge1_Side1_Chip02_Out0p	#9867	Data_Sta3_Wedge1_Side1_Chip02_Out0n
_		19065	
$\supset$ —	Data_Sta3_Wedge1_Side1_Chip01_Out1p		Data_Sta3_Wedge1_Side1_Chip01_Out1n
_	Data_Sta3_Wedge1_Side1_Chip01_Out0p	R9063	Data_Sta3_Wedge1_Side1_Chip01_Out0n
$\rightarrow$			
<u> </u>	Data_Sta3_Wedge1_Side0_Chip13_Out1p	—-VV	Data_Sta3_Wedge1_Side0_Chip13_Out1n
_	D	R1059	
$\rightarrow$	Data_Sta3_Wedge1_Side0_Chip13_Out0p		Data_Sta3_Wedge1_Side0_Chip13_Out0n
	Data_Sta3_Wedge1_Side0_Chip12_Out1p	#9857 	Data_Sta3_Wedge1_Side0_Chip12_Out1n
		100	

_		ORIGINAL ISSUE						_	_	_	_	
REVISION LETTER	CLASS REVIEW	REVISIONS						DATE	CHANGE BY	CHECK BY	APPROVED BY	
							ATION OF	DRAV	/ING: U			
	LOS ALAMOS							DRAV	/ING: U			
LOS A	LOS ALAMOS NATIONAL LABORATORY LOS ALAMOS, NEW MEXICO 87545					(P-25)						
MENTOR	2005					FVTX ROC PROTOTYPE						
		NAME	SIGNATURE	DATE	GROUP				ut Term	ina tic	nne	
ORIGINATED							FPGA	. B	JI ICIIII	III CO II C	,113	
DESIGNED												
DRAWN	5	S. CHACON	S. CHACON	APR 2009	AOT-IC				OTHERWISE NOTED)			
CHECKED	1	1. PROKOP	M. PROKOP	APR 2009	AOT-RFE		-	0.XX= 0.XXX=	±	ANGULAR FINISH	±	
PROJ. ENGR.	١	1. PROKOP	M. PROKOP	APR 2009	AOT-RFE	0.X= ±		0.XXX=	±	FINISH	±	
APPROVED						SCALE	NO. SHEETS	DRAWING	NO.		SHEET SIZE NO.	
RELEASED						N/A	1	126	Y-26789	98	D 73	

	Data_Sta1_Wedge1_Side1_Chip13_Out1p	R1080	Data_Sta1_Wedge1_Side1_Chip13_Out1n	
		£982		
$\Box$	Data_Sta1_Wedge1_Side1_Chip13_Out0p		Data_Sta1_Wedge1_Side1_Chip13_Out0n	$\overline{}$
$\longrightarrow$	Data_Sta1_Wedge1_Side1_Chip12_Out1p		Data_Sta1_Wedge1_Side1_Chip12_Out1n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip12_Out0p	£9086	Data_Sta1_Wedge1_Side1_Chip12_Out0n	
$\longrightarrow$	Data_Sta1_Wedge1_Side1_Chip11_Out1p	<del></del>	Data_Sta1_Wedge1_Side1_Chip11_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta1_Wedge1_Side1_Chip11_Out0p	#9890 	Data_Sta1_Wedge1_Side1_Chip11_Out0n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip10_Out1p	£9892	Data_Sta1_Wedge1_Side1_Chip10_Out1n	
		10094		
$\Box$	Data_Sta1_Wedge1_Side1_Chip10_Out0p		Data_Sta1_Wedge1_Side1_Chip10_Out0n	$\overline{}$
$\overline{}$	Data_Sta1_Wedge1_Side1_Chip09_Out1p	₹9096 	Data_Sta1_Wedge1_Side1_Chip09_Out1n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip09_Out0p	R9098	Data_Sta1_Wedge1_Side1_Chip09_Out0n	
				$\overline{}$
$\Box$	Data_Sta1_Wedge1_Side1_Chip08_Out1p		Data_Sta1_Wedge1_Side1_Chip08_Out1n	$\overline{}$
$\Box$	Data_Sta1_Wedge1_Side1_Chip08_Out0p	#9902	Data_Sta1_Wedge1_Side1_Chip08_Out0n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip07_Out1p	₽99 <sub>04</sub>	Data_Sta1_Wedge1_Side1_Chip07_Out1n	
				$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip07_Out0p		Data_Sta1_Wedge1_Side1_Chip07_Out0n	$\overline{}$
$\Box$	Data_Sta1_Wedge1_Side1_Chip06_Out1p	#9908 	Data_Sta1_Wedge1_Side1_Chip06_Out1n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip06_Out0p	₽9910	Data_Sta1_Wedge1_Side1_Chip06_Out0n	
	Sata_Stat_Freage t_State t_Shippot_Satap		Bala_da1_weage1_dac1_dapod_date1	$\overline{}$
$\Box$	Data_Sta1_Wedge1_Side1_Chip05_Out1p		Data_Sta1_Wedge1_Side1_Chip05_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta1_Wedge1_Side1_Chip05_Out0p	#9914 	Data_Sta1_Wedge1_Side1_Chip05_Out0n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip04_Out1p	₽9916	Data_Sta1_Wedge1_Side1_Chip04_Out1n	
				$\overline{}$
$\Box$	Data_Sta1_Wedge1_Side1_Chip04_Out0p		Data_Sta1_Wedge1_Side1_Chip04_Out0n	$\overline{}$
$\longrightarrow$	Data_Sta1_Wedge1_Side1_Chip03_Out1p	#9920 	Data_Sta1_Wedge1_Side1_Chip03_Out1n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip03_Out0p	10022	Data_Sta1_Wedge1_Side1_Chip03_Out0n	
				$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip02_Out1p	<del></del>	Data_Sta1_Wedge1_Side1_Chip02_Out1n	$\overline{}$
$\overline{}$	Data_Sta1_Wedge1_Side1_Chip02_Out0p	#9936 	Data_Sta1_Wedge1_Side1_Chip02_Out0n	$\overline{}$
	Data_Sta1_Wedge1_Side1_Chip01_Out1p	10026	Data_Sta1_Wedge1_Side1_Chip01_Out1n	
	au_out_rrager_older_ollipsr_out1p			$\overline{}$
$\Box$	Data_Sta1_Wedge1_Side1_Chip01_Out0p		Data_Sta1_Wedge1_Side1_Chip01_Out0n	$\overline{}$
$\Box$	Data_Sta1_Wedge1_Side0_Chip13_Out1p	#9930 	Data_Sta1_Wedge1_Side0_Chip13_Out1n	$\overline{}$
	Data_Sta1_Wedge1_Side0_Chip13_Out0p	₽9932	Data_Sta1_Wedge1_Side0_Chip13_Out0n	
				$\overline{}$
	Data_Sta1_Wedge1_Side0_Chip12_Out1p		Data_Sta1_Wedge1_Side0_Chip12_Out1n	$\overline{}$
$\Box$	BCO_CLK_IN_Bp	1 <del>0</del> 605	BCO_CLK_IN_Bn	$\overline{}$
		100		$\overline{}$

		R1138		
	Data_Sta2_Wedge1_Side1_Chip01_Out1p		Data_Sta2_Wedge1_Side1_Chip01_Out1n	
$\Box$	Data_Sta2_Wedge1_Side1_Chip01_Out0p	#9940 	Data_Sta2_Wedge1_Side1_Chip01_Out0n	$\neg$
	Data_Sta2_Wedge1_Side0_Chip13_Out1p	199 <sub>42</sub>	Data_Sta2_Wedge1_Side0_Chip13_Out1n	_
	Data_Staz_wedge1_stde0_Chip13_Out1p	100 100 144	Data_Sta2_wedge1_side0_Crip13_Out111	Ш
	Data_Sta2_Wedge1_Side0_Chip13_Out0p	<del></del>	Data_Sta2_Wedge1_Side0_Chip13_Out0n	
	Data_Sta2_Wedge1_Side0_Chip12_Out1p	#9946 	Data_Sta2_Wedge1_Side0_Chip12_Out1n	$\neg$
	Data_Sta2_Wedge1_Side0_Chip12_Out0p	100 R9148	Data_Sta2_Wedge1_Side0_Chip12_Out0n	_
	Data_Staz_vveuge1_Stue0_Stilp12_Stutop		Data_staz_vveoga1_stace_critp1z_cutoff	Ш
	Data_Sta2_Wedge1_Side0_Chip11_Out1p		Data_Sta2_Wedge1_Side0_Chip11_Out1n	
	Data_Sta2_Wedge1_Side0_Chip11_Out0p	#9949 	Data_Sta2_Wedge1_Side0_Chip11_Out0n	$\neg$
	D	₽9947		_
	Data_Sta2_Wedge1_Side0_Chip10_Out1p	10045	Data_Sta2_Wedge1_Side0_Chip10_Out1n	$\Box$
$\Box$	Data_Sta2_Wedge1_Side0_Chip10_Out0p		Data_Sta2_Wedge1_Side0_Chip10_Out0n	
	Data_Sta2_Wedge1_Side0_Chip09_Out1p	19943 	Data_Sta2_Wedge1_Side0_Chip09_Out1n	$\overline{}$
		1000 1000 1000 1000 1000 1000 1000 100		_
$\supset$	Data_Sta2_Wedge1_Side0_Chip09_Out0p	100 R1139	Data_Sta2_Wedge1_Side0_Chip09_Out0n	$\Box$
$\longrightarrow$	Data_Sta2_Wedge1_Side0_Chip08_Out1p		Data_Sta2_Wedge1_Side0_Chip08_Out1n	$\neg$
	Data_Sta2_Wedge1_Side0_Chip08_Out0p	<del>1297</del> 6	Data_Sta2_Wedge1_Side0_Chip08_Out0n	_ _
		12978		
	Data_Sta2_Wedge1_Side0_Chip07_Out1p		Data_Sta2_Wedge1_Side0_Chip07_Out1n	
$\longrightarrow$	Data_Sta2_Wedge1_Side0_Chip07_Out0p	<del>1298</del> 0 	Data_Sta2_Wedge1_Side0_Chip07_Out0n	$\neg$
	Data Sta2 Wedge1 Side0 Chip06 Out1p	10982	Data Sta2 Wedge1 Side0 Chip06 Out1n	_
$\longrightarrow$	Data_Sta2_Wedge1_Side0_Chip06_Out0p	1000 1000 1000	Data_Sta2_Wedge1_Side0_Chip06_Out0n	
$\longrightarrow$	Data_Sta2_Wedge1_Side0_Chip05_Out1p		Data_Sta2_Wedge1_Side0_Chip05_Out1n	$\neg$
	Data_Sta2_Wedge1_Side0_Chip05_Out0p	12988	Data_Sta2_Wedge1_Side0_Chip05_Out0n	_
$\Box$	Data_Sta2_Wedge1_Side0_Chip04_Out1p		Data_Sta2_Wedge1_Side0_Chip04_Out1n	
$\Box$	Data_Sta2_Wedge1_Side0_Chip04_Out0p	<del>1</del> <del>2</del> 992	Data_Sta2_Wedge1_Side0_Chip04_Out0n	$\neg$
	Data_Sta2_Wedge1_Side0_Chip03_Out1p	1 <del>0</del> 094	Data_Sta2_Wedge1_Side0_Chip03_Out1n	_
	Sala_Gaz_TradgeT_Gloco_GripoG_GaTp		Data_craz_rreageoraco_cripos_catri	Ш
$\Box$	Data_Sta2_Wedge1_Side0_Chip03_Out0p		Data_Sta2_Wedge1_Side0_Chip03_Out0n	
$\overline{}$	Data_Sta2_Wedge1_Side0_Chip02_Out1p	<del>1</del> 8996 	Data_Sta2_Wedge1_Side0_Chip02_Out1n	$\neg$
	Data Sta2 Wedge1 Side0 Chip02 Out0p	100 100 100 100 100 100 100 100 100 100	Data_Sta2_Wedge1_Side0_Chip02_Out0n	_
	Sale_Staz_weage1_State0_Stripoz_Outop		out_out_rreage i_oute_output_outuri	Ш
$\Box$	Data_Sta2_Wedge1_Side0_Chip01_Out1p		Data_Sta2_Wedge1_Side0_Chip01_Out1n	
$\Box$	Data_Sta2_Wedge1_Side0_Chip01_Out0p	#9802 	Data_Sta2_Wedge1_Side0_Chip01_Out0n	$\neg$
	Date Class Western City Collision City	£9804	Date Class Wardard Cided City Co. 11	_
	Data_Sta2_Wedge1_Side1_Chip13_Out1p		Data_Sta2_Wedge1_Side1_Chip13_Out1n	$\Box$
	Data_Sta2_Wedge1_Side1_Chip13_Out0p		Data_Sta2_Wedge1_Side1_Chip13_Out0n	
	Data_Sta2_Wedge1_Side1_Chip12_Out1p	#9008	Data_Sta2_Wedge1_Side1_Chip12_Out1n	_ —
		₽9810		_
$\supset$	Data_Sta2_Wedge1_Side1_Chip12_Out0p	100	Data_Sta2_Wedge1_Side1_Chip12_Out0n	$\Box$

		R1014	
$\longrightarrow$	Data_Sta2_Wedge1_Side1_Chip11_Out1p		Data_Sta2_Wedge1_Side1_Chip11_Out1n
	Data_Sta2_Wedge1_Side1_Chip11_Out0p	R9816	Data_Sta2_Wedge1_Side1_Chip11_Out0n
	Data_staz_wedge1_stde1_Cnip11_Outop		Data_Sta2_wedge1_stde1_Criip11_Out01
	Data_Sta2_Wedge1_Side1_Chip10_Out1p	£9818	Data_Sta2_Wedge1_Side1_Chip10_Out1n
		10020	
$\longrightarrow$	Data_Sta2_Wedge1_Side1_Chip10_Out0p	——VV	Data_Sta2_Wedge1_Side1_Chip10_Out0n
		£9822	
$\Box$	Data_Sta2_Wedge1_Side1_Chip09_Out1p	<u> </u>	Data_Sta2_Wedge1_Side1_Chip09_Out1n
	Data Sta2 Wedge1 Side1 Chip09 Out0p	R1024	Data Sta2 Wedge1 Side1 Chip09 Out0n
$\longrightarrow$	Data_Sta2_Wedge1_Side1_Chip08_Out1p		Data_Sta2_Wedge1_Side1_Chip08_Out1n
		10028	
$\Box$	Data_Sta2_Wedge1_Side1_Chip08_Out0p		Data_Sta2_Wedge1_Side1_Chip08_Out0n
	Data_Sta2_Wedge1_Side1_Chip07_Out1p	₽9830	Data_Sta2_Wedge1_Side1_Chip07_Out1n
	Buta_otal_wedge _otal _ompor_outrp		Buta_out_medger_order_orapor_out.m
	Data_Sta2_Wedge1_Side1_Chip07_Out0p	R1032	Data_Sta2_Wedge1_Side1_Chip07_Out0n
		10034	
$\overline{}$	Data_Sta2_Wedge1_Side1_Chip06_Out1p	<b></b>	Data_Sta2_Wedge1_Side1_Chip06_Out1n
_	Data Sta2 Wedge1 Side1 Chip06 Out0p	₽9836	Data Sta2 Wedge1 Side1 Chip06 Out0n
	Data_Sta2_wedge1_Side1_Cnipu6_Outup		Data_Sta2_Wedge1_Side1_Chipu6_Outun
	Data_Sta2_Wedge1_Side1_Chip05_Out1p	₽9838	Data_Sta2_Wedge1_Side1_Chip05_Out1n
		19840	
$\longrightarrow$	Data_Sta2_Wedge1_Side1_Chip05_Out0p	——VVV——	Data_Sta2_Wedge1_Side1_Chip05_Out0n
		£9842	
$\Box$	Data_Sta2_Wedge1_Side1_Chip04_Out1p		Data_Sta2_Wedge1_Side1_Chip04_Out1n
	Data_Sta2_Wedge1_Side1_Chip04_Out0p	R9844	Data_Sta2_Wedge1_Side1_Chip04_Out0n
$\longrightarrow$	Data_Sta2_Wedge1_Side1_Chip03_Out1p		Data_Sta2_Wedge1_Side1_Chip03_Out1n
		k9848	
$\Box$	Data_Sta2_Wedge1_Side1_Chip03_Out0p		Data_Sta2_Wedge1_Side1_Chip03_Out0n
	Data_Sta2_Wedge1_Side1_Chip02_Out1p	R1050	Data_Sta2_Wedge1_Side1_Chip02_Out1n
	out-onpos_out-p		
	Data_Sta2_Wedge1_Side1_Chip02_Out0p	#1052 ^^^	Data_Sta2_Wedge1_Side1_Chip02_Out0n
		100	



$\rightarrow$	Data_Sta1_Wedge2_Side0_Chip12_Out0p	R833	Data_Sta1_Wedge2_Side0_Chip12_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge2_Side0_Chip11_Out1p	<del>1</del> 8831 	Data_Sta1_Wedge2_Side0_Chip11_Out1n	
	Data_Sta1_Wedge2_Side0_Chip11_Out0p	1000 17829	Data_Sta1_Wedge2_Side0_Chip11_Out0n	
_	Data_Sta1_Wedge2_Side0_Chip10_Out1p	1002 10027	Data_Sta1_Wedge2_Side0_Chip10_Out1n	
		1000 1000 18825		
	Data_Sta1_Wedge2_Side0_Chip10_Out0p		Data_Sta1_Wedge2_Side0_Chip10_Out0n	
$\rightarrow$	Data_Sta1_Wedge2_Side0_Chip09_Out1p	1000 18821	Data_Sta1_Wedge2_Side0_Chip09_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip09_Out0p		Data_Sta1_Wedge2_Side0_Chip09_Out0n	$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip08_Out1p		Data_Sta1_Wedge2_Side0_Chip08_Out1n	$\overline{}$
<u> </u>	Data_Sta1_Wedge2_Side0_Chip08_Out0p		Data_Sta1_Wedge2_Side0_Chip08_Out0n	
_ 	Data_Sta1_Wedge2_Side0_Chip07_Out1p	<del>1</del> 8815	Data_Sta1_Wedge2_Side0_Chip07_Out1n	$\overline{}$
	Data_Sta1_Wedge2_Side0_Chip07_Out0p	1 <del>88</del> 13	Data_Sta1_Wedge2_Side0_Chip07_Out0n	
	Data_Sta1_Wedge2_Side0_Chip06_Out1p	<del>1</del> 2891	Data_Sta1_Wedge2_Side0_Chip06_Out1n	
				$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip06_Out0p		Data_Sta1_Wedge2_Side0_Chip06_Out0n	$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip05_Out1p		Data_Sta1_Wedge2_Side0_Chip05_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip05_Out0p		Data_Sta1_Wedge2_Side0_Chip05_Out0n	$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip04_Out1p	<del>188</del> 03	Data_Sta1_Wedge2_Side0_Chip04_Out1n	$\overline{}$
_ 	Data_Sta1_Wedge2_Side0_Chip04_Out0p	<del>1</del> 8861	Data_Sta1_Wedge2_Side0_Chip04_Out0n	
	Data_Sta1_Wedge2_Side0_Chip03_Out1p	1 <del>2</del> 799	Data_Sta1_Wedge2_Side0_Chip03_Out1n	
	Data_Sta1_Wedge2_Side0_Chip03_Out0p	1 <del>R</del> 947	Data_Sta1_Wedge2_Side0_Chip03_Out0n	
		1 <del>8</del> 945		$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip02_Out1p		Data_Sta1_Wedge2_Side0_Chip02_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip02_Out0p	1 <del>00</del> 41	Data_Sta1_Wedge2_Side0_Chip02_Out0n	$\overline{}$
$\supset$ —	Data_Sta1_Wedge2_Side0_Chip01_Out1p		Data_Sta1_Wedge2_Side0_Chip01_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge2_Side0_Chip01_Out0p	<del>1</del> <del>8</del> 939 	Data_Sta1_Wedge2_Side0_Chip01_Out0n	$\overline{}$
	Data_Sta0_Wedge2_Side1_Chip05_Out1p	<del>1</del> <del>8</del> 921 	Data_Sta0_Wedge2_Side1_Chip05_Out1n	
	Data_Sta0_Wedge2_Side1_Chip05_Out0p	1 <del>0</del> 937	Data_Sta0_Wedge2_Side1_Chip05_Out0n	
_	Data_Sta3_Wedge2_Side1_Chip13_Out1p	1 <del>2</del> 935	Data_Sta3_Wedge2_Side1_Chip13_Out1n	_
		1000 1R933		
	Data_Sta3_Wedge2_Side1_Chip13_Out0p		Data_Sta3_Wedge2_Side1_Chip13_Out0n	
$\supset$	Data_Sta3_Wedge2_Side1_Chip12_Out1p		Data_Sta3_Wedge2_Side1_Chip12_Out1n	$\overline{}$
$\supset$	Data_Sta3_Wedge2_Side1_Chip12_Out0p	1 <del>8</del> 927	Data_Sta3_Wedge2_Side1_Chip12_Out0n	$\overline{}$
$\supset$	Data_Sta3_Wedge2_Side1_Chip11_Out1p		Data_Sta3_Wedge2_Side1_Chip11_Out1n	$\overline{}$
$\supset$	Data_Sta3_Wedge2_Side1_Chip11_Out0p	1 <del>2</del> 925	Data_Sta3_Wedge2_Side1_Chip11_Out0n	$\overline{}$
<u> </u>	Data_Sta3_Wedge2_Side1_Chip10_Out1p	1 <del>2</del> 923 	Data_Sta3_Wedge2_Side1_Chip10_Out1n	
	Data_Sta3_Wedge2_Side1_Chip10_Out0p	1 <del>1</del> 8919	Data_Sta3_Wedge2_Side1_Chip10_Out0n	
	Data_Sta3_Wedge2_Side1_Chip09_Out1p	12917	Data_Sta3_Wedge2_Side1_Chip09_Out1n	_
	Data_Sta0_Wedge2_Side1_Chip04_Out1p	1 <del>29</del> 15		
			Data_Sta0_Wedge2_Side1_Chip04_Out1n	
$\supset$	Data_Sta0_Wedge2_Side1_Chip04_Out0p		Data_Sta0_Wedge2_Side1_Chip04_Out0n	$\overline{}$
$\supset$	Data_Sta0_Wedge2_Side1_Chip03_Out1p		Data_Sta0_Wedge2_Side1_Chip03_Out1n	$\overline{}$
$\supset$	Data_Sta0_Wedge2_Side1_Chip03_Out0p	1 <del>8</del> 969	Data_Sta0_Wedge2_Side1_Chip03_Out0n	$\overline{}$
<u> </u>	Data_Sta0_Wedge2_Side1_Chip02_Out1p	1 <del>8</del> 967	Data_Sta0_Wedge2_Side1_Chip02_Out1n	
	Data_Sta0_Wedge2_Side1_Chip02_Out0p	1005 	Data_Sta0_Wedge2_Side1_Chip02_Out0n	
	Data_Sta0_Wedge2_Side1_Chip01_Out1p	<del>1</del> R963	Data_Sta0_Wedge2_Side1_Chip01_Out1n	_
<u> </u>	Data_Sta0_Wedge2_Side1_Chip01_Out0p		Data_Sta0_Wedge2_Side1_Chip01_Out0n	$\overline{}$
$\supset$	Data_Sta0_Wedge2_Side0_Chip05_Out1p		Data_Sta0_Wedge2_Side0_Chip05_Out1n	$\overline{}$
$\supset$	Data_Sta0_Wedge2_Side0_Chip05_Out0p	<u>-</u>	Data_Sta0_Wedge2_Side0_Chip05_Out0n	$\overline{}$
$\supset$	Data_Sta0_Wedge2_Side0_Chip04_Out1p	1 <del>88</del> 95	Data_Sta0_Wedge2_Side0_Chip04_Out1n	
_		<del>1</del> 8893		_

$\longrightarrow$	Data_Sta0_Wedge2_Side0_Chip03_Out1p	R851	Data_Sta0_Wedge2_Side0_Chip03_Out1n
	Data_Sta0_Wedge2_Side0_Chip03_Out0p	<del>1284</del> 9 	Data_Sta0_Wedge2_Side0_Chip03_Out0n
	Data_Sta0_Wedge2_Side0_Chip02_Out1p	1 <del>0</del> 847	Data_Sta0_Wedge2_Side0_Chip02_Out1n
	Data_Sta0_Wedge2_Side0_Chip02_Out0p	10045	Data_Sta0_Wedge2_Side0_Chip02_Out0n
	Data_Sta0_Wedge2_Side0_Chip01_Out1p	1000 1000 1000 1000 1000	Data_Sta0_Wedge2_Side0_Chip01_Out1n
	Data_Sta0_Wedge2_Side0_Chip01_Out0p	1000 1000 1000	Data_Sta0_Wedge2_Side0_Chip01_Out0n
		100 100 100 100 100 100	
	Data_Sta3_Wedge2_Side0_Chip12_Out0p		Data_Sta3_Wedge2_Side0_Chip12_Out0n
ightharpoons	Data_Sta3_Wedge2_Side0_Chip11_Out1p		Data_Sta3_Wedge2_Side0_Chip11_Out1n
$\Box$	Data_Sta3_Wedge2_Side0_Chip11_Out0p		Data_Sta3_Wedge2_Side0_Chip11_Out0n
	Data_Sta3_Wedge2_Side0_Chip10_Out1p		Data_Sta3_Wedge2_Side0_Chip10_Out1n
$\longrightarrow$	Data_Sta3_Wedge2_Side0_Chip10_Out0p		Data_Sta3_Wedge2_Side0_Chip10_Out0n
	Data_Sta3_Wedge2_Side0_Chip09_Out1p	1 <del>1</del> 8894 	Data_Sta3_Wedge2_Side0_Chip09_Out1n
$\Box$	Data_Sta3_Wedge2_Side0_Chip09_Out0p	100 100 100 100 100 100 100 100 100 100	Data_Sta3_Wedge2_Side0_Chip09_Out0n
$\overline{}$	Data_Sta3_Wedge2_Side0_Chip08_Out1p	100 17898 	Data_Sta3_Wedge2_Side0_Chip08_Out1n
	Data_Sta3_Wedge2_Side0_Chip08_Out0p	<del>1</del> 8900	Data_Sta3_Wedge2_Side0_Chip08_Out0n
	Data_Sta3_Wedge2_Side0_Chip07_Out1p	<del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del> <del>1</del>	Data_Sta3_Wedge2_Side0_Chip07_Out1n
	Data_Sta3_Wedge2_Side0_Chip07_Out0p	18904	Data_Sta3_Wedge2_Side0_Chip07_Out0n
	Data_Sta3_Wedge2_Side0_Chip06_Out1p	1 <del>2</del> 906	Data_Sta3_Wedge2_Side0_Chip06_Out1n
	Data_Sta3_Wedge2_Side0_Chip06_Out0p	1 <del>0</del> 008	Data_Sta3_Wedge2_Side0_Chip06_Out0n
	Data_Sta3_Wedge2_Side0_Chip05_Out1p		Data_Sta3_Wedge2_Side0_Chip05_Out1n
	Data_Sta3_Wedge2_Side0_Chip05_Out0p	100 18914	Data_Sta3_Wedge2_Side0_Chip05_Out0n
$\Box$	Data_Sta3_Wedge2_Side0_Chip04_Out1p	1 <del>1</del> 8916	Data_Sta3_Wedge2_Side0_Chip04_Out1n
	Data_Sta3_Wedge2_Side0_Chip04_Out0p		Data_Sta3_Wedge2_Side0_Chip04_Out0n
	Data_Sta3_Wedge2_Side0_Chip03_Out1p		Data_Sta3_Wedge2_Side0_Chip03_Out1n
$\Box$	Data_Sta3_Wedge2_Side0_Chip03_Out0p		Data_Sta3_Wedge2_Side0_Chip03_Out0n
$\Box$	Data_Sta3_Wedge2_Side0_Chip02_Out1p	18920 	Data_Sta3_Wedge2_Side0_Chip02_Out1n
$\Box$	Data_Sta3_Wedge2_Side0_Chip02_Out0p	18922 	Data_Sta3_Wedge2_Side0_Chip02_Out0n
	Data_Sta3_Wedge2_Side0_Chip01_Out1p	10024 	Data_Sta3_Wedge2_Side0_Chip01_Out1n
	Data_Sta3_Wedge2_Side0_Chip01_Out0p	1 <del>2</del> 926	Data_Sta3_Wedge2_Side0_Chip01_Out0n
	CLK_FPGA_Cp	#9328	CLK_FPGA_Cn
		100	

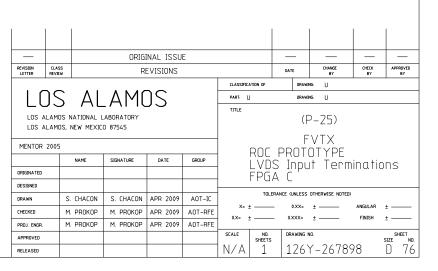
		R891	
$\supset$ —	Data_Sta3_Wedge2_Side1_Chip09_Out0p		Data_Sta3_Wedge2_Side1_Chip09_Out0n
_	Data_Sta3_Wedge2_Side1_Chip08_Out1p	<del>100</del> 9 	Data_Sta3_Wedge2_Side1_Chip08_Out1n
		1000 1000 1000 1000	
$\rightarrow$	Data_Sta3_Wedge2_Side1_Chip08_Out0p		Data_Sta3_Wedge2_Side1_Chip08_Out0n
_	Data Sta3 Wedge2 Side1 Chip07 Out1p	1000 1000 1000 1000	Data Sta3 Wedge2 Side1 Chip07 Out1n
$\rightarrow$	Data_Sta3_Wedge2_Side1_Chip07_Out0p	——W——	Data_Sta3_Wedge2_Side1_Chip07_Out0n
_	D	<del>1000</del> 1 <del>1000</del> 1	
$\rightarrow$	Data_Sta3_Wedge2_Side1_Chip06_Out1p	100	Data_Sta3_Wedge2_Side1_Chip06_Out1n
<b>\</b>	Data_Sta3_Wedge2_Side1_Chip06_Out0p	100 17879 	Data_Sta3_Wedge2_Side1_Chip06_Out0n
_		<del>1007</del> 7	
$\supset$ —	Data_Sta3_Wedge2_Side1_Chip05_Out1p		Data_Sta3_Wedge2_Side1_Chip05_Out1n
	Data_Sta3_Wedge2_Side1_Chip05_Out0p	10075 	Data_Sta3_Wedge2_Side1_Chip05_Out0n
		100	
$\rightarrow$ —	Data_Sta3_Wedge2_Side1_Chip04_Out1p	<del></del>	Data_Sta3_Wedge2_Side1_Chip04_Out1n
_	Data Sta3 Wedge2 Side1 Chip04 Out0p	1 <del>000</del> 1	Data Sta3 Wedge2 Side1 Chip04 Out0n
>	Data_Sta3_Wedge2_Side1_Chip03_Out1p		Data_Sta3_Wedge2_Side1_Chip03_Out1n
_	Data Sta3 Wedge2 Side1 Chip03 Out0p	<del>1</del> <del>1</del> 867	Data Sta3 Wedge2 Side1 Chip03 Out0n
<i></i>	Baa_otab_treagez_otact_ompos_otab		Bala_dab_wedgez_date i_diiipod_dateii
>	Data_Sta3_Wedge2_Side1_Chip02_Out1p		Data_Sta3_Wedge2_Side1_Chip02_Out1n
_	Data Sta3 Wedge2 Side1 Chip02 Out0p	<del>1000</del> 3	Data Sta3 Wedge2 Side1 Chip02 Out0n
$\rightarrow$	Data_Stas_rreugez_Stde1_CriipUz_Outup		Data_Gtab_vveugez_Gtde+_Ctilipuz_Gtdt01
<u> </u>	Data_Sta3_Wedge2_Side1_Chip01_Out1p		Data_Sta3_Wedge2_Side1_Chip01_Out1n
_	D	100 100 100 100 100 100 100 100 100 100	
$\rightarrow$	Data_Sta3_Wedge2_Side1_Chip01_Out0p	100	Data_Sta3_Wedge2_Side1_Chip01_Out0n
<u> </u>	Data_Sta3_Wedge2_Side0_Chip13_Out1p	<del>100</del> 57	Data_Sta3_Wedge2_Side0_Chip13_Out1n
_		1000 1005 1005	
$\supset$ —	Data_Sta3_Wedge2_Side0_Chip13_Out0p		Data_Sta3_Wedge2_Side0_Chip13_Out0n
	Data_Sta3_Wedge2_Side0_Chip12_Out1p	100 1 <del>1</del> 8853	Data_Sta3_Wedge2_Side0_Chip12_Out1n
		100	

_			ORIGI	NAL ISSU	E			_	_	_	<u> </u>
REVISION LETTER	CLASS REVIEW		RE	EVISIONS				DATE	CHANGE BY	CHECK BY	APPROVED BY
						CLASSIFICA	ATION OF	DRA	wing: U		
LOS ALAMOS			PART: L		DRA	WING: U					
LOS A	LAMOS		ABORATORY	, ,		TITLE			P-25)		
MENTOR	2005						חחר		FVTX		
		NAME	SIGNATURE	DATE	GROUP				OTYPE	inatic	nn c
ORIGINATED						- LVDS Input Termination: - FPGA (			1113		
DESIGNED											
DRAWN	S	S. CHACON	S. CHACON	APR 2009	AOT-IC				S OTHERWISE NOTED)		
CHECKED	١	1. PROKOP	M. PROKOP	APR 2009	AOT-RFE	0.X= -	. —	0.XX= 0.XXX=	-	ANGULAR FINISH	±
PROJ. ENGR.	١	1. PROKOP	M. PROKOP	APR 2009	AOT-RFE	V.A= 3		v.^^^=	<u>. — </u>	neam	<u>. — </u>
APPROVED						SCALE	NO. SHEETS	DRAWING			SHEET SIZE NO.
RELEASED						N/A	1	126	Y-26789	98	D 75

$\Box$	Data_Sta1_Wedge2_Side1_Chip13_Out1p	R930	Data_Sta1_Wedge2_Side1_Chip13_Out1n	—		Data_Sta2_Wedge2_Side1
$\overline{}$	Data_Sta1_Wedge2_Side1_Chip13_Out0p	1 <del>R9</del> 32	Data_Sta1_Wedge2_Side1_Chip13_Out0n	<u> </u>		Data_Sta2_Wedge2_Side1
$\overline{\Box}$	Data_Sta1_Wedge2_Side1_Chip12_Out1p	1R934	Data_Sta1_Wedge2_Side1_Chip12_Out1n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip12_Out0p	1 <del>2</del> 936	Data_Sta1_Wedge2_Side1_Chip12_Out0n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip11_Out1p	<del>1</del> 0038	Data_Sta1_Wedge2_Side1_Chip11_Out1n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip11_Out0p		Data_Sta1_Wedge2_Side1_Chip11_Out0n			Data_Sta2_Wedge2_Side0
$\Box$	Data_Sta1_Wedge2_Side1_Chip10_Out1p		Data_Sta1_Wedge2_Side1_Chip10_Out1n			Data_Sta2_Wedge2_Side0
$\Box$	Data_Sta1_Wedge2_Side1_Chip10_Out0p	1 <del>89</del> 46	Data_Sta1_Wedge2_Side1_Chip10_Out0n	$\overline{}$		Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip09_Out1p		Data_Sta1_Wedge2_Side1_Chip09_Out1n	—		Data_Sta2_Wedge2_Side0
$\longrightarrow$	Data_Sta1_Wedge2_Side1_Chip09_Out0p		Data_Sta1_Wedge2_Side1_Chip09_Out0n	—		Data_Sta2_Wedge2_Side0
$\Box$	Data_Sta1_Wedge2_Side1_Chip08_Out1p	100 100 100 100 100 100 100 100 100 100	Data_Sta1_Wedge2_Side1_Chip08_Out1n			Data_Sta2_Wedge2_Side0
$\Box$	Data_Sta1_Wedge2_Side1_Chip08_Out0p	1 <del>2</del> 952	Data_Sta1_Wedge2_Side1_Chip08_Out0n	—	ightharpoons	Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip07_Out1p	100 18954 	Data_Sta1_Wedge2_Side1_Chip07_Out1n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip07_Out0p	<del>1</del> 8956 	Data_Sta1_Wedge2_Side1_Chip07_Out0n	<u> </u>		Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip06_Out1p	1 <del>R</del> 958	Data_Sta1_Wedge2_Side1_Chip06_Out1n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip06_Out0p	1 <del>2</del> 960	Data_Sta1_Wedge2_Side1_Chip06_Out0n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip05_Out1p	1 <del>1</del> 0062	Data_Sta1_Wedge2_Side1_Chip05_Out1n			Data_Sta2_Wedge2_Side0
		1 <del>R9</del> 64				
	Data_Sta1_Wedge2_Side1_Chip05_Out0p		Data_Sta1_Wedge2_Side1_Chip05_Out0n			Data_Sta2_Wedge2_Side0
$\supset$	Data_Sta1_Wedge2_Side1_Chip04_Out1p		Data_Sta1_Wedge2_Side1_Chip04_Out1n		ightharpoons	Data_Sta2_Wedge2_Side0
$\overline{}$	Data_Sta1_Wedge2_Side1_Chip04_Out0p		Data_Sta1_Wedge2_Side1_Chip04_Out0n			Data_Sta2_Wedge2_Side0
$\Box$	Data_Sta1_Wedge2_Side1_Chip03_Out1p	10072	Data_Sta1_Wedge2_Side1_Chip03_Out1n	$\overline{}$		Data_Sta2_Wedge2_Side0
$\Box$	Data_Sta1_Wedge2_Side1_Chip03_Out0p	<b></b>	Data_Sta1_Wedge2_Side1_Chip03_Out0n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip02_Out1p	1 <del>297</del> 4 	Data_Sta1_Wedge2_Side1_Chip02_Out1n	—		Data_Sta2_Wedge2_Side0
$\Box$	Data_Sta1_Wedge2_Side1_Chip02_Out0p	1 <del>R</del> 963	Data_Sta1_Wedge2_Side1_Chip02_Out0n	—		Data_Sta2_Wedge2_Side0
ightharpoons	Data_Sta1_Wedge2_Side1_Chip01_Out1p	10073 	Data_Sta1_Wedge2_Side1_Chip01_Out1n		ightharpoonup	Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side1_Chip01_Out0p	1007 1 	Data_Sta1_Wedge2_Side1_Chip01_Out0n		$\overline{}$	Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side0_Chip13_Out1p	<del>189</del> 69 	Data_Sta1_Wedge2_Side0_Chip13_Out1n		ightharpoonup	Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side0_Chip13_Out0p	1 <del>1</del> 8967	Data_Sta1_Wedge2_Side0_Chip13_Out0n			Data_Sta2_Wedge2_Side0
	Data_Sta1_Wedge2_Side0_Chip12_Out1p	1 <del>2</del> 965	Data_Sta1_Wedge2_Side0_Chip12_Out1n			Data_Sta2_Wedge2_Side1
	BCO_CLK_IN_Cp	<del>1</del> 2606	BCO_CLK_IN_Cn			Data_Sta2_Wedge2_Side1
		100				Data_Sta2_Wedge2_Side1
						Data_Gtaz_vveugez_Stde I

$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip01_Out1p	R961	Data_Sta2_Wedge2_Side1_Chip01_Out1n	$\overline{}$
	Data_Sta2_Wedge2_Side1_Chip01_Out0p	1 <del>1095</del> 9	Data_Sta2_Wedge2_Side1_Chip01_Out0n	_
	Data_Sta2_Wedge2_Side0_Chip13_Out1p	18957 	Data_Sta2_Wedge2_Side0_Chip13_Out1n	
	Data_Sta2_Wedge2_Side0_Chip13_Out0p	1 <del>0</del> 955	Data_Sta2_Wedge2_Side0_Chip13_Out0n	
				$\overline{}$
$\supset$	Data_Sta2_Wedge2_Side0_Chip12_Out1p		Data_Sta2_Wedge2_Side0_Chip12_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta2_Wedge2_Side0_Chip12_Out0p		Data_Sta2_Wedge2_Side0_Chip12_Out0n	$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip11_Out1p		Data_Sta2_Wedge2_Side0_Chip11_Out1n	$\overline{}$
$\longrightarrow$	Data_Sta2_Wedge2_Side0_Chip11_Out0p	<del></del>	Data_Sta2_Wedge2_Side0_Chip11_Out0n	$\overline{}$
$\overline{}$	Data_Sta2_Wedge2_Side0_Chip10_Out1p	1002 	Data_Sta2_Wedge2_Side0_Chip10_Out1n	_
	Data_Sta2_Wedge2_Side0_Chip10_Out0p	1004 17804	Data_Sta2_Wedge2_Side0_Chip10_Out0n	
	Data_Sta2_Wedge2_Side0_Chip09_Out1p	<del>1</del> 006	Data_Sta2_Wedge2_Side0_Chip09_Out1n	
				$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip09_Out0p		Data_Sta2_Wedge2_Side0_Chip09_Out0n	$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip08_Out1p		Data_Sta2_Wedge2_Side0_Chip08_Out1n	$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip08_Out0p		Data_Sta2_Wedge2_Side0_Chip08_Out0n	$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip07_Out1p	<del>1</del> 8814	Data_Sta2_Wedge2_Side0_Chip07_Out1n	$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip07_Out0p	<del>1</del> 2816 	Data_Sta2_Wedge2_Side0_Chip07_Out0n	_
	Data_Sta2_Wedge2_Side0_Chip06_Out1p	1001 17818 	Data_Sta2_Wedge2_Side0_Chip06_Out1n	
	Data_Sta2_Wedge2_Side0_Chip06_Out0p	1000 10020	Data_Sta2_Wedge2_Side0_Chip06_Out0n	
		1000 18822		$\overline{}$
	Data_Sta2_Wedge2_Side0_Chip05_Out1p		Data_Sta2_Wedge2_Side0_Chip05_Out1n	$\overline{}$
$\overline{}$	Data_Sta2_Wedge2_Side0_Chip05_Out0p	1000 1000 178826	Data_Sta2_Wedge2_Side0_Chip05_Out0n	$\overline{}$
	Data_Sta2_Wedge2_Side0_Chip04_Out1p		Data_Sta2_Wedge2_Side0_Chip04_Out1n	$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip04_Out0p	<b></b>	Data_Sta2_Wedge2_Side0_Chip04_Out0n	$\overline{}$
$\longrightarrow$	Data_Sta2_Wedge2_Side0_Chip03_Out1p	1 <del>2</del> 830 	Data_Sta2_Wedge2_Side0_Chip03_Out1n	$\overline{}$
	Data_Sta2_Wedge2_Side0_Chip03_Out0p	100 17848	Data_Sta2_Wedge2_Side0_Chip03_Out0n	_
	Data_Sta2_Wedge2_Side0_Chip02_Out1p	1000 10832	Data_Sta2_Wedge2_Side0_Chip02_Out1n	
	Data_Sta2_Wedge2_Side0_Chip02_Out0p	1000 178834	Data_Sta2_Wedge2_Side0_Chip02_Out0n	
				$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side0_Chip01_Out1p		Data_Sta2_Wedge2_Side0_Chip01_Out1n	$\overline{}$
	Data_Sta2_Wedge2_Side0_Chip01_Out0p		Data_Sta2_Wedge2_Side0_Chip01_Out0n	$\overline{}$
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip13_Out1p		Data_Sta2_Wedge2_Side1_Chip13_Out1n	$\overline{}$
$\Box$	Data_Sta2_Wedge2_Side1_Chip13_Out0p	1 <del>2</del> 842	Data_Sta2_Wedge2_Side1_Chip13_Out0n	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip12_Out1p	1000 17844 	Data_Sta2_Wedge2_Side1_Chip12_Out1n	$\overline{}$
	Data_Sta2_Wedge2_Side1_Chip12_Out0p	1000 178846	Data_Sta2_Wedge2_Side1_Chip12_Out0n	
		100		

		D050	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip11_Out1p	R850 	Data_Sta2_Wedge2_Side1_Chip11_Out1n
		1005 10052	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip11_Out0p		Data_Sta2_Wedge2_Side1_Chip11_Out0n
	Data_Sta2_Wedge2_Side1_Chip10_Out1p	<del>1</del> 0054	Data_Sta2_Wedge2_Side1_Chip10_Out1n
	Data_Staz_wedgez_Stde1_Cliip10_Cdt1p		Data_Staz_Wedgez_Stde1_Criip10_Stdt11
	Data_Sta2_Wedge2_Side1_Chip10_Out0p	1 <del>000</del> 1 <del>1000</del> 1000	Data_Sta2_Wedge2_Side1_Chip10_Out0n
		1000 R858	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip09_Out1p		Data_Sta2_Wedge2_Side1_Chip09_Out1n
		<del>1</del> 000	
$\Box$	Data_Sta2_Wedge2_Side1_Chip09_Out0p	<u> </u>	Data_Sta2_Wedge2_Side1_Chip09_Out0n
	Data_Sta2_Wedge2_Side1_Chip08_Out1p	1 <del>1</del> 7862	Data_Sta2_Wedge2_Side1_Chip08_Out1n
	Data_Sta2_Wedge2_Side1_Chip08_Out0p	<del>1</del> 8664 	Data_Sta2_Wedge2_Side1_Chip08_Out0n
		1000 18866	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip07_Out1p	<del></del>	Data_Sta2_Wedge2_Side1_Chip07_Out1n
_	D	<del>1</del> 8868	
	Data_Sta2_Wedge2_Side1_Chip07_Out0p		Data_Sta2_Wedge2_Side1_Chip07_Out0n
	Data_Sta2_Wedge2_Side1_Chip06_Out1p	1 <del>()()</del> 70	Data_Sta2_Wedge2_Side1_Chip06_Out1n
		1000 R872	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip06_Out0p		Data_Sta2_Wedge2_Side1_Chip06_Out0n
		<del>100</del> 74	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip05_Out1p	<b>\</b>	Data_Sta2_Wedge2_Side1_Chip05_Out1n
	Data_Sta2_Wedge2_Side1_Chip05_Out0p	<del>1</del> <del>287</del> 6	Data_Sta2_Wedge2_Side1_Chip05_Out0n
		10078	
ightharpoons	Data_Sta2_Wedge2_Side1_Chip04_Out1p		Data_Sta2_Wedge2_Side1_Chip04_Out1n
		<del>18</del> 880	
	Data_Sta2_Wedge2_Side1_Chip04_Out0p		Data_Sta2_Wedge2_Side1_Chip04_Out0n
	Data Sta2 Wedge2 Side1 Chip03 Out1p	1000 1000 1000 1000 1000 1000 1000 100	Data Sta2 Wedge2 Side1 Chip03 Out1n
	Sua_suar_mosgar_suber_Shipos_outrp		Sus_ous_rosgus_ousos/pus_outil
	Data_Sta2_Wedge2_Side1_Chip03_Out0p	#R884 ^^^-	Data_Sta2_Wedge2_Side1_Chip03_Out0n
		1000 1000 1000	
$\longrightarrow$	Data_Sta2_Wedge2_Side1_Chip02_Out1p		Data_Sta2_Wedge2_Side1_Chip02_Out1n
_	D	1 <del>0</del> 0088	
$\Box$	Data_Sta2_Wedge2_Side1_Chip02_Out0p		Data_Sta2_Wedge2_Side1_Chip02_Out0n
		100	



$\supset$	Data_Sta1_Wedge3_Side0_Chip12_Out0p	R643	Data_Sta1_Wedge3_Side0_Chip12_Out0n	
	Data_Sta1_Wedge3_Side0_Chip11_Out1p	10041 	Data_Sta1_Wedge3_Side0_Chip11_Out1n	
	Data_Sta1_Wedge3_Side0_Chip11_Out0p	1 <del>1</del> 0039	Data_Sta1_Wedge3_Side0_Chip11_Out0n	
$\supset$	Data_Sta1_Wedge3_Side0_Chip10_Out1p	1000 18635	Data_Sta1_Wedge3_Side0_Chip10_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge3_Side0_Chip10_Out0p		Data_Sta1_Wedge3_Side0_Chip10_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge3_Side0_Chip09_Out1p	<del>1</del> 8633 	Data_Sta1_Wedge3_Side0_Chip09_Out1n	
	Data_Sta1_Wedge3_Side0_Chip09_Out0p	100 18631	Data_Sta1_Wedge3_Side0_Chip09_Out0n	
	Data_Sta1_Wedge3_Side0_Chip08_Out1p	1 <del>1</del> 0029	Data_Sta1_Wedge3_Side0_Chip08_Out1n	
	Data_Sta1_wedges_Stdeo_Criipbio_Cdc1p			
$\supset$	Data_Sta1_Wedge3_Side0_Chip08_Out0p	10025	Data_Sta1_Wedge3_Side0_Chip08_Out0n	$\overline{}$
$\supset$	Data_Sta1_Wedge3_Side0_Chip07_Out1p		Data_Sta1_Wedge3_Side0_Chip07_Out1n	$\overline{}$
$\rightarrow$	Data_Sta1_Wedge3_Side0_Chip07_Out0p	10023 	Data_Sta1_Wedge3_Side0_Chip07_Out0n	
	Data_Sta1_Wedge3_Side0_Chip06_Out1p	<del>10/6</del> 7	Data_Sta1_Wedge3_Side0_Chip06_Out1n	
	Date Stat Western Sides Chiefs Outlon	1 <del>000</del> 55	Date Cled Wednes Cides Chines Outon	
_>_	Data_Sta1_Wedge3_Side0_Chip06_Out0p		Data_Sta1_Wedge3_Side0_Chip06_Out0n	$\overline{}$
$\supset$	Data_Sta1_Wedge3_Side0_Chip05_Out1p	1005 1005 18751	Data_Sta1_Wedge3_Side0_Chip05_Out1n	$\overline{}$
$\supset$	Data_Sta1_Wedge3_Side0_Chip05_Out0p		Data_Sta1_Wedge3_Side0_Chip05_Out0n	$\overline{}$
<u> </u>	Data_Sta1_Wedge3_Side0_Chip04_Out1p	<del>1</del> 8749 	Data_Sta1_Wedge3_Side0_Chip04_Out1n	
_	Data_Sta1_Wedge3_Side0_Chip04_Out0p	10747	Data_Sta1_Wedge3_Side0_Chip04_Out0n	_
		10745		
$\rightarrow$	Data_Sta1_Wedge3_Side0_Chip03_Out1p	100 18743	Data_Sta1_Wedge3_Side0_Chip03_Out1n	-
$\supset$	Data_Sta1_Wedge3_Side0_Chip03_Out0p		Data_Sta1_Wedge3_Side0_Chip03_Out0n	$\overline{}$
$\supset$ —	Data_Sta1_Wedge3_Side0_Chip02_Out1p	<del>100</del> 41	Data_Sta1_Wedge3_Side0_Chip02_Out1n	$\overline{}$
_ 	Data_Sta1_Wedge3_Side0_Chip02_Out0p	1 <del>00</del> 39	Data_Sta1_Wedge3_Side0_Chip02_Out0n	
_	Data_Sta1_Wedge3_Side0_Chip01_Out1p	10037	Data_Sta1_Wedge3_Side0_Chip01_Out1n	
$\supset$	Data_Sta1_Wedge3_Side0_Chip01_Out0p		Data_Sta1_Wedge3_Side0_Chip01_Out0n	$\overline{}$
$\supset$	Data_Sta0_Wedge3_Side1_Chip05_Out1p		Data_Sta0_Wedge3_Side1_Chip05_Out1n	$\overline{}$
$\rightarrow$	Data_Sta0_Wedge3_Side1_Chip05_Out0p	<del>1</del> 8733 	Data_Sta0_Wedge3_Side1_Chip05_Out0n	$\overline{}$
	Data_Sta3_Wedge3_Side1_Chip13_Out1p	<del>100</del> 31 	Data_Sta3_Wedge3_Side1_Chip13_Out1n	
_	Data_Sta3_Wedge3_Side1_Chip13_Out0p	100 129	Data_Sta3_Wedge3_Side1_Chip13_Out0n	
$\supset$	Data_Sta3_Wedge3_Side1_Chip12_Out1p	1000 18725	Data_Sta3_Wedge3_Side1_Chip12_Out1n	$\overline{}$
$\supset$	Data_Sta3_Wedge3_Side1_Chip12_Out0p		Data_Sta3_Wedge3_Side1_Chip12_Out0n	$\overline{}$
$\supset$	Data_Sta3_Wedge3_Side1_Chip11_Out1p	<del>10/2</del> 3 	Data_Sta3_Wedge3_Side1_Chip11_Out1n	$\overline{}$
	Data_Sta3_Wedge3_Side1_Chip11_Out0p	1 <del>0/2</del> 1	Data_Sta3_Wedge3_Side1_Chip11_Out0n	
_	Data_Sta3_Wedge3_Side1_Chip10_Out1p	1 <del>0</del> 00	Data_Sta3_Wedge3_Side1_Chip10_Out1n	
$\supset$	Data_Sta3_Wedge3_Side1_Chip10_Out0p	100 100 100 100 100 100	Data_Sta3_Wedge3_Side1_Chip10_Out0n	$\overline{}$
$\supset$	Data_Sta3_Wedge3_Side1_Chip09_Out1p		Data_Sta3_Wedge3_Side1_Chip09_Out1n	$\overline{}$
<u> </u>	Data_Sta0_Wedge3_Side1_Chip04_Out1p	<del>100</del> 1 	Data_Sta0_Wedge3_Side1_Chip04_Out1n	
_	Data_Sta0_Wedge3_Side1_Chip04_Out0p	<del>10</del> 209	Data_Sta0_Wedge3_Side1_Chip04_Out0n	_
	Data_Sta0_Wedge3_Side1_Chip03_Out1p			
_>_		1005	Data_Sta0_Wedge3_Side1_Chip03_Out1n	
$\supset$	Data_Sta0_Wedge3_Side1_Chip03_Out0p		Data_Sta0_Wedge3_Side1_Chip03_Out0n	$\overline{}$
$\supset$	Data_Sta0_Wedge3_Side1_Chip02_Out1p	<del>10/0</del> 3	Data_Sta0_Wedge3_Side1_Chip02_Out1n	
	Data_Sta0_Wedge3_Side1_Chip02_Out0p	<del>100</del> 1 	Data_Sta0_Wedge3_Side1_Chip02_Out0n	
	Data_Sta0_Wedge3_Side1_Chip01_Out1p	<del>1</del> 2699	Data_Sta0_Wedge3_Side1_Chip01_Out1n	_
				$\overline{}$
$\supset$	Data_Sta0_Wedge3_Side1_Chip01_Out0p		Data_Sta0_Wedge3_Side1_Chip01_Out0n	$\overline{}$
$\supset$	Data_Sta0_Wedge3_Side0_Chip05_Out1p		Data_Sta0_Wedge3_Side0_Chip05_Out1n	$\overline{}$
<u> </u>	Data_Sta0_Wedge3_Side0_Chip05_Out0p	<del>12</del> 693	Data_Sta0_Wedge3_Side0_Chip05_Out0n	
$\vec{}$	Data_Sta0_Wedge3_Side0_Chip04_Out1p	1 <del>1</del> 2691	Data_Sta0_Wedge3_Side0_Chip04_Out1n	_
$\supset$ —	Data_Sta0_Wedge3_Side0_Chip04_Out0p		Data_Sta0_Wedge3_Side0_Chip04_Out0n	$\overline{}$

		R647	
$\supset$	Data_Sta0_Wedge3_Side0_Chip03_Out1p		Data_Sta0_Wedge3_Side0_Chip03_Out1n
	Data_Sta0_Wedge3_Side0_Chip03_Out0p	<del>1</del> <del>2</del> 645	Data_Sta0_Wedge3_Side0_Chip03_Out0n
>	Data_State_evedges_States_Chipos_States		Data_Stab_Wedges_Stoes_Critpos_Cutori
_	Data_Sta0_Wedge3_Side0_Chip02_Out1p	100 100 100 100 100 100 100 100 100 100	Data_Sta0_Wedge3_Side0_Chip02_Out1n
	Buil_Glad_Wedged_Glace_Glipoz_Gat.ip		Bala_clab_medges_closes_cmpsz_clatin
_	Data_Sta0_Wedge3_Side0_Chip02_Out0p	<del>1</del> <del>0</del> 730	Data_Sta0_Wedge3_Side0_Chip02_Out0n
		100 17732	
_	Data_Sta0_Wedge3_Side0_Chip01_Out1p	<del>1</del> R732	Data_Sta0_Wedge3_Side0_Chip01_Out1n
		1003 10734	
	Data_Sta0_Wedge3_Side0_Chip01_Out0p	<del>1</del> 8734 \/\/	Data_Sta0_Wedge3_Side0_Chip01_Out0n
		1000 10736	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip12_Out0p		Data_Sta3_Wedge3_Side0_Chip12_Out0n
		1 <del>0</del> 738	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip11_Out1p		Data_Sta3_Wedge3_Side0_Chip11_Out1n
		1000	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip11_Out0p	——VV——	Data_Sta3_Wedge3_Side0_Chip11_Out0n
		1000	_
$\rightarrow$	Data_Sta3_Wedge3_Side0_Chip10_Out1p		Data_Sta3_Wedge3_Side0_Chip10_Out1n
		100	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip10_Out0p	—— <del>~</del>	Data_Sta3_Wedge3_Side0_Chip10_Out0n
		<del>1</del> 2746	_
$\rightarrow$	Data_Sta3_Wedge3_Side0_Chip09_Out1p	<del></del>	Data_Sta3_Wedge3_Side0_Chip09_Out1n
		100 18748	_
$\rightarrow$ —	Data_Sta3_Wedge3_Side0_Chip09_Out0p		Data_Sta3_Wedge3_Side0_Chip09_Out0n
		<del>107</del> 50	
$\supset$ —	Data_Sta3_Wedge3_Side0_Chip08_Out1p		Data_Sta3_Wedge3_Side0_Chip08_Out1n
		<del>107</del> 52	
_>—	Data_Sta3_Wedge3_Side0_Chip08_Out0p		Data_Sta3_Wedge3_Side0_Chip08_Out0n
	Data_Sta3_Wedge3_Side0_Chip07_Out1p	<del>1275</del> 4	Data_Sta3_Wedge3_Side0_Chip07_Out1n
_>—	Data_stas_vveoges_stdeo_criipo/_cdt ip		Data_Stas_wedges_stded_Critp07_Out III
_	Data_Sta3_Wedge3_Side0_Chip07_Out0p	1 <del>0</del> 756	Data_Sta3_Wedge3_Side0_Chip07_Out0n
>	Buta_otab_vvedgeo_otaco_otapov_otatop		Bala_clab_rreages_class_crispor_cateri
	Data_Sta3_Wedge3_Side0_Chip06_Out1p	100 R758	Data_Sta3_Wedge3_Side0_Chip06_Out1n
		100	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip06_Out0p	10760	Data_Sta3_Wedge3_Side0_Chip06_Out0n
		10062	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip05_Out1p	1 <del>1</del> 7762	Data_Sta3_Wedge3_Side0_Chip05_Out1n
		1000 1000 1000	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip05_Out0p		Data_Sta3_Wedge3_Side0_Chip05_Out0n
		1000 1R766	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip04_Out1p	——VVV——	Data_Sta3_Wedge3_Side0_Chip04_Out1n
		1000 1000 1000	
$\rightarrow$	Data_Sta3_Wedge3_Side0_Chip04_Out0p	——VVV——	Data_Sta3_Wedge3_Side0_Chip04_Out0n
		<del>1</del> 8780	
$\rightarrow$	Data_Sta3_Wedge3_Side0_Chip03_Out1p	—— <del>/</del> ///	Data_Sta3_Wedge3_Side0_Chip03_Out1n
		<del>1000</del> 0	
$\rightarrow$	Data_Sta3_Wedge3_Side0_Chip03_Out0p	—— <b>V</b> V——	Data_Sta3_Wedge3_Side0_Chip03_Out0n
		10072	
$\overline{}$	Data_Sta3_Wedge3_Side0_Chip02_Out1p		Data_Sta3_Wedge3_Side0_Chip02_Out1n
		<del>1,07</del> 74	
$\supset$	Data_Sta3_Wedge3_Side0_Chip02_Out0p		Data_Sta3_Wedge3_Side0_Chip02_Out0n
_	Data disa Martina dia Angela Tiri	100 R776	
$\rightarrow$	Data_Sta3_Wedge3_Side0_Chip01_Out1p		Data_Sta3_Wedge3_Side0_Chip01_Out1n
_	Data Stra Wadana Sid-o Chi-od Cuts	<del>107</del> 78	Data Sta2 Wadan2 Sida0 Chin04 Cudo
>	Data_Sta3_Wedge3_Side0_Chip01_Out0p		Data_Sta3_Wedge3_Side0_Chip01_Out0n
	CLK_FPGA_Dp	<del>1</del> 9931	CLK_FPGA_Dn
<b>&gt;</b> ——	051_11 G1_5p	100	0.00,000,000

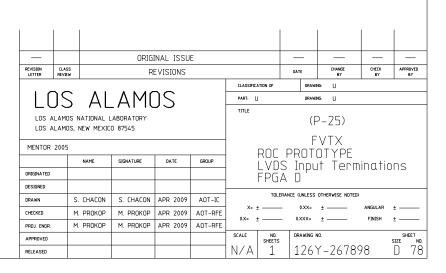
		R687		
$\supset$ —	Data_Sta3_Wedge3_Side1_Chip09_Out0p		Data_Sta3_Wedge3_Side1_Chip09_Out0n	$\overline{}$
_	Data Sta3 Wedge3 Side1 Chip08 Out1p	<del>1</del> <del>1</del> 8685	Data Sta3 Wedge3 Side1 Chip08 Out1n	_
				$\overline{}$
<u> </u>	Data_Sta3_Wedge3_Side1_Chip08_Out0p		Data_Sta3_Wedge3_Side1_Chip08_Out0n	
		1 <del>0</del> 081		
$\supset$ —	Data_Sta3_Wedge3_Side1_Chip07_Out1p		Data_Sta3_Wedge3_Side1_Chip07_Out1n	$\overline{}$
_	Data_Sta3_Wedge3_Side1_Chip07_Out0p	<del>1</del> <del>1</del> <del>00</del> 79	Data_Sta3_Wedge3_Side1_Chip07_Out0n	_
$\rightarrow$	Bata_ctab_Weegeo_ctab _ctripor_ctab		Bata_Gab_Wedges_Gae1_Gripps1_Gatori	$\overline{}$
<u> </u>	Data_Sta3_Wedge3_Side1_Chip06_Out1p	18677 	Data_Sta3_Wedge3_Side1_Chip06_Out1n	
		10075		_
$\supset$ —	Data_Sta3_Wedge3_Side1_Chip06_Out0p		Data_Sta3_Wedge3_Side1_Chip06_Out0n	$\overline{}$
_	Data_Sta3_Wedge3_Side1_Chip05_Out1p	<del>100</del> 73	Data_Sta3_Wedge3_Side1_Chip05_Out1n	_
$\rightarrow$	Bata_otab_Weegeo_otae1_ottlpbo_out1p		Bata_Gab_Wedges_Gae1_Grapos_Gae111	$\overline{}$
<u> </u>	Data_Sta3_Wedge3_Side1_Chip05_Out0p	18671 	Data_Sta3_Wedge3_Side1_Chip05_Out0n	
		18669		_
$\supset$ —	Data_Sta3_Wedge3_Side1_Chip04_Out1p		Data_Sta3_Wedge3_Side1_Chip04_Out1n	$\overline{}$
_	Data Sta3 Wedge3 Side1 Chip04 Out0p	<del>1</del> <del>1</del> 8667	Data Sta3 Wedge3 Side1 Chip04 Out0n	
$\rightarrow$	Bata_ctab_Weegeo_ctab _ctabpot_ctabp		Bata_Gab_Wedgeo_Gae1_Grippo4_Gatori	$\overline{}$
<u> </u>	Data_Sta3_Wedge3_Side1_Chip03_Out1p	<b>1</b> ₹665	Data_Sta3_Wedge3_Side1_Chip03_Out1n	
		1 <del>1</del> 0663		_
$\supset$ —	Data_Sta3_Wedge3_Side1_Chip03_Out0p		Data_Sta3_Wedge3_Side1_Chip03_Out0n	${-}\!$
_	Data Sta3 Wedge3 Side1 Chip02 Out1p	<del>1</del> <del>2</del> 661	Data Sta3 Wedge3 Side1 Chip02 Out1n	_
				$\overline{}$
<u> </u>	Data_Sta3_Wedge3_Side1_Chip02_Out0p		Data_Sta3_Wedge3_Side1_Chip02_Out0n	
_		10057		_
$\supset$ —	Data_Sta3_Wedge3_Side1_Chip01_Out1p		Data_Sta3_Wedge3_Side1_Chip01_Out1n	$-\!\!\!\!-$
$\overline{}$	Data Sta3 Wedge3 Side1 Chip01 Out0p	<del>1</del> <del>2</del> 655	Data_Sta3_Wedge3_Side1_Chip01_Out0n	_
				$\overline{}$
<u> </u>	Data_Sta3_Wedge3_Side0_Chip13_Out1p		Data_Sta3_Wedge3_Side0_Chip13_Out1n	$\overline{}$
_		1 <del>1</del> 0051		_
$\supset$ —	Data_Sta3_Wedge3_Side0_Chip13_Out0p		Data_Sta3_Wedge3_Side0_Chip13_Out0n	$-\!$
$\overline{}$	Data Sta3 Wedge3 Side0 Chip12 Out1p	<del>1</del> 2649	Data Sta3 Wedge3 Side0 Chip12 Out1n	_
$\rightarrow$		100		$-\!$

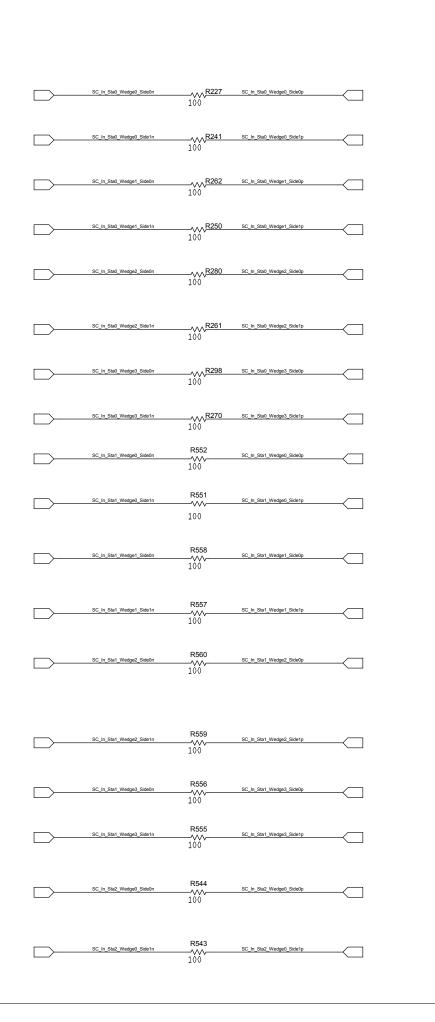
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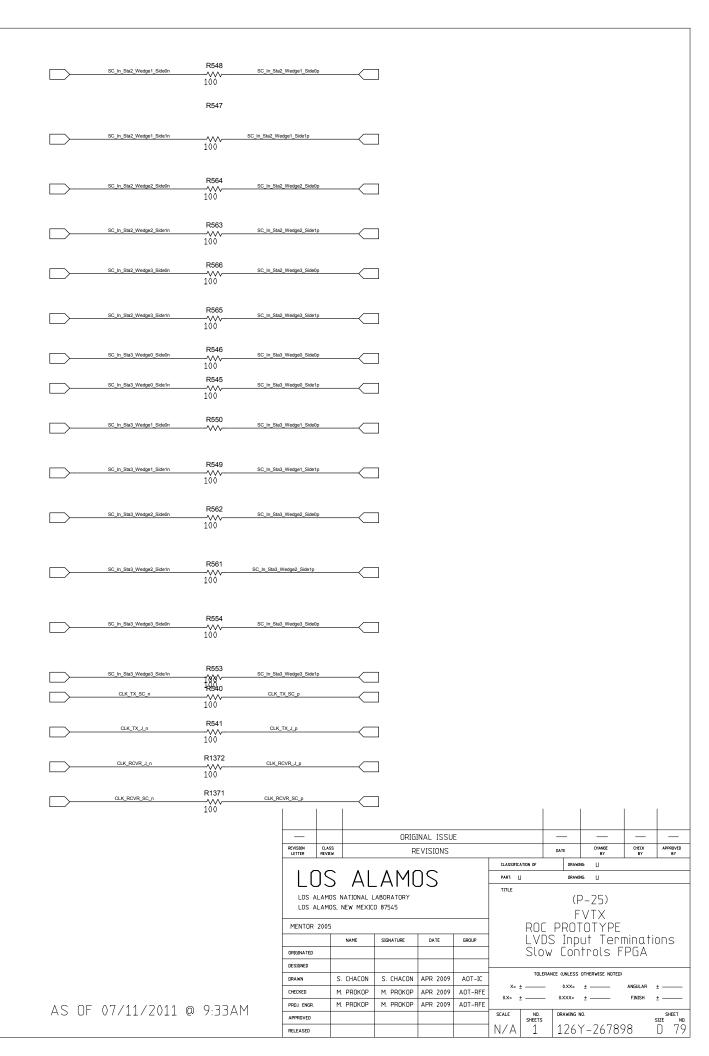
Data	Sta1_Wedge3_Side1_Chip13_Out1p	R782	Data_Sta1_Wedge3_Side1_Chip13_Out1n		
_310_		100.		$\overline{}$	
Data	Sta1_Wedge3_Side1_Chip13_Out0p	10784	Data_Sta1_Wedge3_Side1_Chip13_Out0n		
Data_	Sta1_Wedge3_Side1_Chip12_Out1p	<del>-1</del> R786 	Data_Sta1_Wedge3_Side1_Chip12_Out1n		
		10788			
Data_	Sta1_Wedge3_Side1_Chip12_Out0p	<del>1</del> 8788	Data_Sta1_Wedge3_Side1_Chip12_Out0n		
		10790			
Data_	Sta1_Wedge3_Side1_Chip11_Out1p	18790 	Data_Sta1_Wedge3_Side1_Chip11_Out1n		
		1000 18792			
Data_	Sta1_Wedge3_Side1_Chip11_Out0p		Data_Sta1_Wedge3_Side1_Chip11_Out0n		
		10794			
Data_	Sta1_Wedge3_Side1_Chip10_Out1p		Data_Sta1_Wedge3_Side1_Chip10_Out1n		
		10796			
Data_	Sta1_Wedge3_Side1_Chip10_Out0p		Data_Sta1_Wedge3_Side1_Chip10_Out0n		
		<del>1879</del> 8			
Data_	Sta1_Wedge3_Side1_Chip09_Out1p		Data_Sta1_Wedge3_Side1_Chip09_Out1n		
		18797			
Data_	Sta1_Wedge3_Side1_Chip09_Out0p	—	Data_Sta1_Wedge3_Side1_Chip09_Out0n		
		1 <del>0</del> 005		$\overline{}$	
Data_	Sta1_Wedge3_Side1_Chip08_Out1p	——-W	Data_Sta1_Wedge3_Side1_Chip08_Out1n		
		<del>100</del> 3			
Data_	Sta1_Wedge3_Side1_Chip08_Out0p		Data_Sta1_Wedge3_Side1_Chip08_Out0n	$\overline{}$	
		<del>107</del> 91			
Data_	Sta1_Wedge3_Side1_Chip07_Out1p		Data_Sta1_Wedge3_Side1_Chip07_Out1n	$\overline{}$	
		<del>1078</del> 9			
Data_	Sta1_Wedge3_Side1_Chip07_Out0p		Data_Sta1_Wedge3_Side1_Chip07_Out0n	$\overline{}$	
		<del>1</del> 8787			
Data_	Sta1_Wedge3_Side1_Chip06_Out1p		Data_Sta1_Wedge3_Side1_Chip06_Out1n	$\overline{}$	
		10785			
Data_	Sta1_Wedge3_Side1_Chip06_Out0p		Data_Sta1_Wedge3_Side1_Chip06_Out0n	$\overline{}$	
		<del>1,078</del> 3			
Data_	Sta1_Wedge3_Side1_Chip05_Out1p		Data_Sta1_Wedge3_Side1_Chip05_Out1n		
		<del>1,0/8</del> 1		_	
Data_	Sta1_Wedge3_Side1_Chip05_Out0p	<del></del> ^^^	Data_Sta1_Wedge3_Side1_Chip05_Out0n	$\overline{}$	
Dete	Sta1_Wedge3_Side1_Chip04_Out1p	<del>107</del> 9	Data_Sta1_Wedge3_Side1_Chip04_Out1n		
Data_	Stall_wedges_Sidell_Criipo+_Out ip	<u> </u>	Data_Sta1_vveuges_Side1_Criip04_Out111	$\overline{}$	
Data	Sta1_Wedge3_Side1_Chip04_Out0p	<del>10/7</del> 7	Data_Sta1_Wedge3_Side1_Chip04_Out0n		
Duta_	ota1_Weageo_olde1_olispo4_outop		Data_ota1_vvedgeo_otac1_otilpov_outor	$\overline{}$	
Data	Sta1_Wedge3_Side1_Chip03_Out1p	<del>1077</del> 5	Data_Sta1_Wedge3_Side1_Chip03_Out1n		
Data	Sta1_Wedge3_Side1_Chip03_Out0p	<del>1077</del> 3	Data_Sta1_Wedge3_Side1_Chip03_Out0n		
_310_		100		$\overline{}$	
Data	Sta1_Wedge3_Side1_Chip02_Out1p	<del>1077</del> 1	Data_Sta1_Wedge3_Side1_Chip02_Out1n		
	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	100.		$\overline{}$	
Data	Sta1_Wedge3_Side1_Chip02_Out0p	<del>1</del> 6759	Data_Sta1_Wedge3_Side1_Chip02_Out0n		
		1000			
Data	Sta1_Wedge3_Side1_Chip01_Out1p	<del>107</del> 69	Data_Sta1_Wedge3_Side1_Chip01_Out1n		
Data_	Sta1_Wedge3_Side1_Chip01_Out0p	#R767 	Data_Sta1_Wedge3_Side1_Chip01_Out0n		
		18765			
Data	Sta1_Wedge3_Side0_Chip13_Out1p	<del>1</del> 2765	Data_Sta1_Wedge3_Side0_Chip13_Out1n		
		10763			
Data	Sta1_Wedge3_Side0_Chip13_Out0p	<del>-1</del> R763 	Data_Sta1_Wedge3_Side0_Chip13_Out0n		
_		10761			
Data_	Sta1_Wedge3_Side0_Chip12_Out1p	#R761	Data_Sta1_Wedge3_Side0_Chip12_Out1n		
		1 <del>1</del> 2608		$\sim$	
	BCO_CLK_IN_Dp	<del>1</del> 8608	BCO_CLK_IN_Dn		
		v v v		\ I	

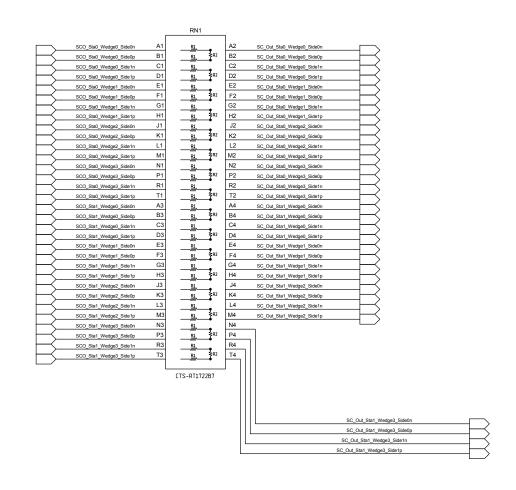
_	Data_Sta2_Wedge3_Side1_Chip01_Out1p	R624	Data_Sta2_Wedge3_Side1_Chip01_Out1n
		1 <del>2</del> 626	
$\supset$	Data_Sta2_Wedge3_Side1_Chip01_Out0p		Data_Sta2_Wedge3_Side1_Chip01_Out0n
	Data_Sta2_Wedge3_Side0_Chip13_Out1p	1 <del>2</del> 628	Data_Sta2_Wedge3_Side0_Chip13_Out1n
		1 <del>2</del> 630	
$\supset$ —	Data_Sta2_Wedge3_Side0_Chip13_Out0p	<del></del>	Data_Sta2_Wedge3_Side0_Chip13_Out0n
<u> </u>	Data_Sta2_Wedge3_Side0_Chip12_Out1p	100 18632 	Data_Sta2_Wedge3_Side0_Chip12_Out1n
		100 1 <del>1</del> 634	
$\rightarrow$	Data_Sta2_Wedge3_Side0_Chip12_Out0p	100	Data_Sta2_Wedge3_Side0_Chip12_Out0n
<u> </u>	Data_Sta2_Wedge3_Side0_Chip11_Out1p	1 <del>2</del> 636 	Data_Sta2_Wedge3_Side0_Chip11_Out1n
		<del>1</del> 8638	
$\rightarrow$	Data_Sta2_Wedge3_Side0_Chip11_Out0p		Data_Sta2_Wedge3_Side0_Chip11_Out0n
	Data_Sta2_Wedge3_Side0_Chip10_Out1p	<del>1</del> R640 	Data_Sta2_Wedge3_Side0_Chip10_Out1n
_	D	12642	
_>_	Data_Sta2_Wedge3_Side0_Chip10_Out0p	100	Data_Sta2_Wedge3_Side0_Chip10_Out0n
<u> </u>	Data_Sta2_Wedge3_Side0_Chip09_Out1p	1 <del>2</del> 644 	Data_Sta2_Wedge3_Side0_Chip09_Out1n
	Date Olea Western Olivin Olivino Oliv	1 <del>2</del> 646	Date Shed Wednes Sides Street Street
_>_	Data_Sta2_Wedge3_Side0_Chip09_Out0p		Data_Sta2_Wedge3_Side0_Chip09_Out0n
$\rightarrow$	Data_Sta2_Wedge3_Side0_Chip08_Out1p	#R648 	Data_Sta2_Wedge3_Side0_Chip08_Out1n
	Data_Sta2_Wedge3_Side0_Chip08_Out0p	<del>1</del> <del>265</del> 0	Data_Sta2_Wedge3_Side0_Chip08_Out0n
_>	Data_Sta2_vveugeS_StuetO_Striptot_Outop		Data_Graz_Wedges_Graete_Criptot_Cutoff
$\rightarrow$	Data_Sta2_Wedge3_Side0_Chip07_Out1p	<del></del>	Data_Sta2_Wedge3_Side0_Chip07_Out1n
_	Data_Sta2_Wedge3_Side0_Chip07_Out0p	<del>1</del> 2654	Data_Sta2_Wedge3_Side0_Chip07_Out0n
_>_	bala_stat_vreages_state_striper_statep		Bud_out_reages_out_onipor_cutori
$\rightarrow$	Data_Sta2_Wedge3_Side0_Chip06_Out1p		Data_Sta2_Wedge3_Side0_Chip06_Out1n
_	Data_Sta2_Wedge3_Side0_Chip06_Out0p	1 <del>R</del> 658	Data_Sta2_Wedge3_Side0_Chip06_Out0n
$\rightarrow$	Data_Sta2_Wedge3_Side0_Chip05_Out1p	<del></del>	Data_Sta2_Wedge3_Side0_Chip05_Out1n
_	Data_Sta2_Wedge3_Side0_Chip05_Out0p	12662 	Data_Sta2_Wedge3_Side0_Chip05_Out0n
		18664	
$\supset$	Data_Sta2_Wedge3_Side0_Chip04_Out1p		Data_Sta2_Wedge3_Side0_Chip04_Out1n
	Data_Sta2_Wedge3_Side0_Chip04_Out0p	1 <del>1</del> 0666	Data_Sta2_Wedge3_Side0_Chip04_Out0n
		1 <del>R</del> 668	
$\supset$	Data_Sta2_Wedge3_Side0_Chip03_Out1p	100	Data_Sta2_Wedge3_Side0_Chip03_Out1n
	Data_Sta2_Wedge3_Side0_Chip03_Out0p	12686 	Data_Sta2_Wedge3_Side0_Chip03_Out0n
	D	<del>1</del> 2670	
_>_	Data_Sta2_Wedge3_Side0_Chip02_Out1p		Data_Sta2_Wedge3_Side0_Chip02_Out1n
<u> </u>	Data_Sta2_Wedge3_Side0_Chip02_Out0p		Data_Sta2_Wedge3_Side0_Chip02_Out0n
	Data Sta2 Wadan2 Sidn0 Chin01 C-44-	18674	Data Str3 Wordan2 Sides Chin01 Out1n
_>_	Data_Sta2_Wedge3_Side0_Chip01_Out1p		Data_Sta2_Wedge3_Side0_Chip01_Out1n
$\rightarrow$	Data_Sta2_Wedge3_Side0_Chip01_Out0p	<b></b>	Data_Sta2_Wedge3_Side0_Chip01_Out0n
_	Data_Sta2_Wedge3_Side1_Chip13_Out1p	<del>1</del> 2678	Data_Sta2_Wedge3_Side1_Chip13_Out1n
	out_reages_outompro_outrp		
$\rightarrow$	Data_Sta2_Wedge3_Side1_Chip13_Out0p		Data_Sta2_Wedge3_Side1_Chip13_Out0n
_	Data_Sta2_Wedge3_Side1_Chip12_Out1p	1000 1000 1000 1000 1000 1000 1000 100	Data_Sta2_Wedge3_Side1_Chip12_Out1n
$\rightarrow$	Data_Sta2_Wedge3_Side1_Chip12_Out0p		Data_Sta2_Wedge3_Side1_Chip12_Out0n
_		100	_

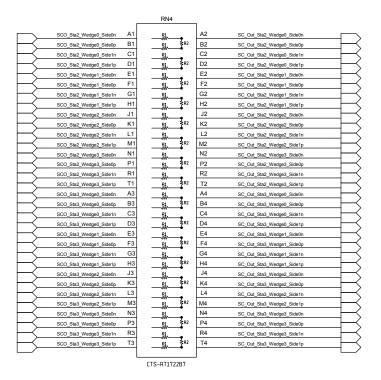
		Door	
$\longrightarrow$	Data_Sta2_Wedge3_Side1_Chip11_Out1p	R688 	Data_Sta2_Wedge3_Side1_Chip11_Out1n
		<del>1</del> 8690	
	Data_Sta2_Wedge3_Side1_Chip11_Out0p		Data_Sta2_Wedge3_Side1_Chip11_Out0n
	Date Class Wardens Sided Chiefs Code	<del>1</del> R692	Date Cto2 Mades2 Sided Chieft Outle
	Data_Sta2_Wedge3_Side1_Chip10_Out1p		Data_Sta2_Wedge3_Side1_Chip10_Out1n
	Data Sta2 Wedge3 Side1 Chip10 Out0p	1 <del>R</del> 694	Data Sta2 Wedge3 Side1 Chip10 Out0n
		1000 10096	
	Data_Sta2_Wedge3_Side1_Chip09_Out1p	~~~^^^~~	Data_Sta2_Wedge3_Side1_Chip09_Out1n
		<del>1</del> 0098	
$\longrightarrow$	Data_Sta2_Wedge3_Side1_Chip09_Out0p		Data_Sta2_Wedge3_Side1_Chip09_Out0n
_		<del>12/0</del> 0	
	Data_Sta2_Wedge3_Side1_Chip08_Out1p		Data_Sta2_Wedge3_Side1_Chip08_Out1n
	Data_Sta2_Wedge3_Side1_Chip08_Out0p	1 <del>07</del> 02	Data_Sta2_Wedge3_Side1_Chip08_Out0n
		10004	
	Data_Sta2_Wedge3_Side1_Chip07_Out1p	<del>1</del> ₹704	Data_Sta2_Wedge3_Side1_Chip07_Out1n
		18706	
$\longrightarrow$	Data_Sta2_Wedge3_Side1_Chip07_Out0p	<del>-</del>	Data_Sta2_Wedge3_Side1_Chip07_Out0n
		<del>1,2/0</del> ,8	
$\bigcirc$	Data_Sta2_Wedge3_Side1_Chip06_Out1p	<del></del> ^^^	Data_Sta2_Wedge3_Side1_Chip06_Out1n
	Data Sta2 Wedge3 Side1 Chip06 Out0p	<del>127</del> 10	Data Sta2 Wedge3 Side1 Chip06 Out0n
	Sau-out-wedges-out-onipos-out-o	100	Data_State_Wedges_Good_Gonpos_Goton
	Data_Sta2_Wedge3_Side1_Chip05_Out1p	<del>1</del> R712	Data_Sta2_Wedge3_Side1_Chip05_Out1n
		100	
$\longrightarrow$	Data_Sta2_Wedge3_Side1_Chip05_Out0p		Data_Sta2_Wedge3_Side1_Chip05_Out0n
		<del>10/</del> 16	
	Data_Sta2_Wedge3_Side1_Chip04_Out1p		Data_Sta2_Wedge3_Side1_Chip04_Out1n
	Data Sta2 Wedge3 Side1 Chip04 Out0p	<del>127</del> 18	Data Sta2 Wedge3 Side1 Chip04 Out0n
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	Data_Sta2_Wedge3_Side1_Chip03_Out1p	#R720	Data_Sta2_Wedge3_Side1_Chip03_Out1n
		1000	
$\overline{}$	Data_Sta2_Wedge3_Side1_Chip03_Out0p		Data_Sta2_Wedge3_Side1_Chip03_Out0n
		100 18724	
$\Box$	Data_Sta2_Wedge3_Side1_Chip02_Out1p		Data_Sta2_Wedge3_Side1_Chip02_Out1n
	Data Sta? Wadan? Sida! Chin0? C-10-	<del>10/2</del> 6	Data Sta2 Wedge3 Side1 Chip02 Out0n
$\Box$	Data_Sta2_Wedge3_Side1_Chip02_Out0p	100	Data_Staz_wedges_stde1_Critpto2_Outon
		100	

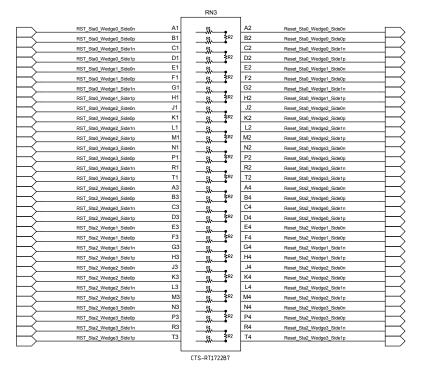


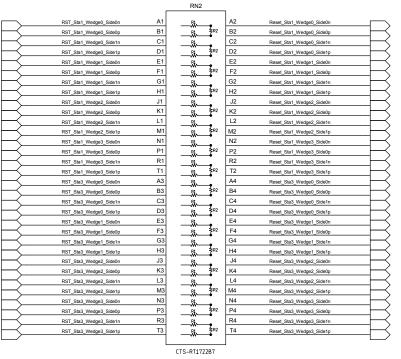












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SCALE

M. PROKOP M. PROKOP APR 2009 AOT-RFE

PROJ. ENGR.

APPROVED

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DRAWING NO.

