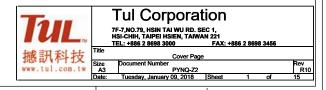
## COMPRESSED IMAGE **Page** Index **Cover Page** 01 **Revision History** 02 PMODs, Buttons, LEDs 03 RaspberryPi and Arduino I/O 04 05 **Ethernet and SD Card** 06 HDMI **USB & Audio** 07 80 **JTAG** 09 **FPGA Configuration, Flash** 10 **DDR and MIO Banks** 11 FPGA Banks, Anti Alias Filte s 12 **FPGA Power** 13 **DDR3 Memory** Power Regulation 14 15 **Power Regulation 2**

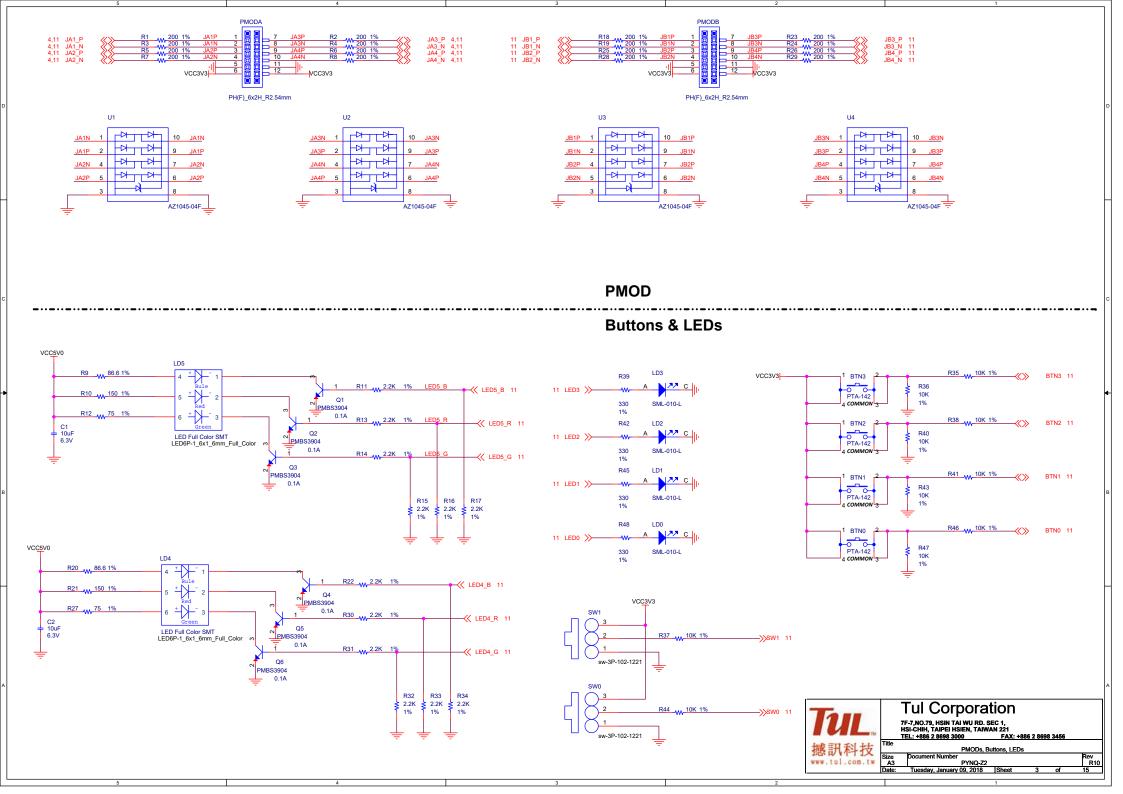
## COMPRESSED IMAGE

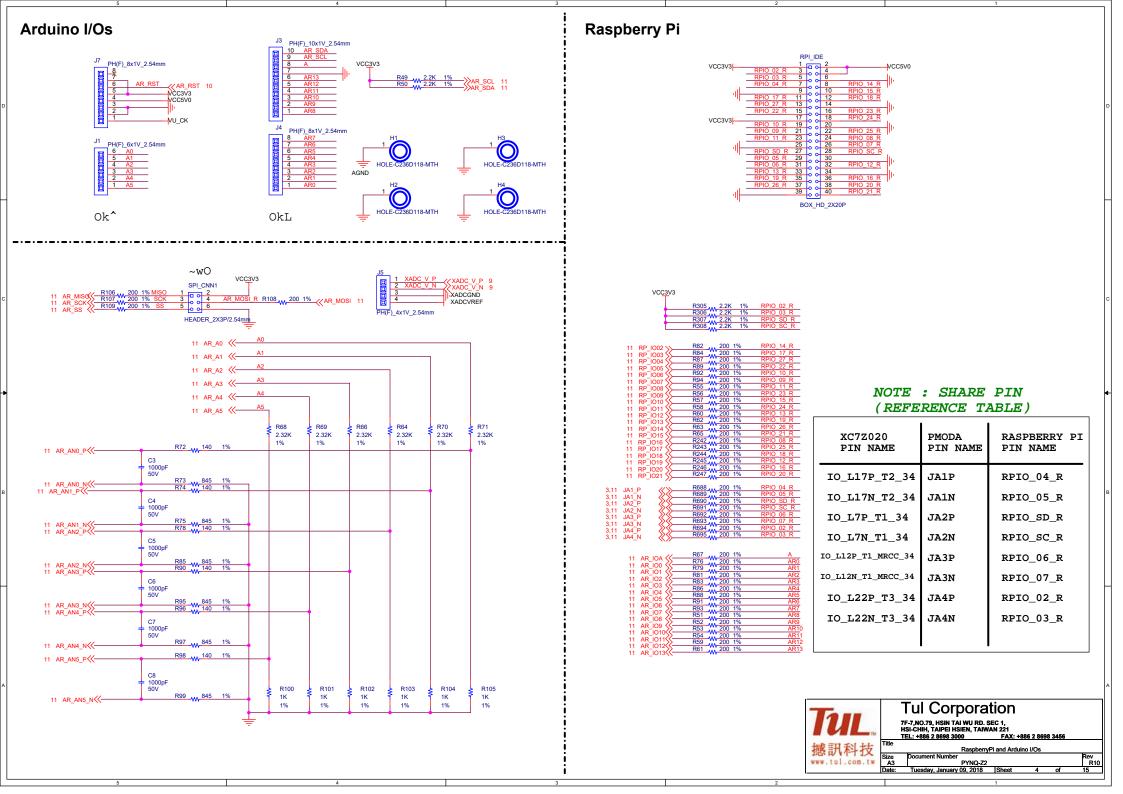


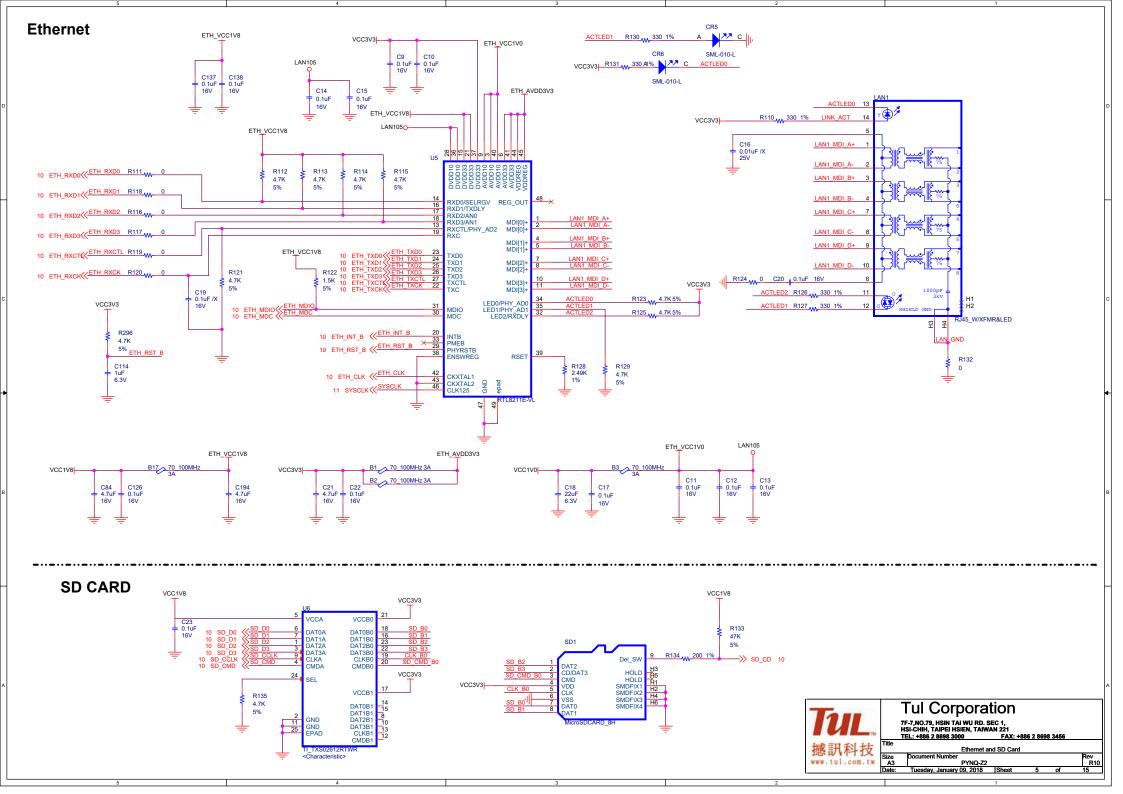
## **REVISION HISTORY:**

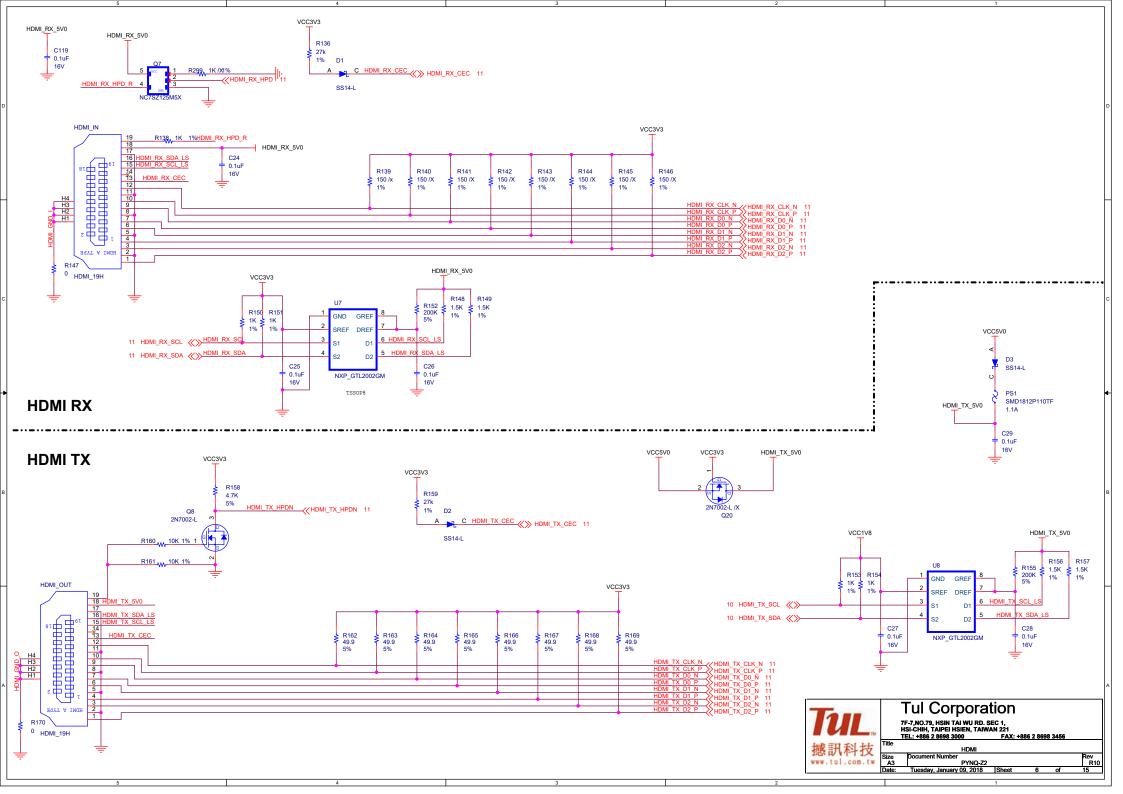
REV	DATE	NOTE
В	2017.09	Page 7: Change BTN1~4 pin definition
		Page 9: Change R296 pull up to +3.3V power rail.  ADD B3  DEL L1
		Page 10: Change Q7 to NC7SZ125M5X installed. Change Q8 to 2N7002 installed.
		Page 11: ADD C125 Change U9 PIN30 to PULL HI 1.8V Change U9 PIN29 to OTG_STP Change U11 PIN30 pill down Change U11 PIN18 pill down
		Page 12: Add pull up resistor for JTAG signal Change JTAG power name ADD R316~319 Change U12 PIN38,39 net name
		Page 14: Change R232 PIN1 net name
		Page 19: Modify JTAG POWER name for MAIN Power Select Change R292 to 100K Change C210 to 22uF/25V
		Page 8: JA4_P , JA4_N LINK TO RPI_IDE PIN3,5 RPI_IDE PIN12,16 SWAP PIN27,28
		Page 15: SOC PIN : V5 MODIFY NET NAME TO AU_RESET SOC PIN : U5 MODIFY NET NAME TO AU_MCLK_R
		Page 11: DEL C84,OSC2 ADD R312

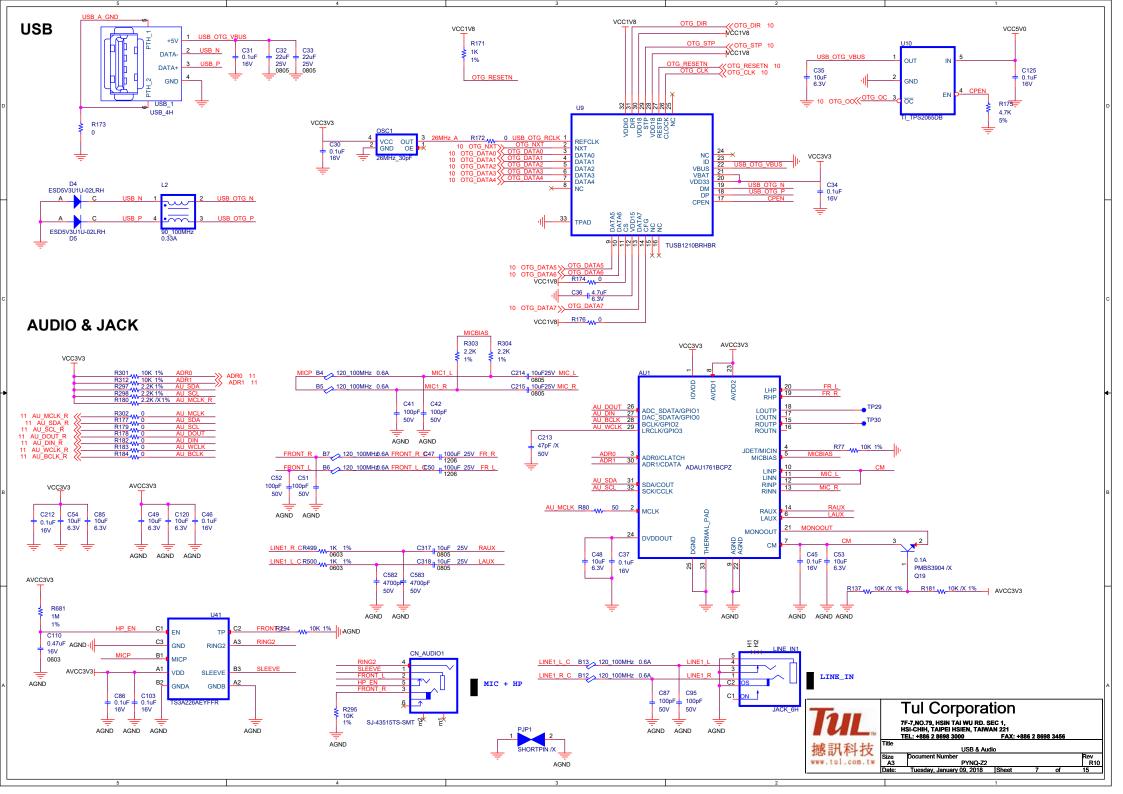


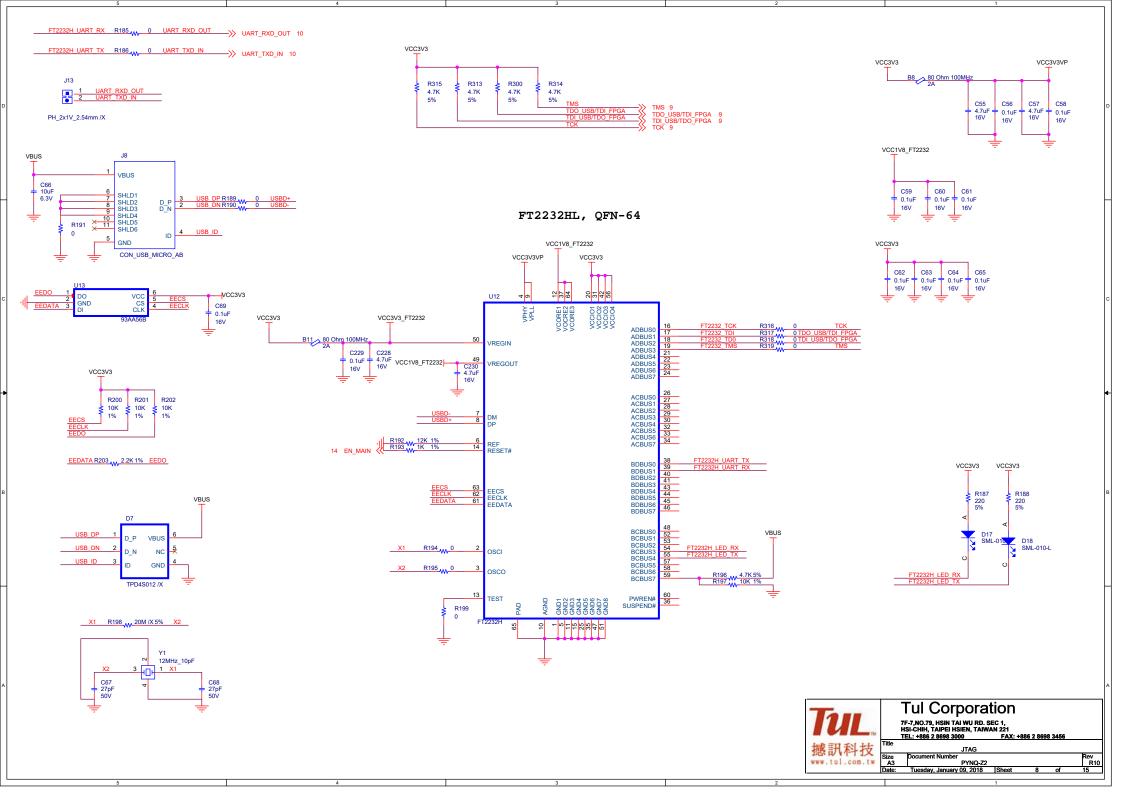


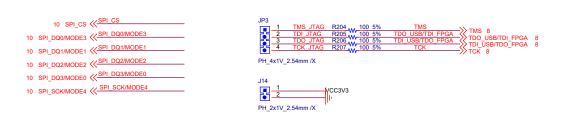


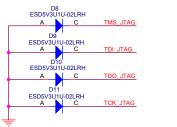


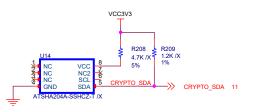




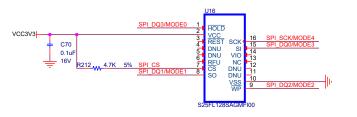


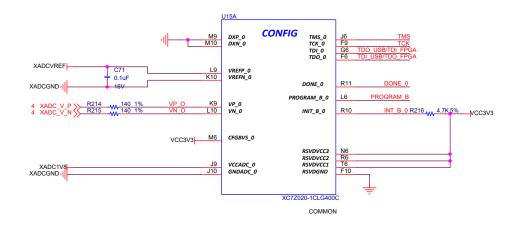


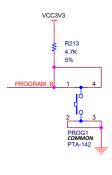


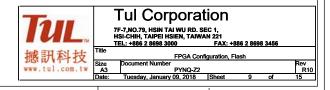


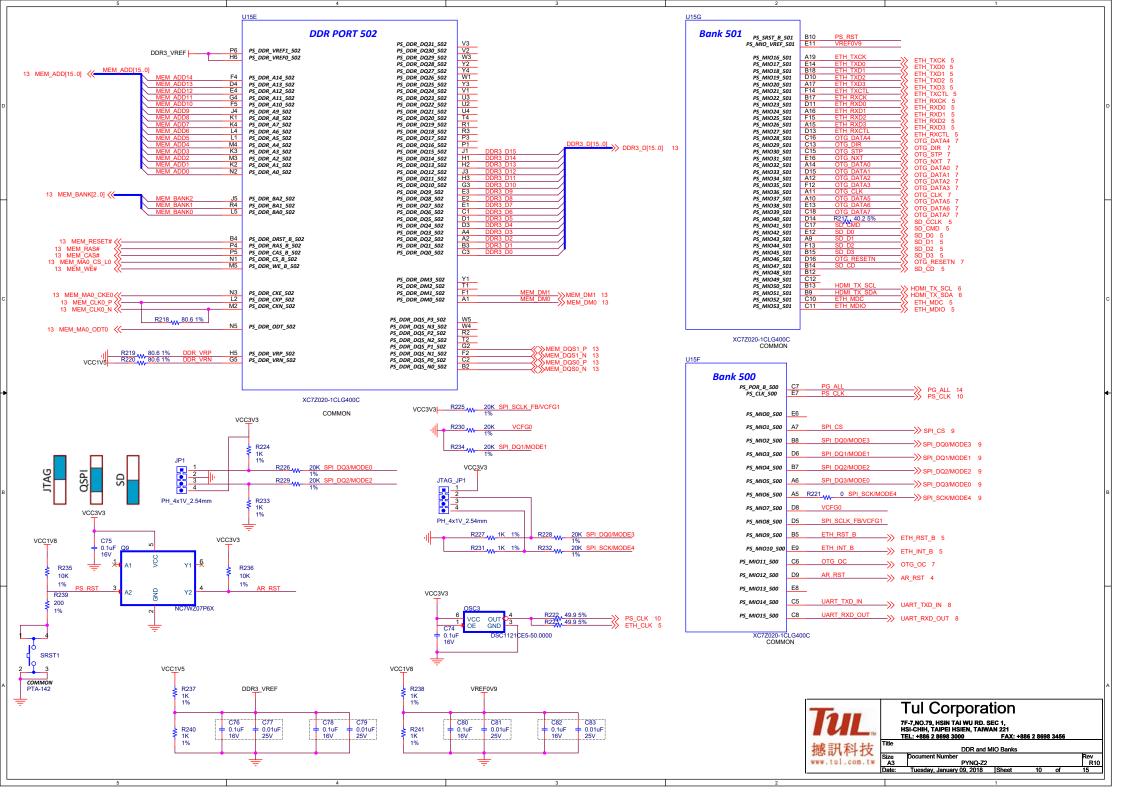


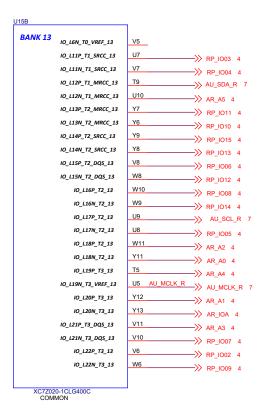


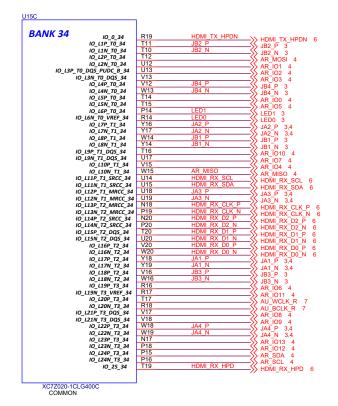


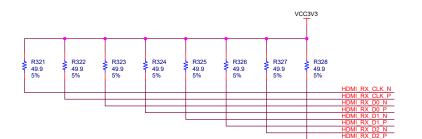


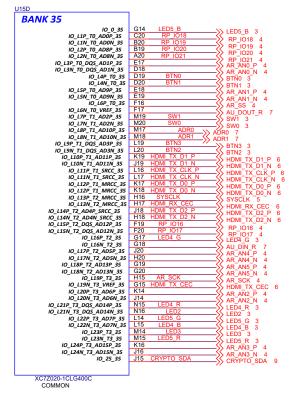




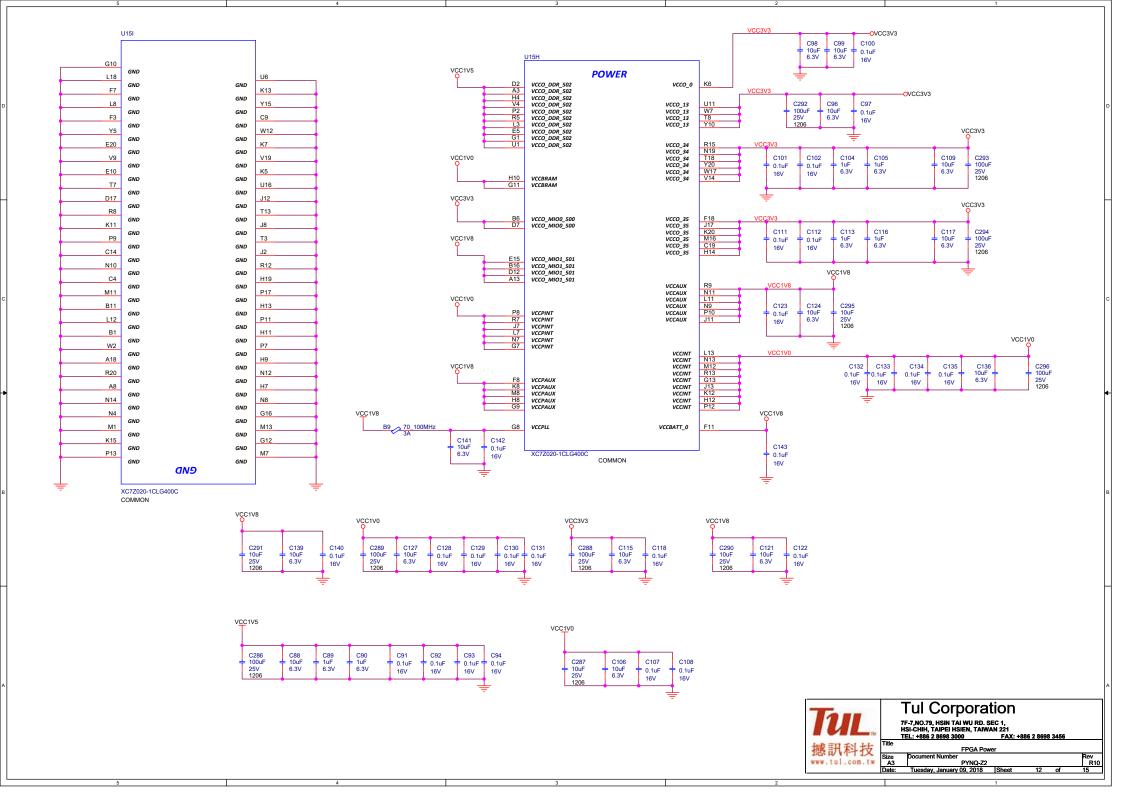


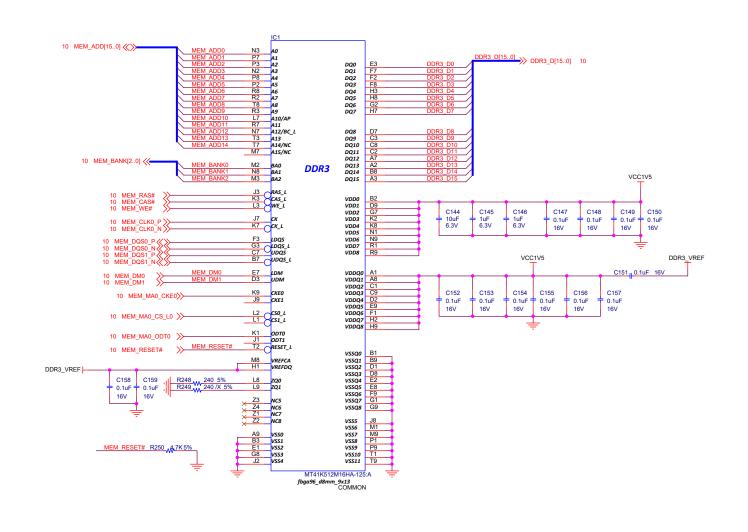














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DDR3 Memory

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