

## Lab 5 Critical Path

Select “Design Summary/Reports → Detailed Reports → Post-PAR Static Timing Report” and tell us the slack, source, and destination of the most critical path in the design.

Slack: -2.487 ns

Source: adau1761\_codec/i2c\_interface/Inst\_i2s\_data\_interface/new\_sample\_reg/C

Destination: music\_player/beat\_generator/counter/q\_reg[0]/CE