Lab 5 Timing Diagrams

					wave_display				
Clk	x	y	valid	next_addr	addr	read_value	curr_read_val	valid_pixel	{r,g,b}
0	x1	y1	1	Adr1	xxxxxxxxx	xxxxxxxxxx	xxxxxxxxxx	0	0
1	x1	y1	1	Adr1	Adr1	xxxxxxxxxx	xxxxxxxxxx	0	0
2	x2	y2	1	Adr2	Adr1	val1	xxxxxxxxxx	0	0
3	x2	y2	1	Adr2	Adr2	val1	val1	1	1
4	x2	y3	1	Adr2	Adr2	val2	val1	1	1
5	x3	y3	1	Adr3	Adr2	val2	val1	1	1
6	x3	y3	1	Adr3	Adr3	val2	val2	0	0
7	x3	y3	1	Adr3	Adr3	val3	val2	1	1
8	x3	invalid y4	1	Adr3	Adr3	val3	val2	0	0
9	x3	y4 valid	1	Adr3	Adr3	val3	val2	1	1
10	x4	y4 valid	1	Adr4	Adr3	val3	val2	1	1
11	x4	y4 valid	1	Adr4	Adr4	val3	val3	0	0
12	x4	y4 valid	1	Adr4	Adr4	val4	val3	1	1