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DOCUMENT
SCHEMATIC DIAGRAM 824-129511-0010

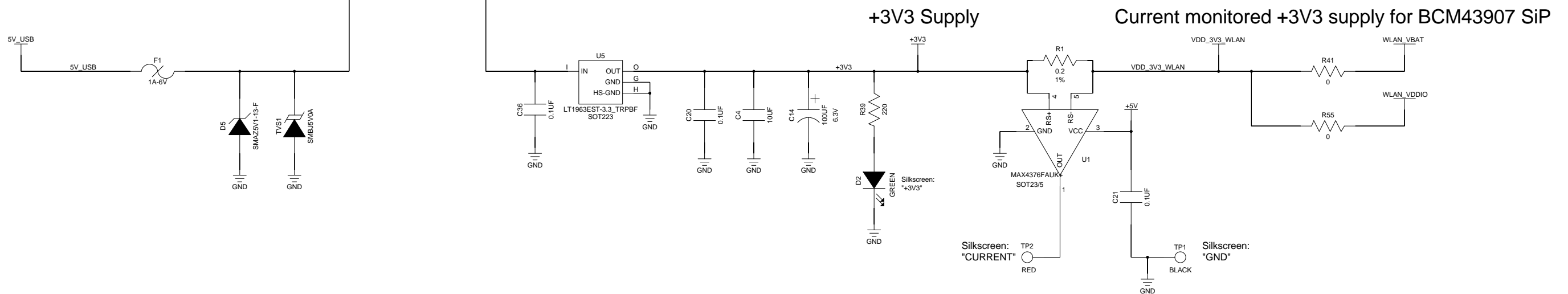
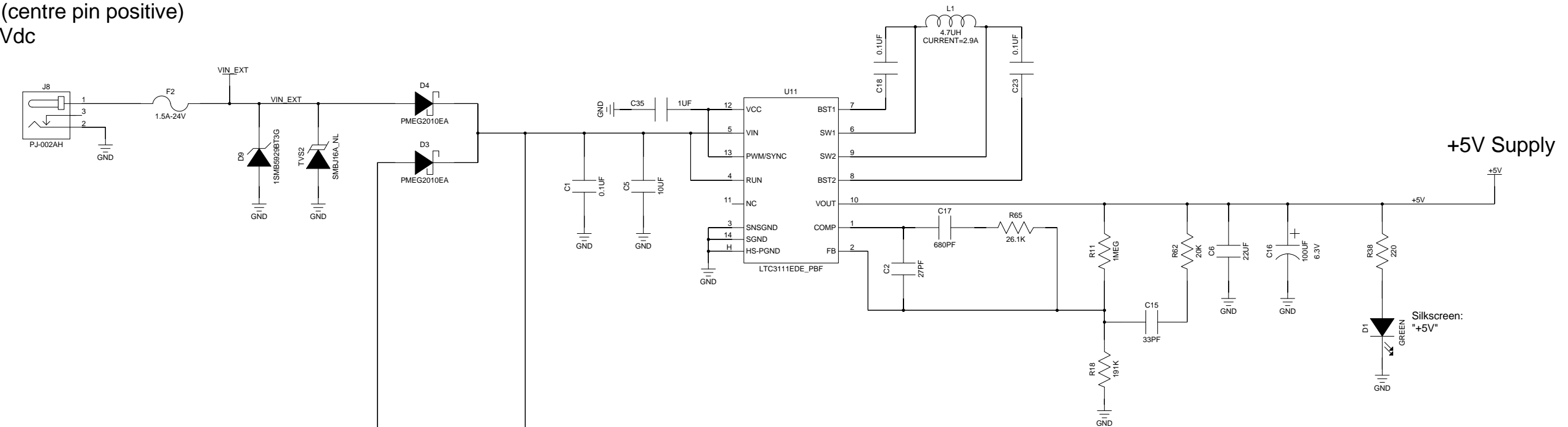
PRINTED CIRCUIT BOARD
200-129511-0010

SYSTEM BLOCK DIAGRAM

Approvals		<div>Broadcom</div> <div>Company Confidential</div>			
Designer	Date				
Reviewer	Date	SIZE B	TITLE BCM943907AEVAL1_1		
Reviewer	Date	Designer LEX		Drawing Number 824-129511-0010	Version 03
Reviewer	Date	DATE 28/10/2015:16:14			SHEET 1 OF 10

[illegible]

2.1mm DC Jack (centre pin positive)
Typ.= 9Vdc to 12Vdc
(Min.= 7Vdc)
(Max.= 15Vdc)



POWER

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SIZE B	TITLE BCM943907AEVAL1_1			
DATE	13/07/2015:15:07	SHEET	3	OF 10

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B

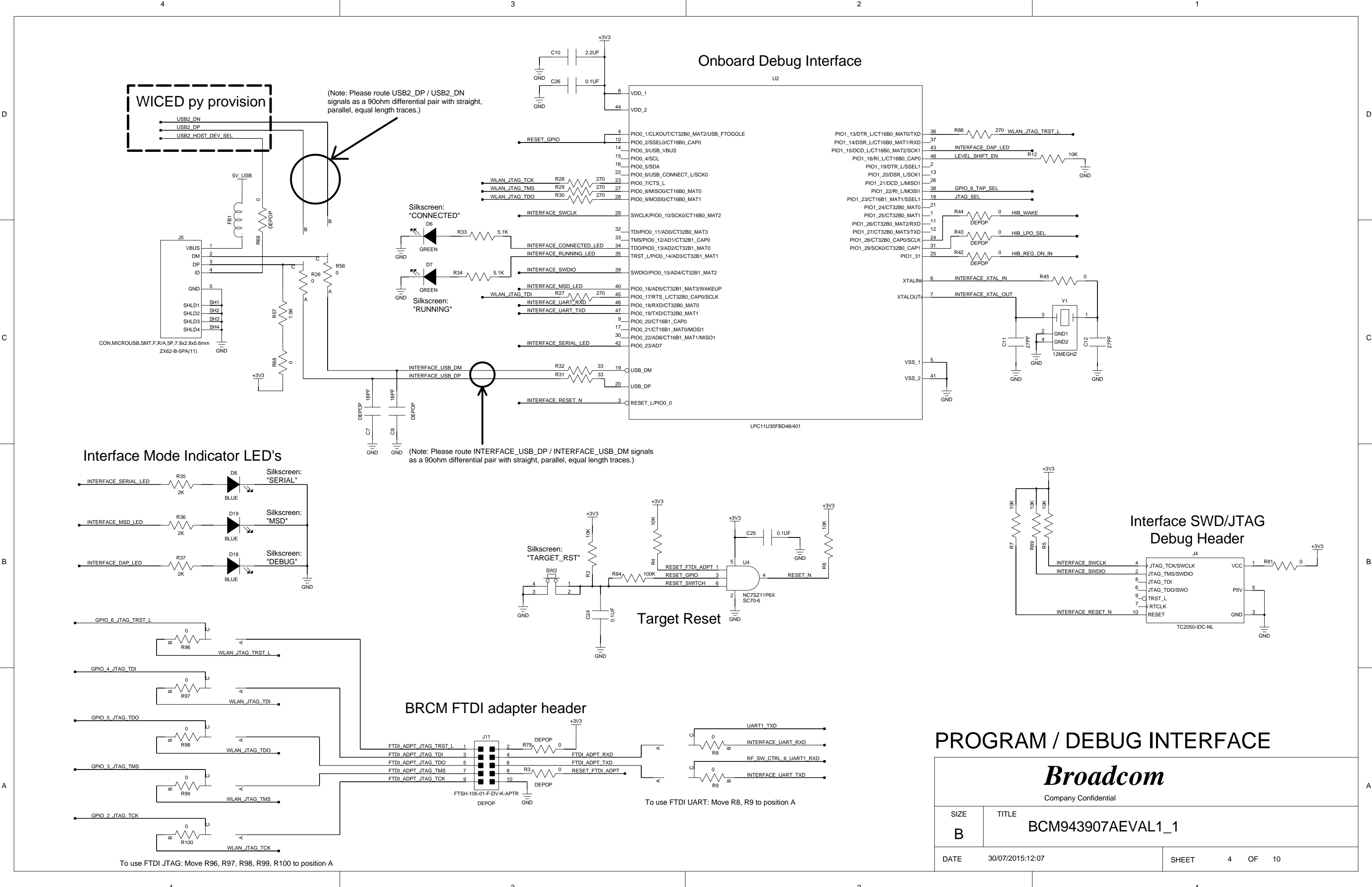
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D

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B

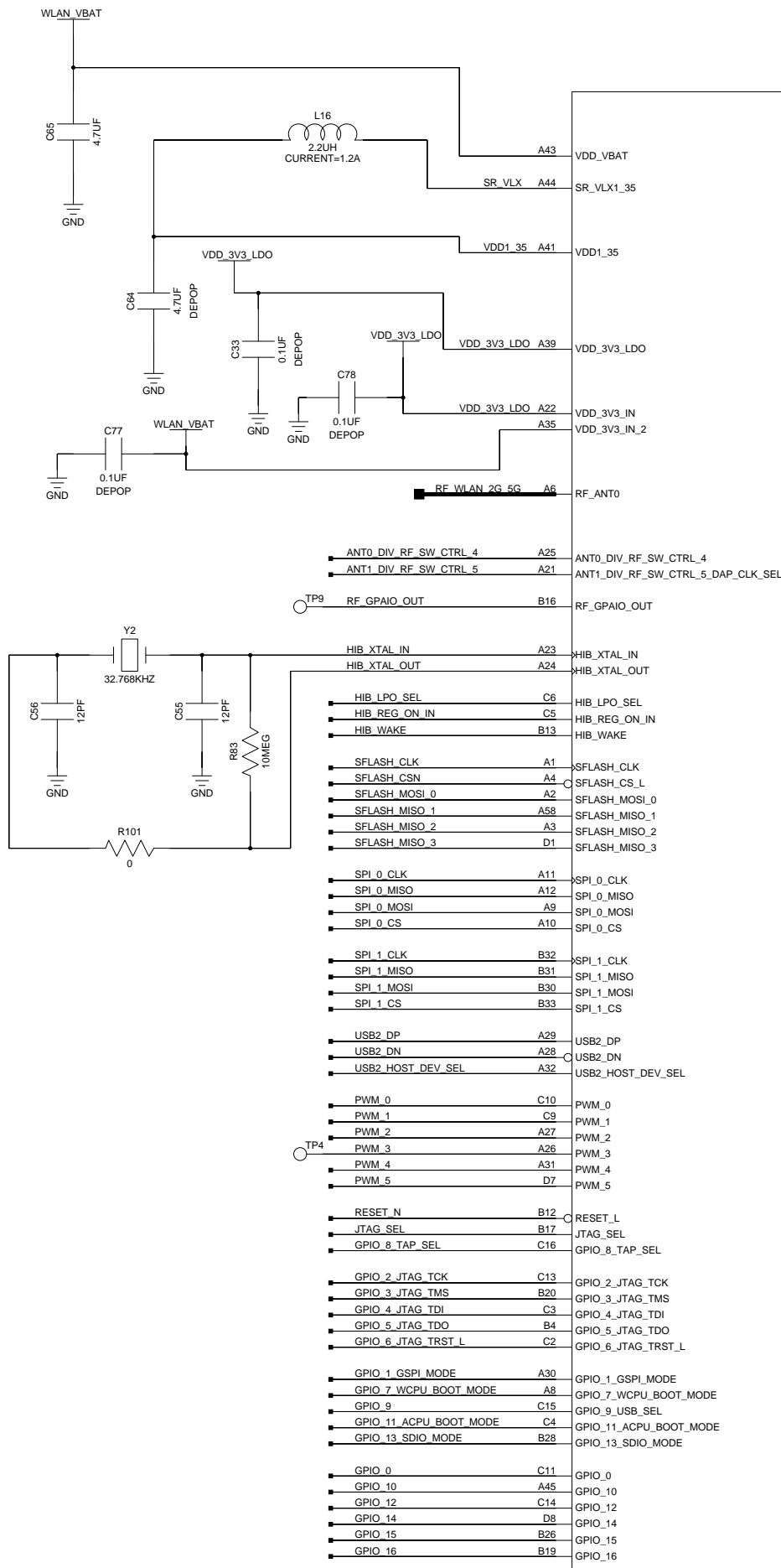
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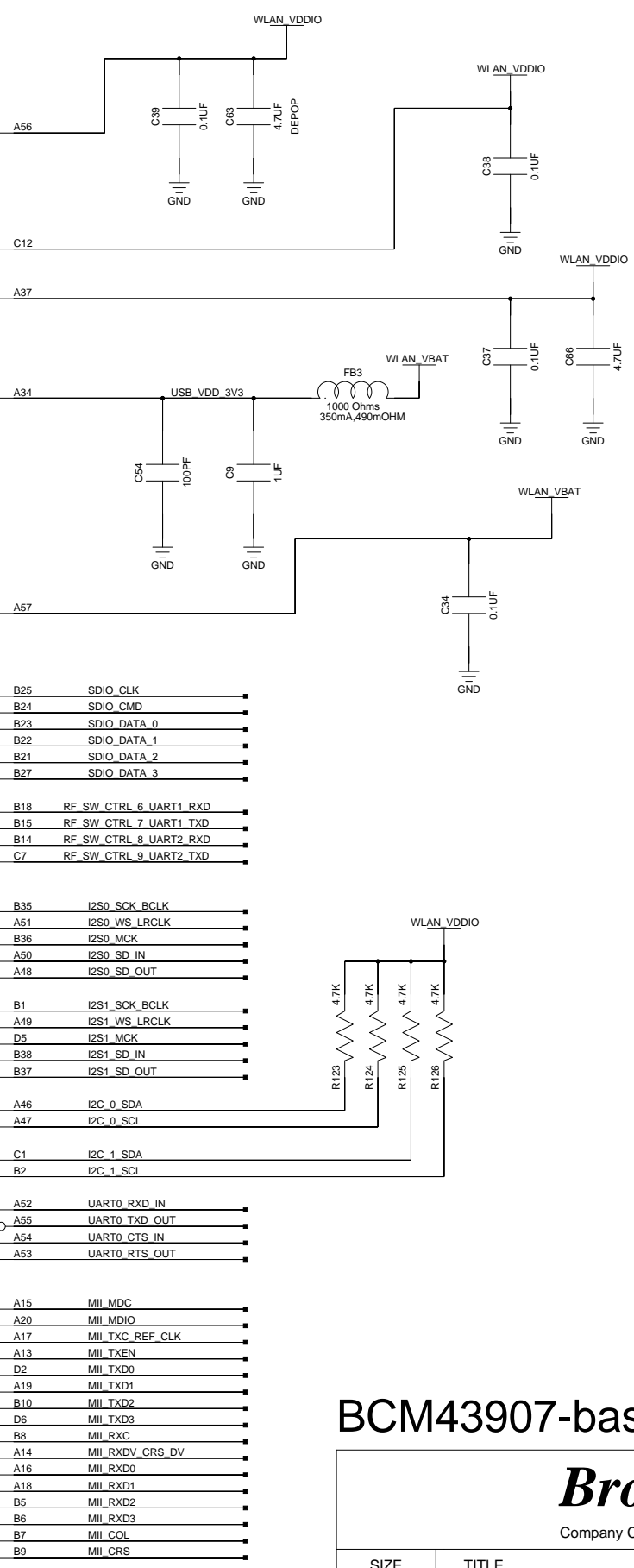
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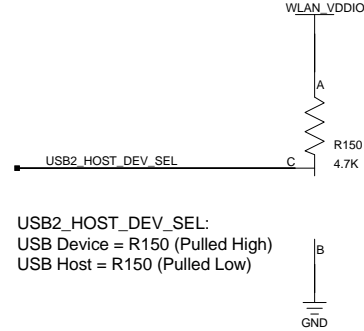
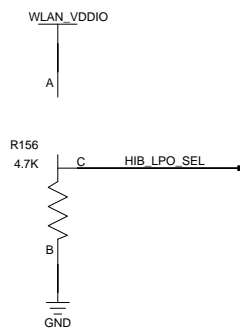
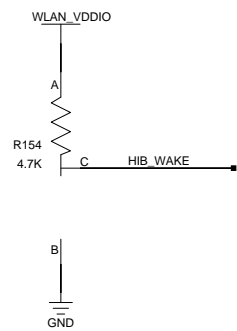
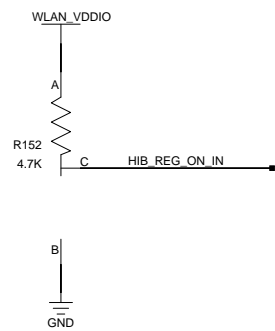
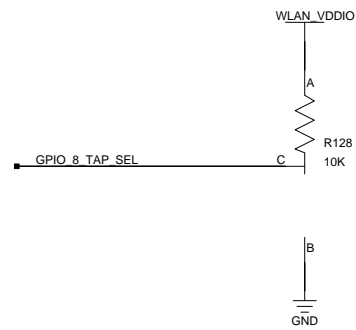
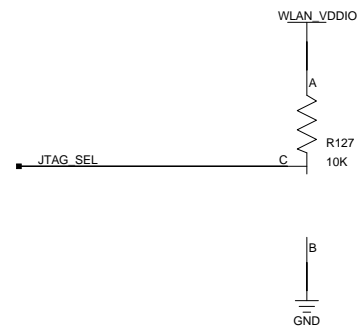
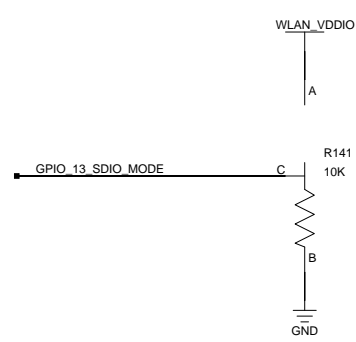
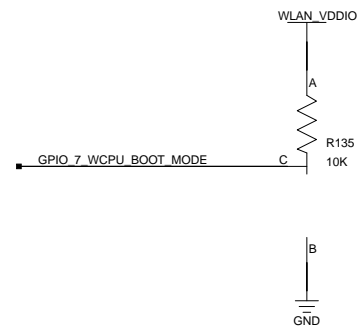


MURATA
LBWA1U21GC-TEMP

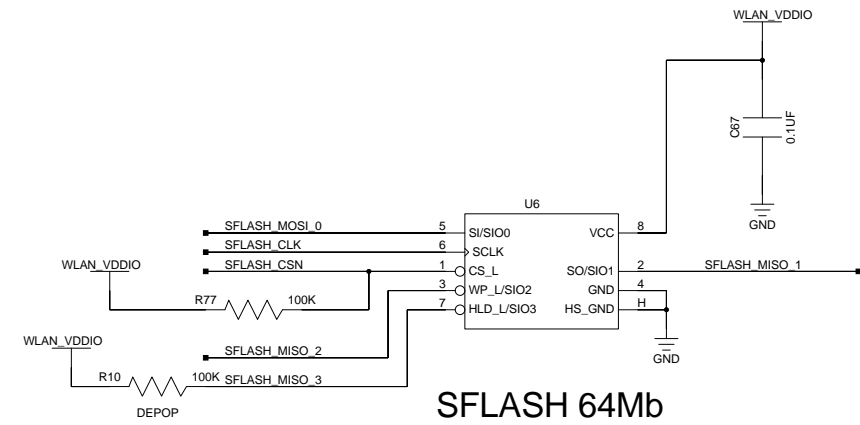


BCM43907-based SiP (10mm x 10mm)

<div>BCM943907AEVAL1_1</div> <div>Company Confidential</div>			
SIZE	TITLE		
B	BCM943907AEVAL1_1		
DATE	28/10/2015:16:15	SHEET	5 OF 10



Pin	Strap Function	Strap Pull	
		Chip Default	Board Default
GPIO_1	gSPI Mode	0	0
GPIO_7	WCPU Boot Mode : 0 = TCROM Boot 1 = TCMSRAM Boot	0	1 R135 = 4.7K to WLAN_VDDIO
GPIO_11	ACPU Boot Mode : 0 = SOCROM Boot 1 = SOCSRAM Boot	0	0
GPIO_13	SDIO Mode : 0 = SDIO Device 1 = SDIO Host	0	0 R141= 4.7K to GND
GPIO_15	(Note: GPIO_15 is not a strap option for B0 IC)	0	0
RF_SW_CTRL_5	Host DAP Clock Sel	0	0
RF_SW_CTRL_7	Host RSRC Init	0	0
RF_SW_CTRL_9	LPO Sel : 0 = LPO from HIB	0	0

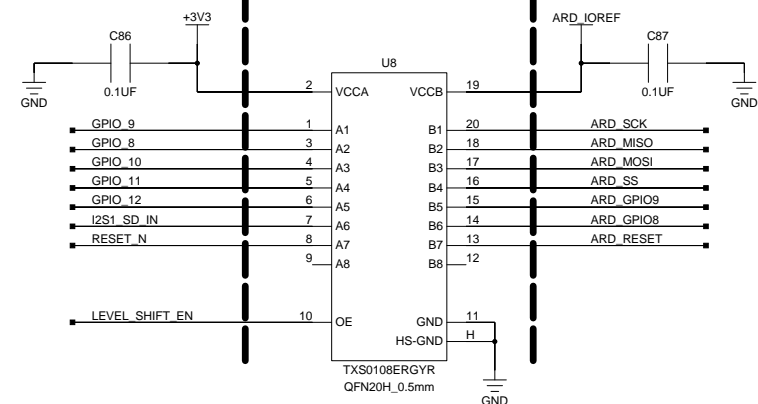
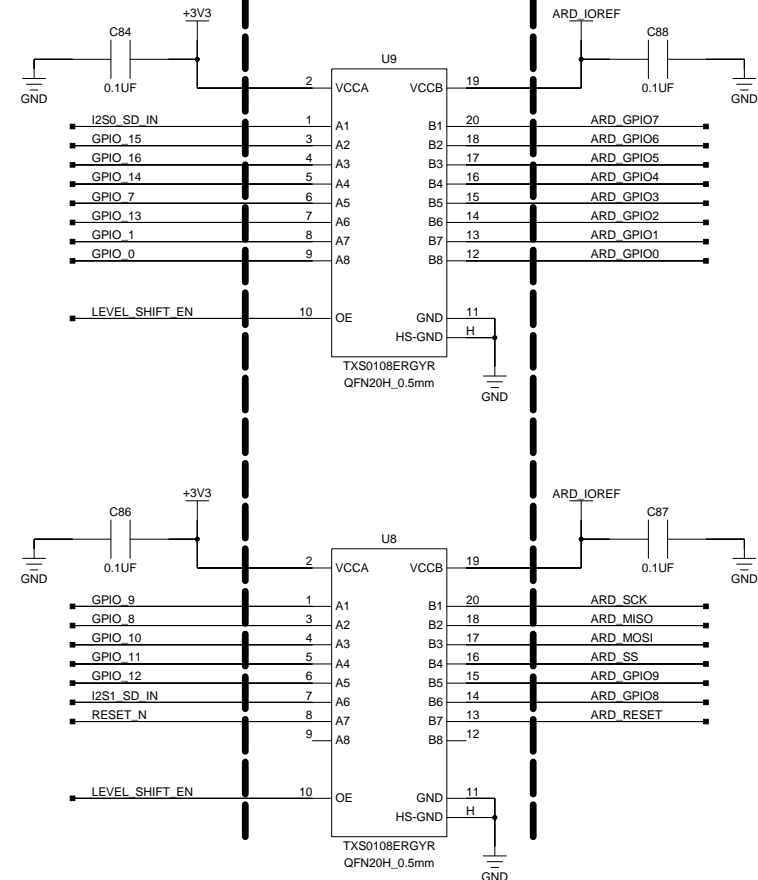
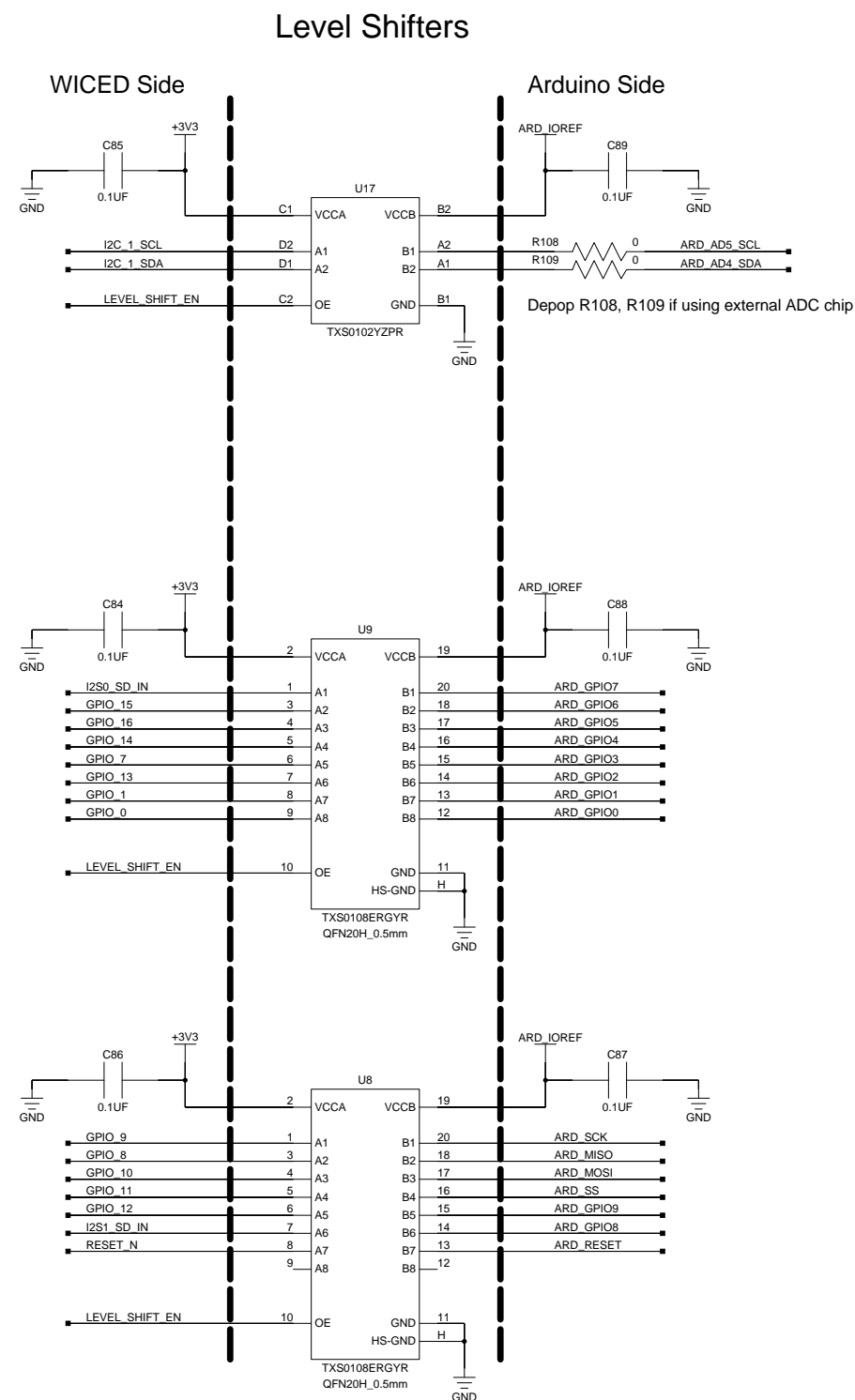
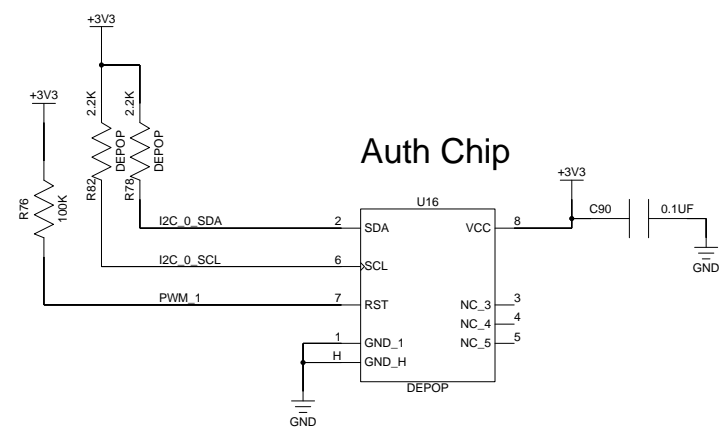
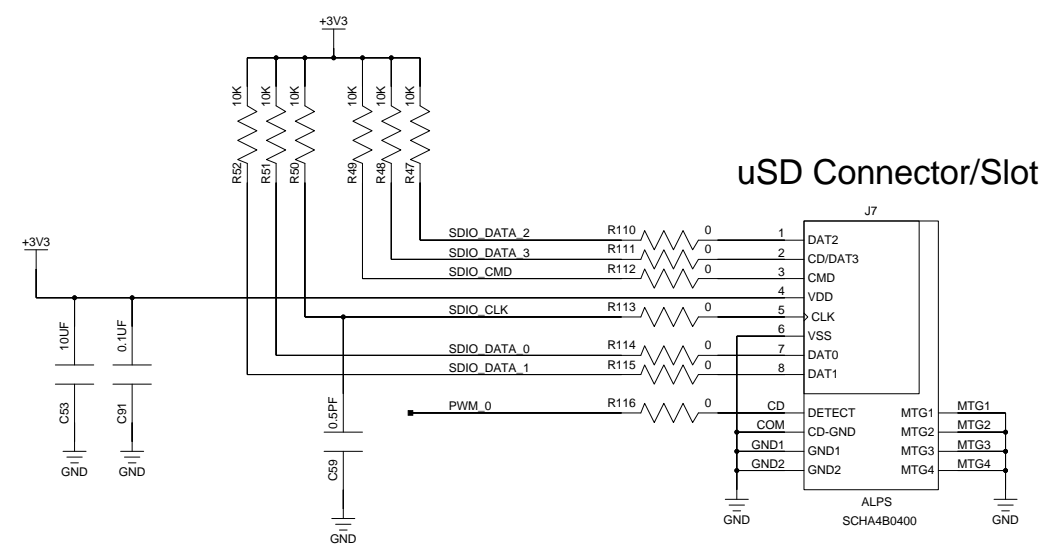
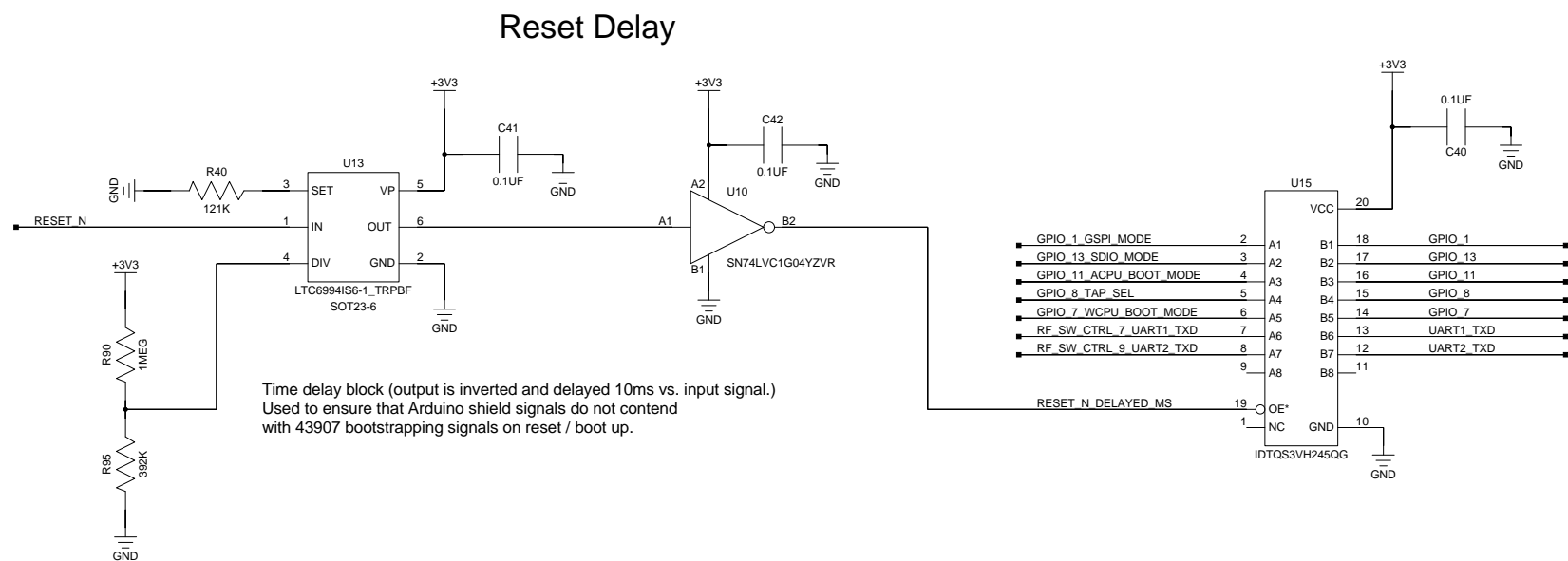


BOOTSTRAPS + SFLASH

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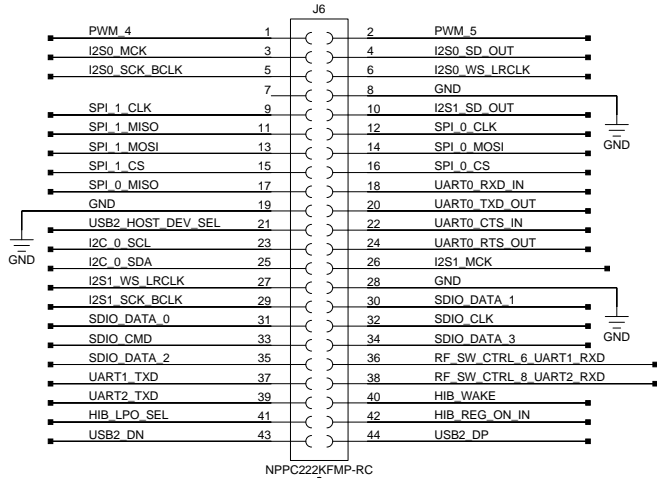
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DATE	30/07/2015:12:08	SHEET	6 OF 10



LEVEL SHIFTERS, DELAY, AUTH IC, SD

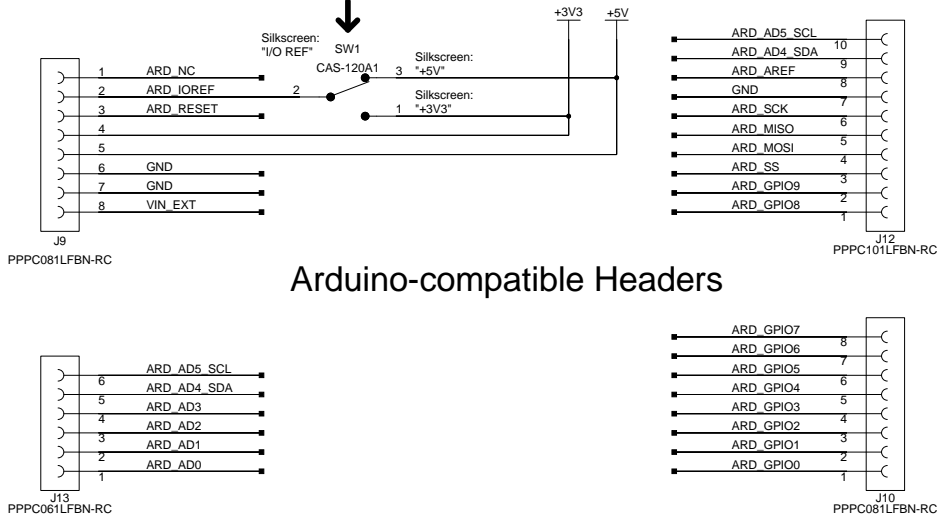
WICED Header (+3V3 only)

Silkscreen:
"WICED HEADER (+3V3 ONLY)"



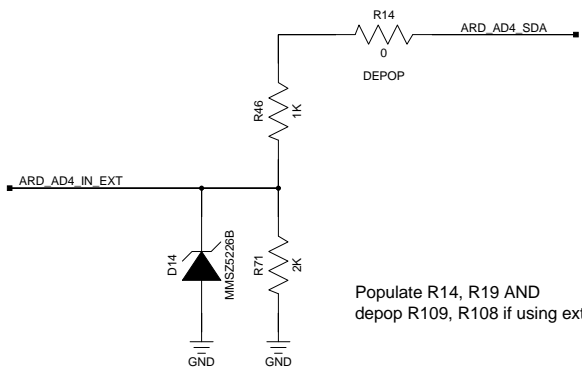
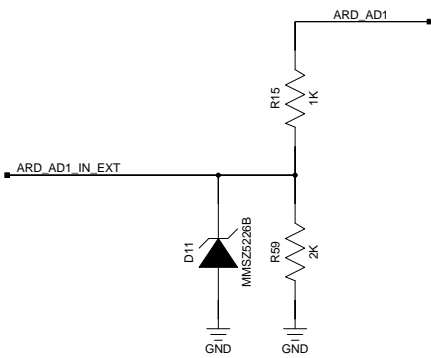
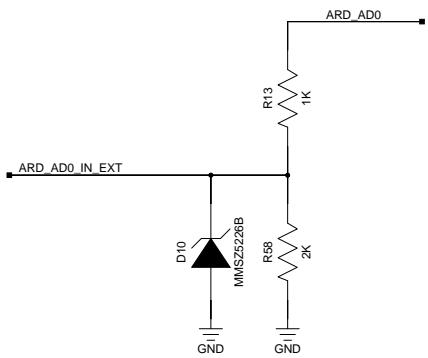
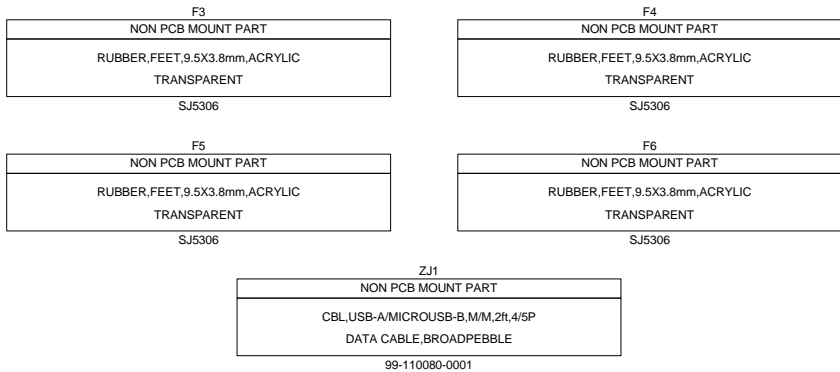
These IO's go directly to the BCM43907 SiP (not via level shifters) and therefore should be +3V3 logic only!!

SW1 is used to indicate to the shield what logic should be used (+3V3 or +5V)

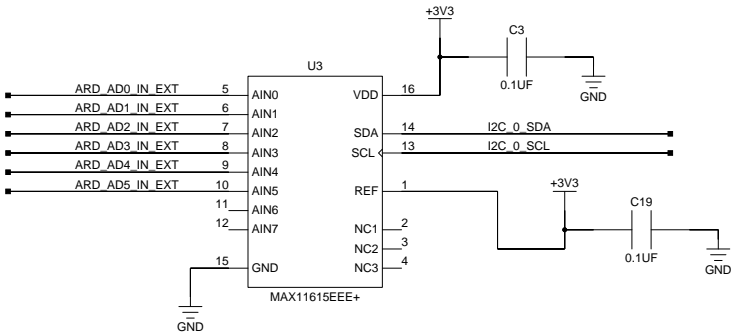
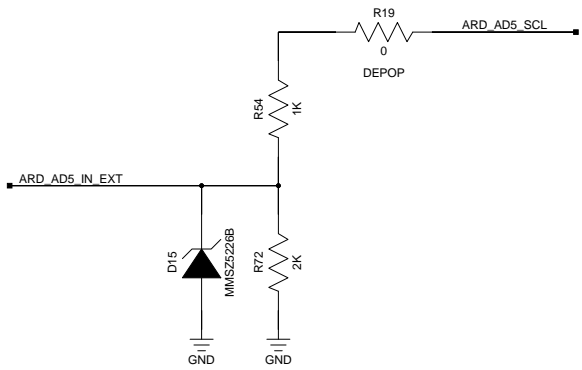
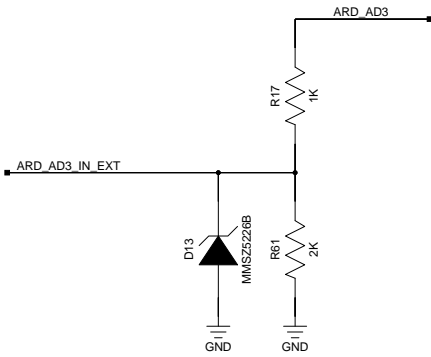
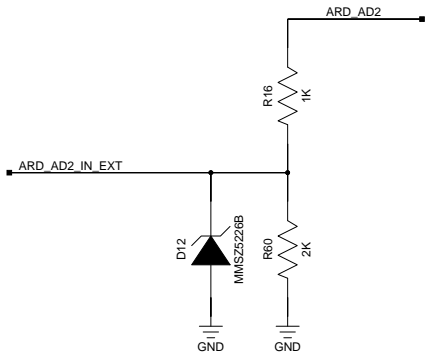


Arduino-compatible Headers

Place acrylic feet at the corners (or as close to) of the board on the bottom layer. Make sure they do not overlap the edge of the board.



Populate R14, R19 AND depop R109, R108 if using external ADC chip



External ADC

ARDUINO HEADERS + WICED HEADER

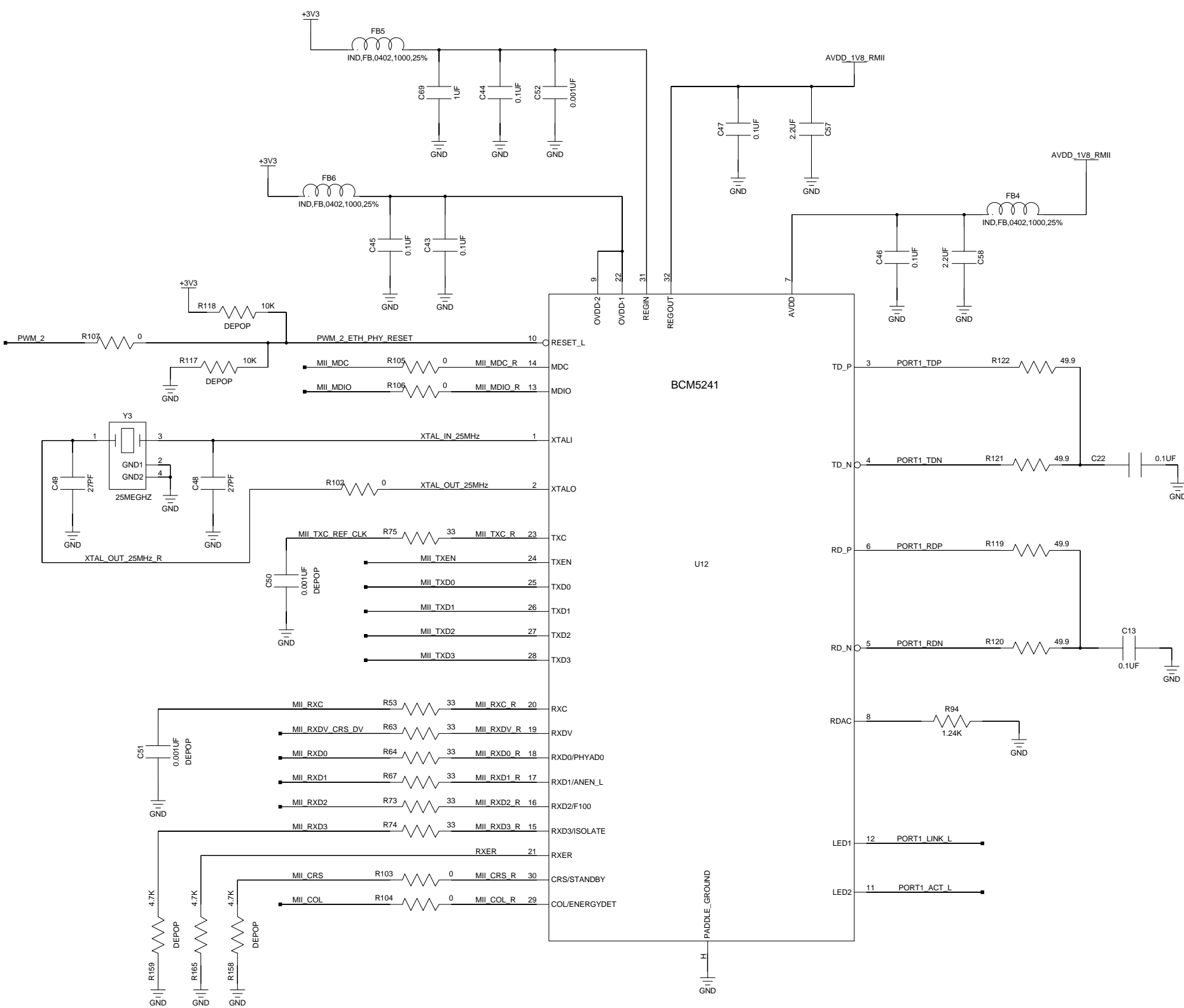
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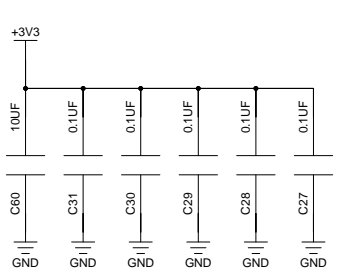
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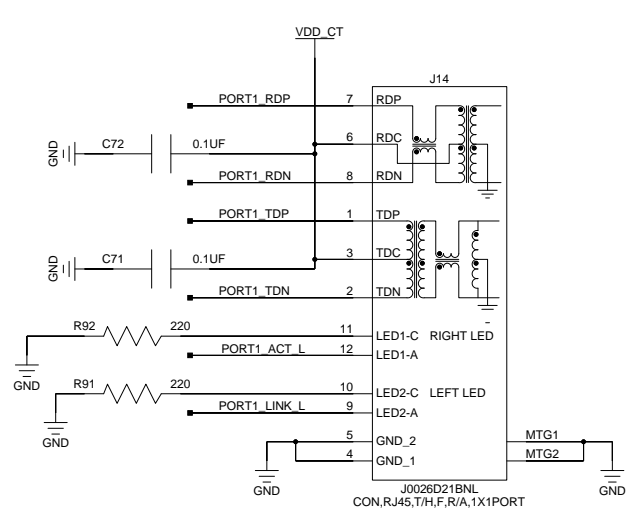
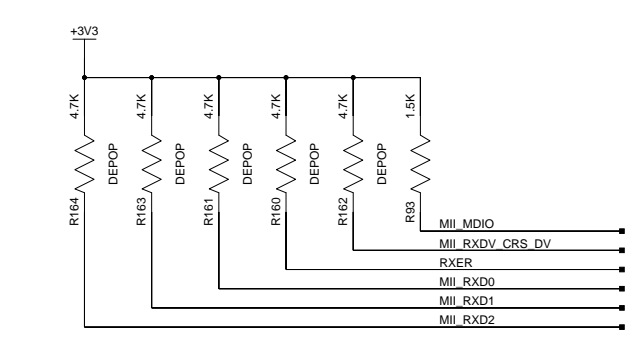
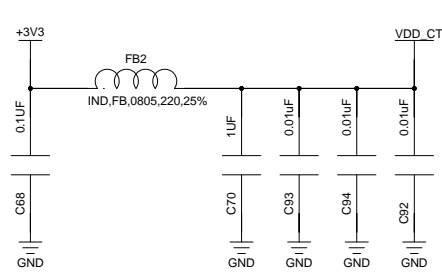
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MII

<div><div><div><div><div><div></div><div>Broadcom</div><div>Company Confidential</div></div></div><div><div>SIZE</div><div>B</div></div><div><div>DATE</div><div>27/07/2015:13:55</div></div></div></div><div><div>TITLE</div><div>BCM943907AEVAL1_1</div></div></div>				
SHEET		10	OF	10

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