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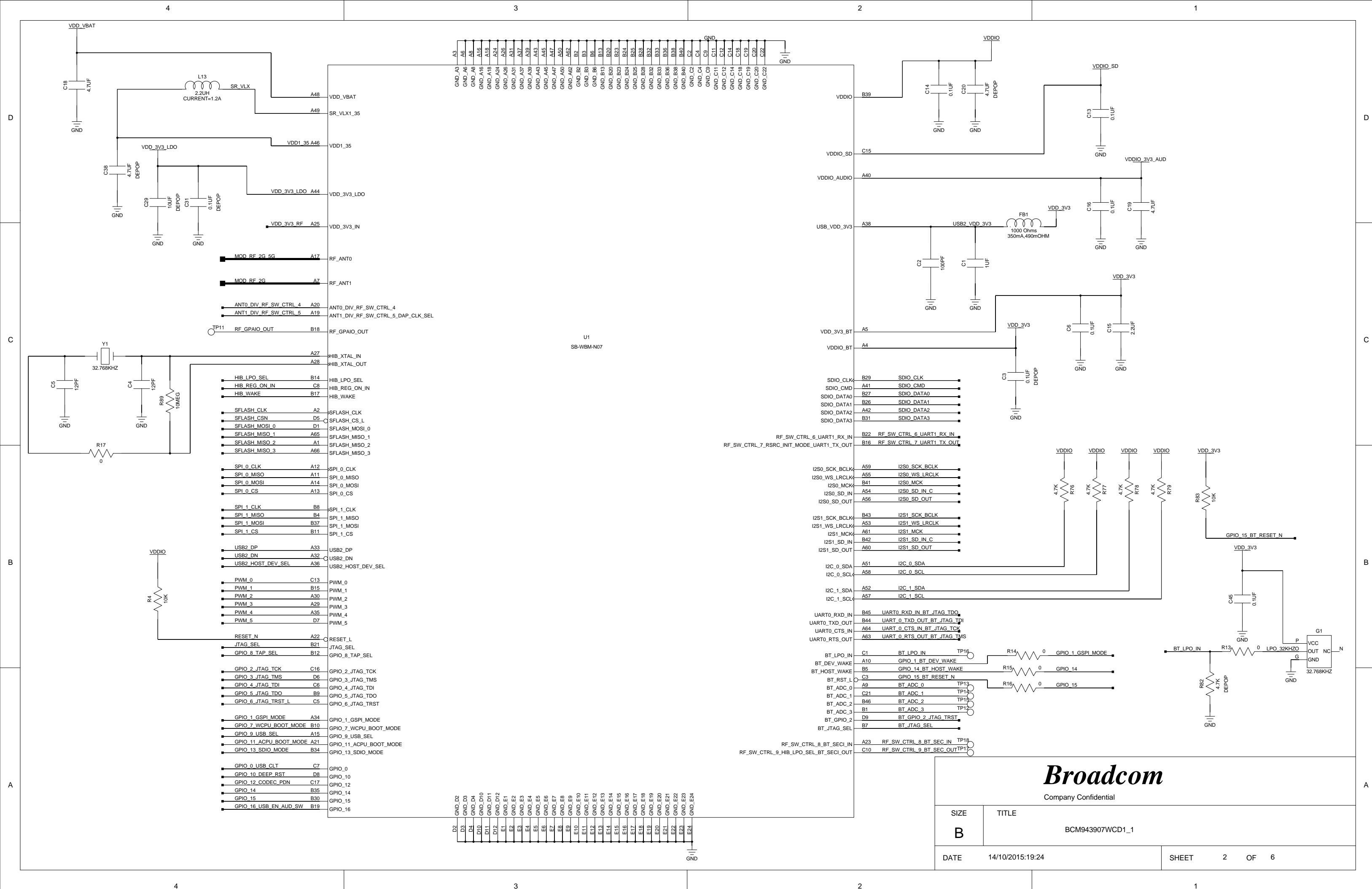
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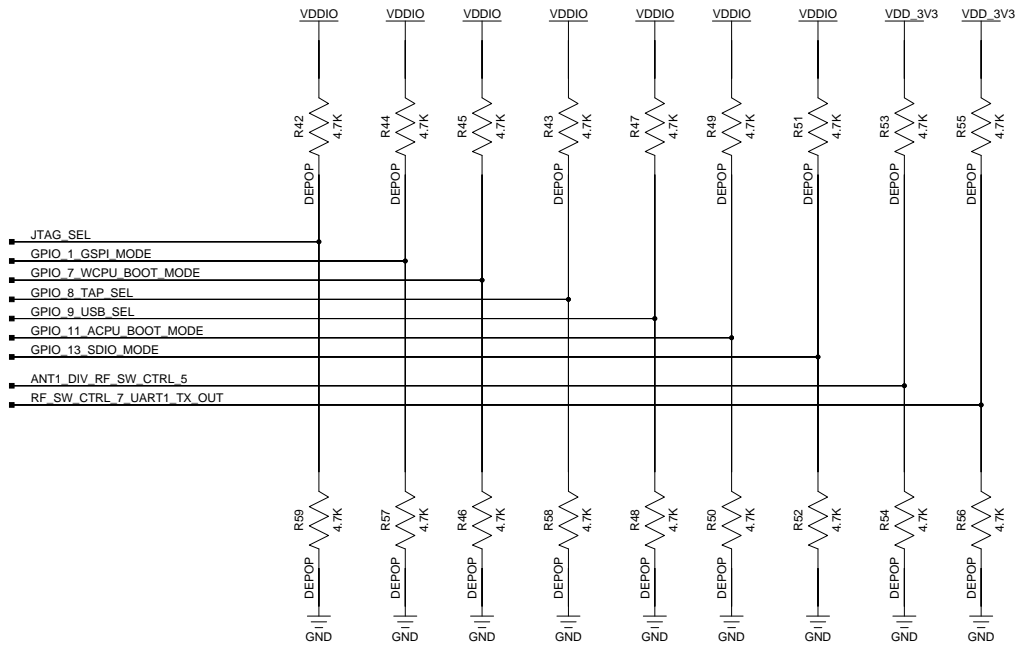
DOCUMENT
SCHEMATIC DIAGRAM 824-129058-0020

PRINTED CIRCUIT BOARD
200-129058-0020

Approvals		<div><div><div>Broadcom</div><div>Company Confidential</div></div></div>				
Designer	Date					
Reviewer	Date	SIZE B	TITLE BCM943907WCD1_2			
Reviewer	Date	Designer VIJAY DESAI		Drawing Number 824-129058-0020		Version 05
Reviewer	Date	DATE 14/10/2015:19:24			SHEET 1 OF 6	



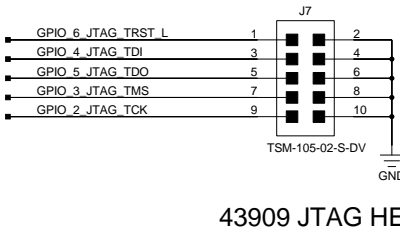
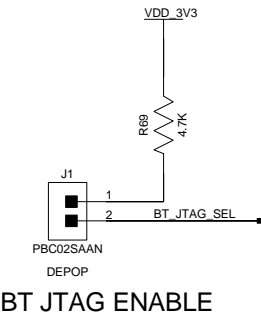
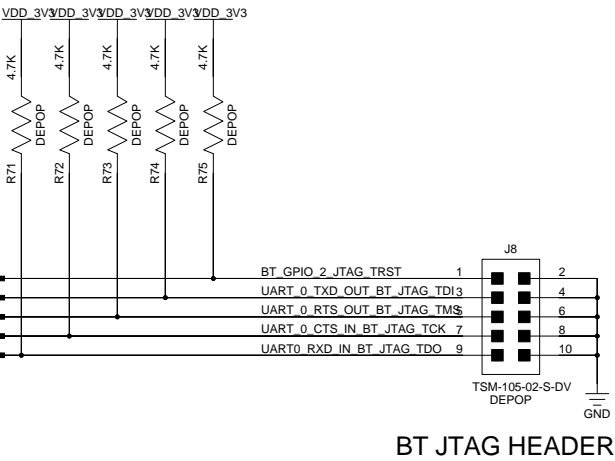
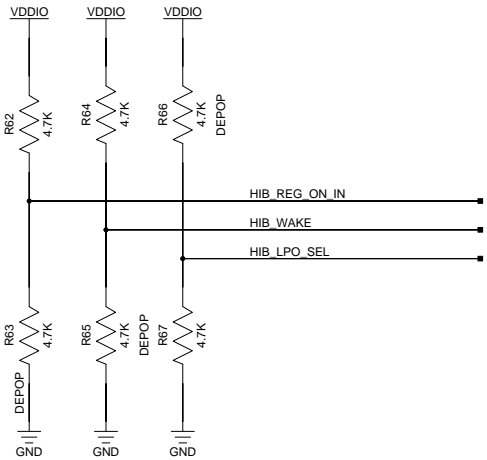
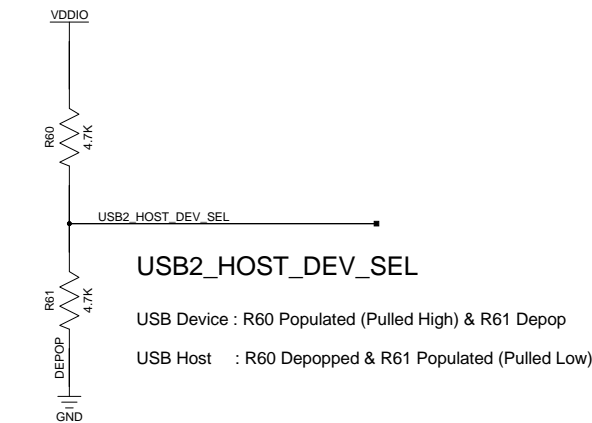
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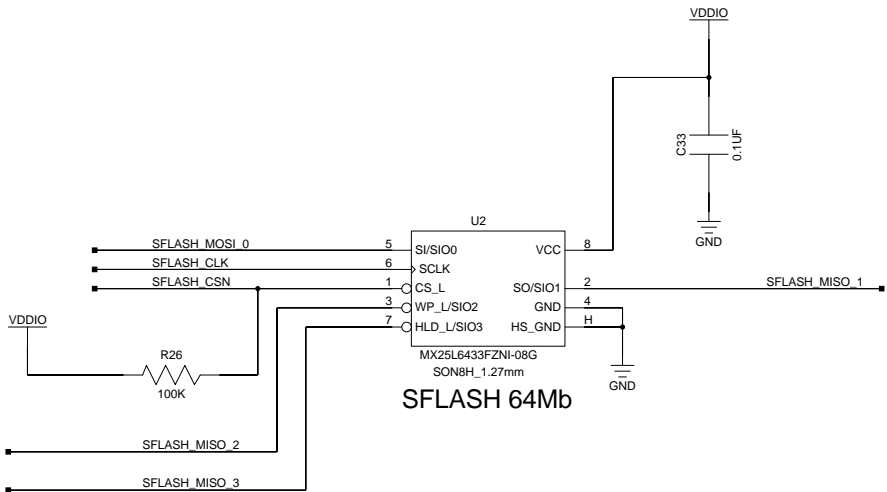
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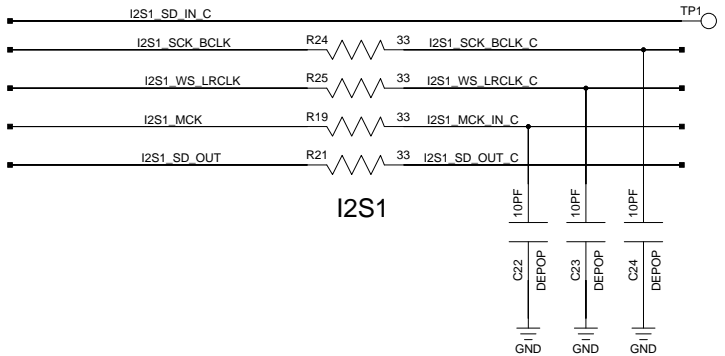
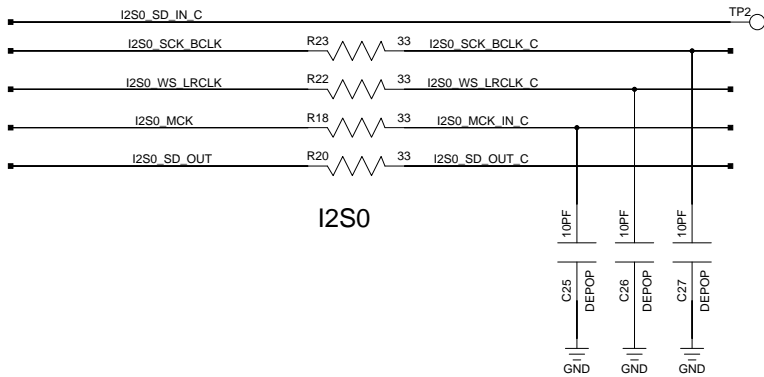
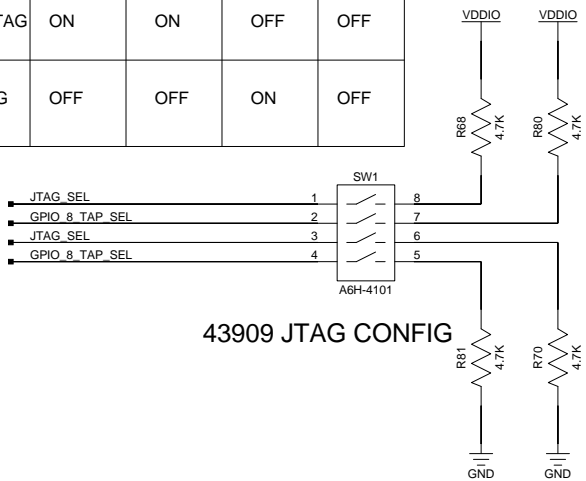


Pin	Strap Function	Strap Pull	
		Chip Default	Board Default
GPIO_1	gSPI Mode	0	0
GPIO_7	WCPU Boot Mode : 0 = TCROM Boot 1 = TCMSRAM Boot	0	1 R45= 4.7K to VDDIO
GPIO_9	USB/HSIC Sel : 0 = HSIC PHY 1 = USB PHY	0	1 R47= 4.7K to 3.3V
GPIO_11	ACPU Boot Mode : 0 = SOCROM Boot 1 = SOCSRAM Boot	0	0
GPIO_13	SDIO Mode : 0 = SDIO Device 1 = SDIO Host	0	0 R52= 4.7K to GND
GPIO_15	Note : GPIO_15 not a strap Option for B0 IC	0	0
RF_SW_CTRL_5	Host DAP Clock Sel	0	0
RF_SW_CTRL_7	Host RSRC Init	0	0
RF_SW_CTRL_9	LPO Sel : 0 = LPO from HIB	0	0



APPS/WLAN JTAG Enable SW1 Switch Control

Mode	SW1 Pin1	SW1 Pin2	SW1 Pin3	SW1 Pin4
WLAN JTAG	ON	OFF	OFF	ON
APPS JTAG	ON	ON	OFF	OFF
NO JTAG	OFF	OFF	ON	OFF



Straps + SFLASH + MISC

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SIZE B	TITLE BCM943907WCD1_1		
DATE	21/05/2015:15:53	SHEET	3 OF 6

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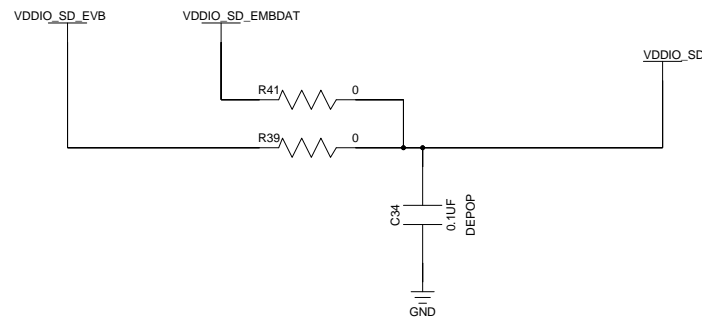
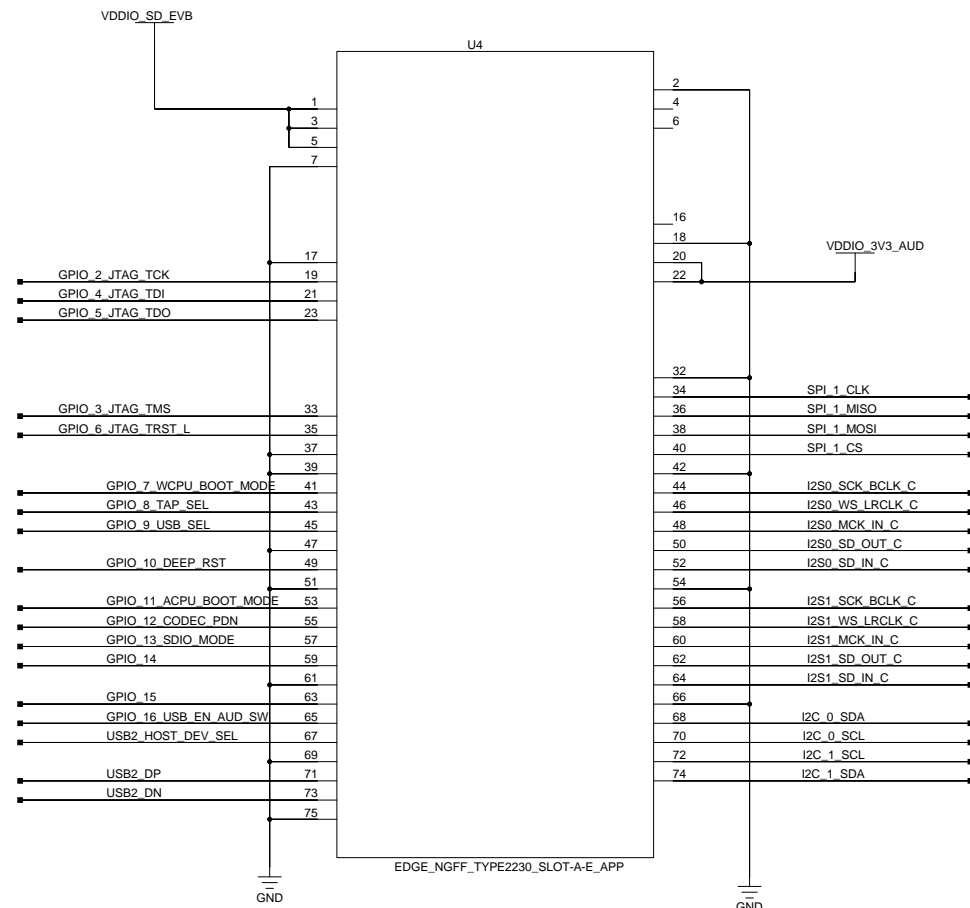
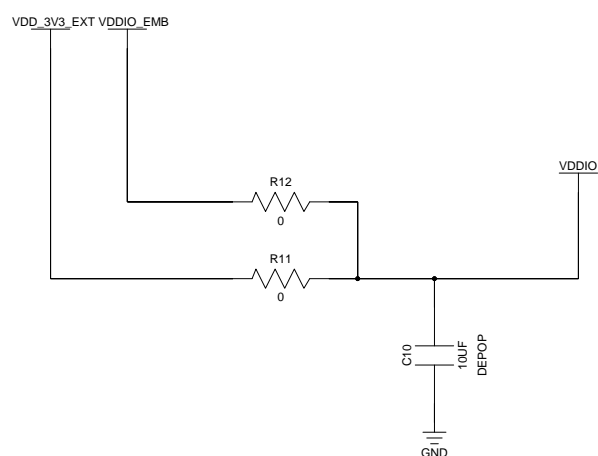
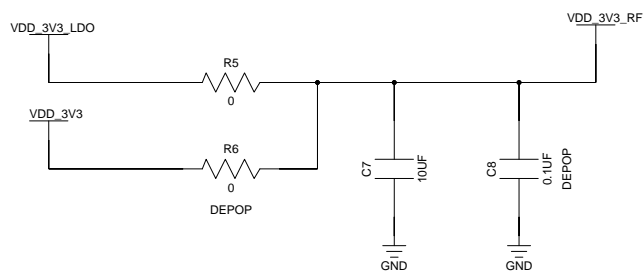
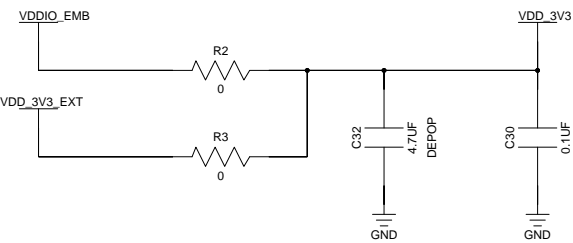
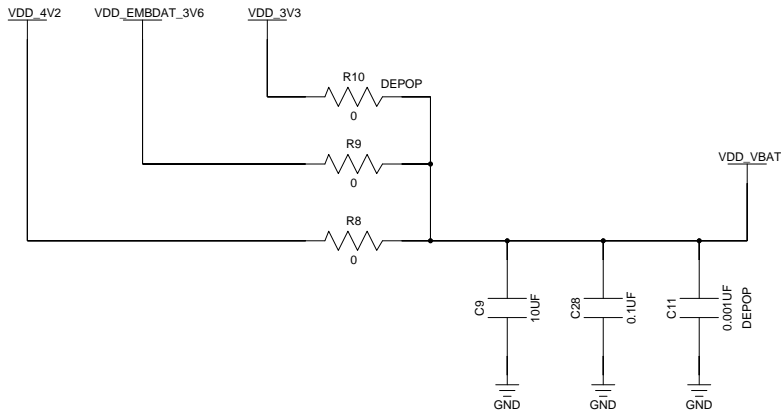
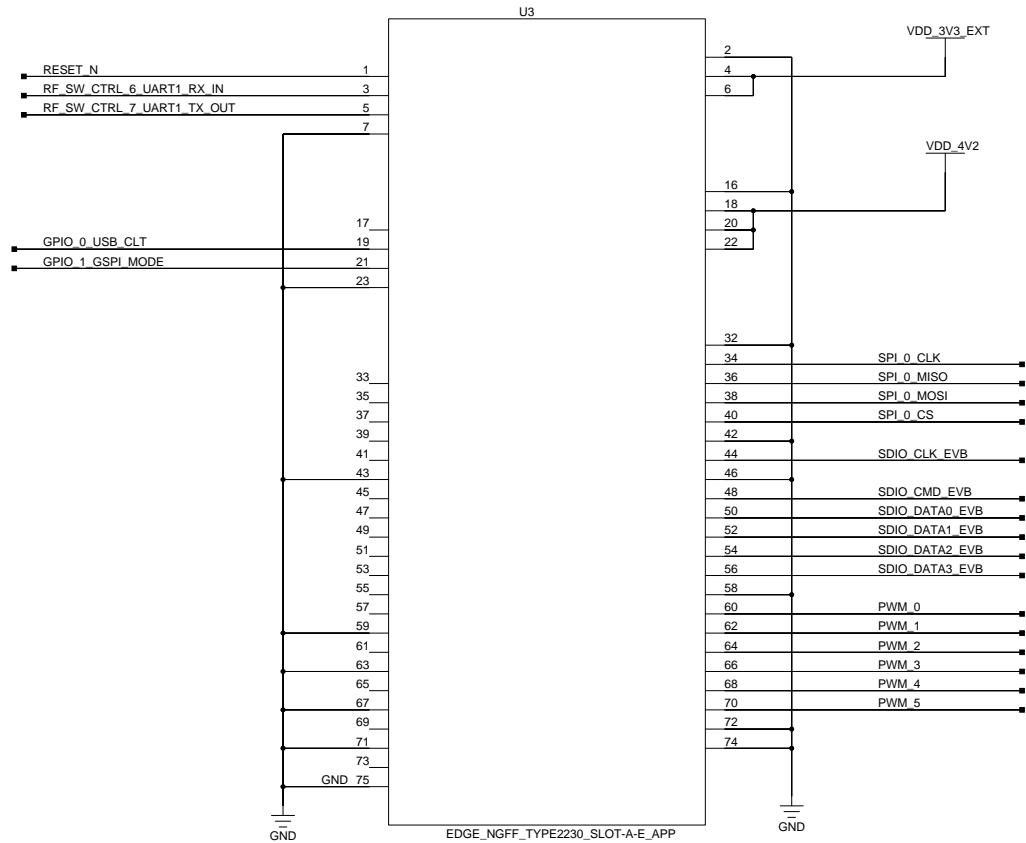
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M.2 Connectors

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SIZE	TITLE	DATE	11/03/2015:19:16	SHEET	4	OF	6
B	BCM943907WCD1_1						

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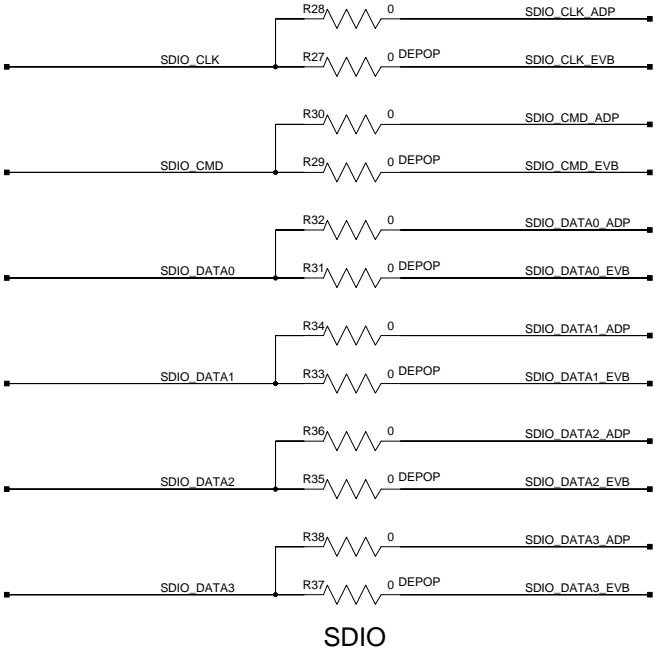
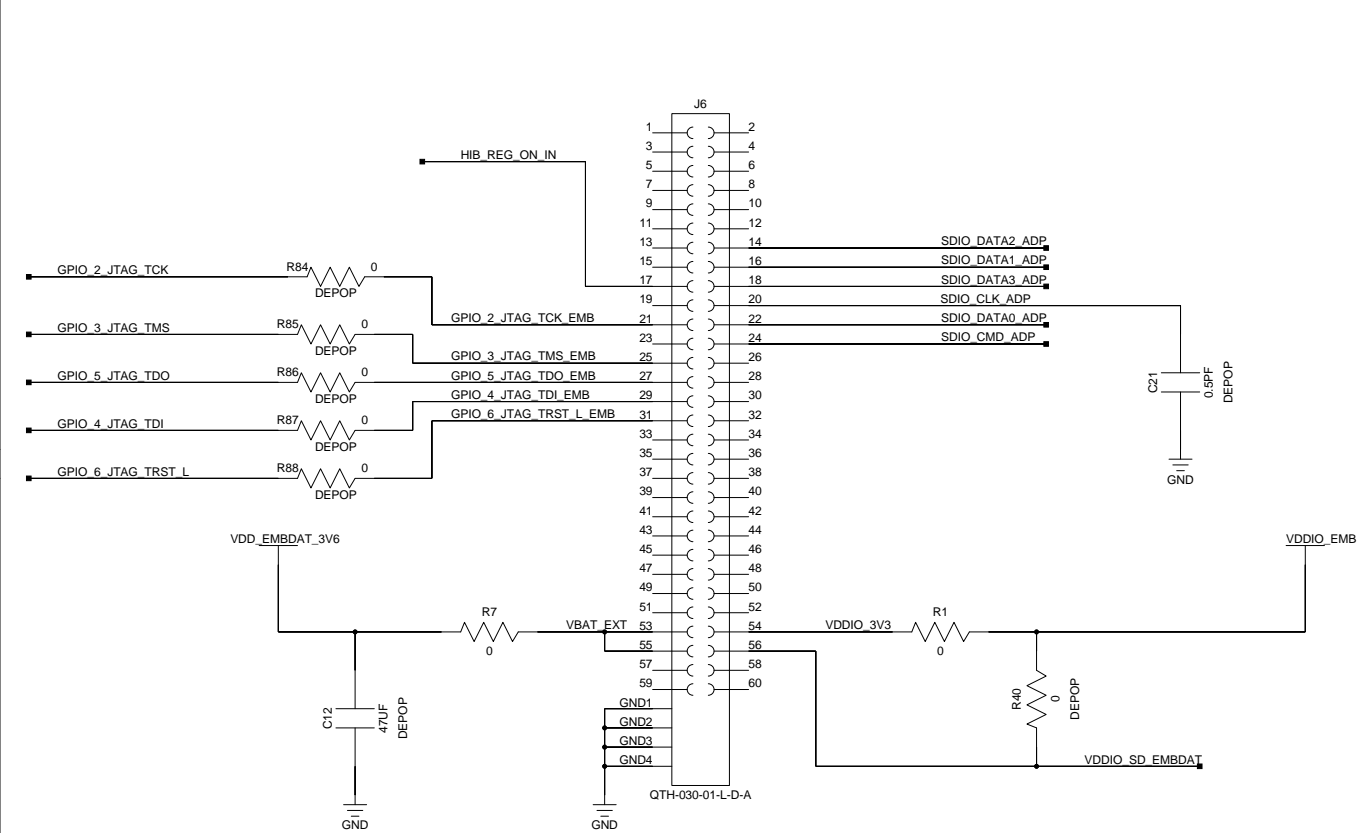
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60 Pin Connector

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SIZE	TITLE		
B	BCM943907WCD1_1		
DATE	10/03/2015:16:45	SHEET	5 OF 6

RF Front End Configurations

1. Default (WLAN + BT Module) - MOD_RF_2G and MOD_RF_2G_5G through DPDT Ant Diversity - Stuffing Options Populate : C58, C41, C57, C35, C36, C37, C59, C55, C52, Depop : C42, C17, C44, C43, C60, C61, C50, C54, C56, C51, C53
2. Bypass Option1(WLAN + BT Module) - MOD_RF_2G to Ant1 and MOD_RF_2G_5G to Ant0 - Stuffing Options Populate : C58, C57, C42, C17, C44, C43, C59, C52, Depop : C41, C35, C60, C61, C37, C36, C50, C54, C56, C51, C53
3. Bypass Option3 (WLAN Only No BT Module) - MOD_RF_2G_5G through SPDT Diversity SW to Ant0 or ANT1 - Stuffing Options Populate - C57, C44, C43, C50, C54, C56, C51, C53, Depop : C58, C41, C42, C17, C36, C55, C37, C59, C52, C60, C61
4. Bypass Option4 (WLAN Only No BT Module) - MOD_RF_2G_5G to Ant0 through Matching Components - Stuffing Options Populate : C57, C44, C43, C59, C52 Depop : C58, C41, C42, C17, C36, C55, C37, C50, C56, C54, C51, C53
5. Bypass Option5 (WLAN Only No BT Module) - MOD_RF_2G_5G to Ant0 shortest path Populate : C57, C60, C61 Depop : C44, C43, C59, C52, C58, C41, C42, C17, C36, C55, C37, C50, C56, C54, C51, C53

RF Front End Configurations

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SIZE B		TITLE SCHEMATIC - BCM9XXXX			
DATE 13/03/2015:16:07			SHEET 34 OF 7		