

SCHEMATIC DIAGRAM

824-129511-0010

200-129511-0010

SHEET 1: SYSTEM BLOCK DIAGRAM

SHEET 2: WICED / ARDUINO HEADER PAIRS

SHEET 3: POWER

SHEET 4: PROGRAM / DEBUG INTERFACE

SHEET 5: BCM43907-based SiP (10mm x 10mm)

SHEET 6: BOOTSTRAPS + SFLASH

SHEET 7: RF

SHEET 8: LEVEL SHIFTERS, DELAY, AUTH IC, SD

SHEET 9: ARDUINO HEADERS + WICED HEADER

SHEET 10: MII

SYSTEM BLOCK DIAGRAM

Approvals			Broadcom						
Designer	Date		Diodacont						
		Company Confidential							
Reviewer	Date	SIZE	TITLE						
Reviewer Date		В	BCM943907AEVAL1_1						
		Ь							
Reviewer	Date	Designer			Drawing Number	Version			
		LI	LEX		824-129511-0010				
Reviewer	Date								
		DATE	28/10/2015:16:14		SHEET 1 OF 10				
		DATE	28/10/2015:16:14		SHEET 1 OF 10				

muRata BCM43907-based SiP Pin Name	WICED header net name
PWM_5 1250_MCK 1250_SD_OUT 1250_SCK_BLCK 1250_WS_LRCLK NC GND SPI_1_CLK 1251_SD_OUT SPI_1_MISO SPI_0_CLK SPI_1_MOSI SPI_0_MOSI SPI_0_MOSI SPI_0_CS SPI_0_CS SPI_0_CS SPI_0_CS SPI_0_CTS_IN 12C_0_SCL UART0_CTS_IN 12C_0_SCL UART0_RTS_OUT 12C_0_SDA 1251_MCK 1251_WS_LRCLK GND 1251_SCK_BCLK SDIO_DATA1 SDIO_DATA3 SDIO_CMD SDIO_DATA2 RF_SW_CTRL_7_RSRC_INIT_MODE_UART1_TX_OU RF_SW_CTRL_9_HIB_LPO_SEL_UART2_TX HIB_WAKE HIB_LPO_SEL HIB_REG_ON_IN USB2_DP	PWM_4 PWM_5 I2S0_MCK I2S0_SD_OUT I2S0_SCK_BLCK I2S0_WS_LRCLK NC GND SPI_1_CLK I2S1_SD_OUT SPI_1_MISO SPI_0_CLK SPI_1_MOSI SPI_1_CS SPI_0_MOSI SPI_1_CS SPI_0_MISO UART0_RXD_IN GND UART0_TXD_OUT USB2_HOST_DEV_SEL UART0_CTS_IN I2C_0_SDA I2S1_MCK I2S1_WS_LRCLK GND I2S1_SCK_BCLK SDIO_DATA_1 SDIO_DATA_1 SDIO_DATA_2 RF_SW_CTRL_6_UART1_RXD T UART1_TXD RF_SW_CTRL_8_UART2_RXD UART2_TXD HIB_WAKE HIB_LPO_SEL HIB_REG_ON_IN USB2_DP
muRata BCM43907-based SiP Pin Name	On Board Debug Interface

muRata BCM43907-based SiP Pin Name	muRata BCM43907-based SiP Pin Function	43907 bootstrap option	Arduino header net name	Arduino functions
GPIO_0 GPIO_1_GSPI_MODE GPIO_13_SDIO_MODE GPIO_7_WCPU_BOOT_MODE GPIO_14 GPIO_16 GPIO_15 I2SO_SD_IN N/A (uses external ADC chip) I2C_1_SDA I2C_1_SCL I2S1_SD_IN GPIO_12 GPIO_11_ACPU_BOOT_MODE GPIO_10 GPIO_8_TAP_SEL GND N/A (supplied off eval board) I2C_1_SCL NC N/A (supplied off eval board) RESET_L N/A (supplied off eval board) N/A (supplied off eval board) GND GND GND N/A (supplied off eval board)	UARTO_RX UARTO_TX GPIO_13 PWM1 GPIO_14 PWM0 PWM1 GPIO_26 ADC ADC ADC ADC I2C_SDA I2C_SCL GPIO_29 PWM2 SPI1_CS, PWM5 SPI1_MOSI, PWM4 SPI1_MISO, PWM2 SPI1_CLK, PWM3 GND N/A (supplied off eval board) I2C_SDA I2C_SCL NC N/A (supplied off eval board) RESET N/A (supplied off eval board) N/A (supplied off eval board) N/A (supplied off eval board) RESET N/A (supplied off eval board) N/A (supplied off eval board) GND GND GND N/A (supplied off eval board)	GSPI MODE SDIO MODE WLAN BOOT	ARD_GPIO0 ARD_GPIO1 ARD_GPIO2 ARD_GPIO3 ARD_GPIO4 ARD_GPIO5 ARD_GPIO6 ARD_GPIO7 ARD_AD0 ARD_AD1 ARD_AD2 ARD_AD3 ARD_AD4_SDA ARD_AD5_SCL ARD_GPIO9 ARD_SS ARD_MOSI ARD_MISO ARD_SCK GND ARD_AREF ARD_AD4_SDA ARD_AD5_SCL ARD_RESET +3V3 5V_EXT GND GND VIN_EXT	UART_RX UART_TX GPIO PWM GPIO PWM PWM GPIO AD0 AD1 AD2 AD3 AD4, SDA AD5, SCL GPIO PWM PWM, SPI_CS PWM, SPI_MOSI SPI_MISO SPI_SCK GND POWER AD4, SDA AD5, SCL NC POWER RESET POWER POWER GND GND POWER

muRata BCM43907-based SiP Pin Name	On Board Debug Interface
GPIO2_JTAG_TCK GPIO3_JTAG_TMS GPIO4_JTAG_TDI GPIO5_JTAG_TDO GPIO6_JTAG_TRST_L RF_SW_CTRL_7_RSRC_INIT_MODE_UART1_TX_OUT RF_SW_CTRL_6_UART1_RX_IN	GPIO2_JTAG_TCK GPIO3_JTAG_TMS GPIO4_JTAG_TDI GPIO5_JTAG_TDO GPIO6_JTAG_TRST_L RF_SW_CTRL_7_UART1_TXD RF_SW_CTRL_6_UART1_RXD

WICED / ARDUINO HEADER PAIRS

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Company Confidential								
SIZE	TITLE							
В	BCM943907AEVAL1_1							
DATE	21/07/2015:17:16		SHEET	2	OF	10		















