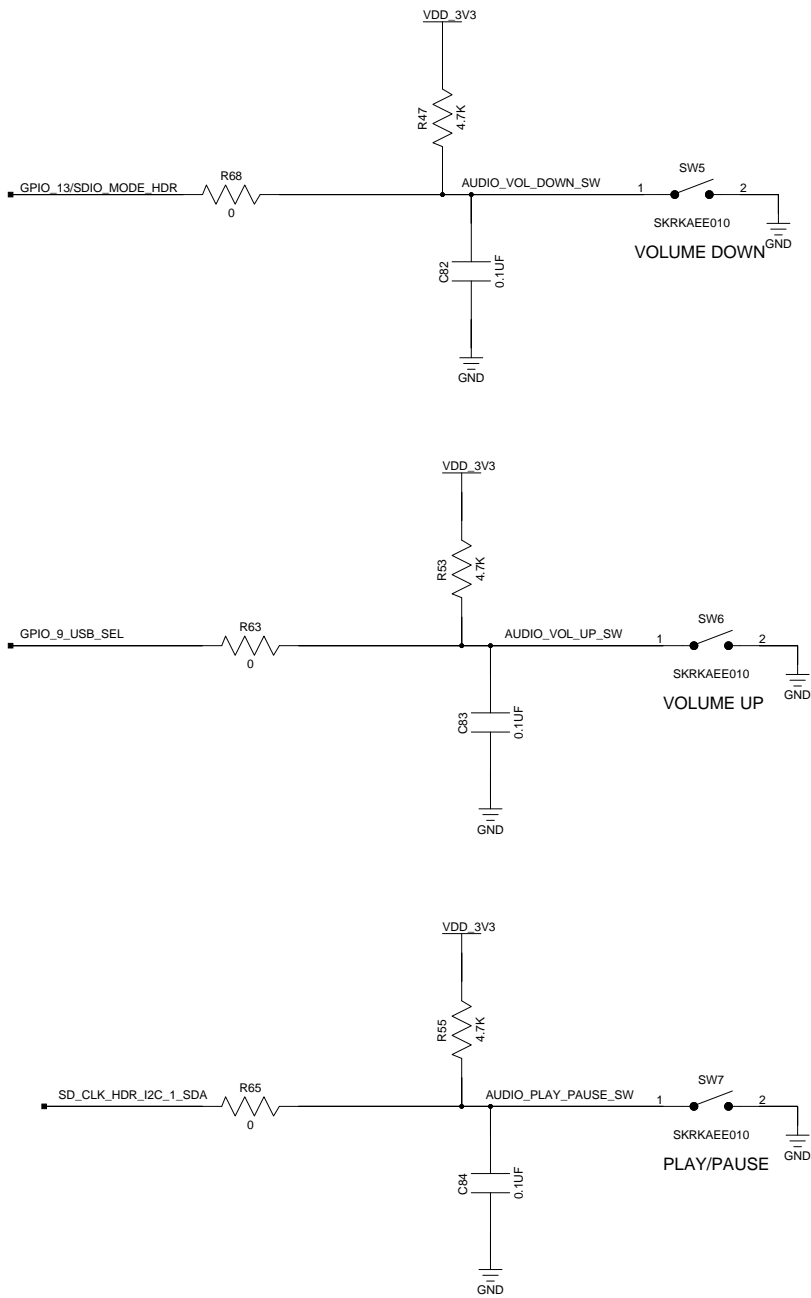
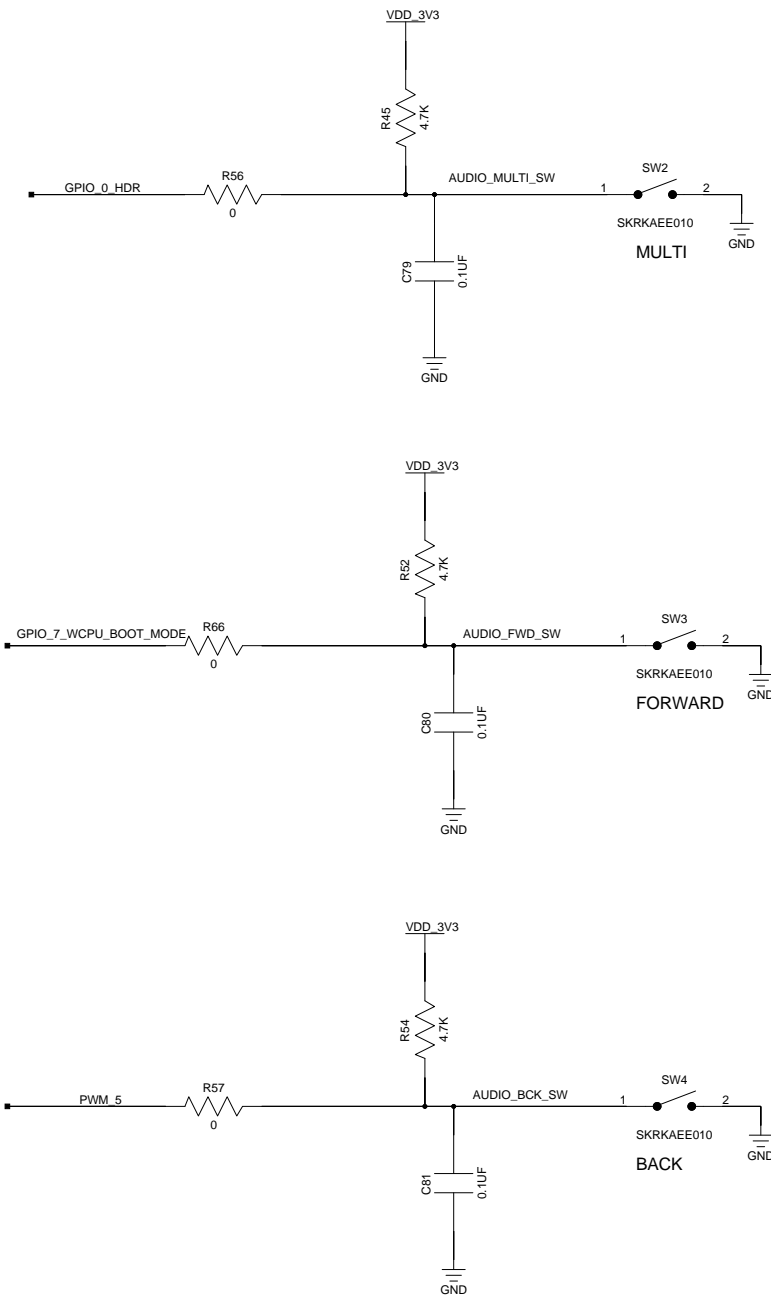


AUDIO SWITCHES ARRAY

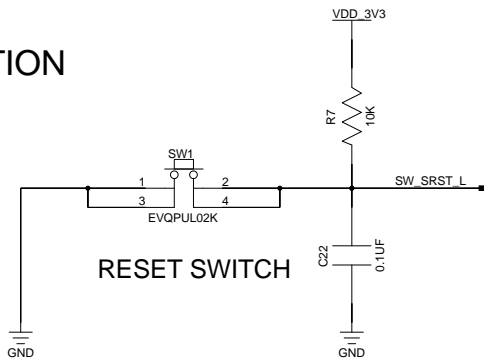


DOCUMENT
SCHEMATIC DIAGRAM 824-129279-0010

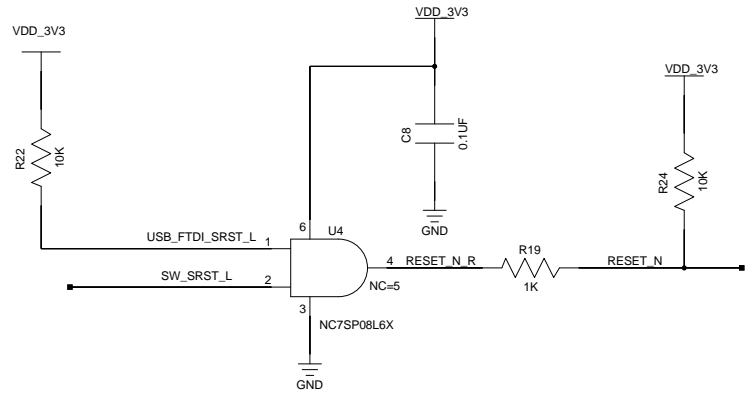
PRINTED CIRCUIT BOARD
200-129279-0010

Approvals		<div>Broadcom</div> <div>Company Confidential</div>		
Designer	Date			
Reviewer	Date	SIZE B	TITLE BCM943907WAE_1	
Reviewer	Date	Designer MILIU		Drawing Number 824-129279-0010
Reviewer	Date			Version 01
DATE 05/05/2015:17:23			SHEET 1 OF 6	

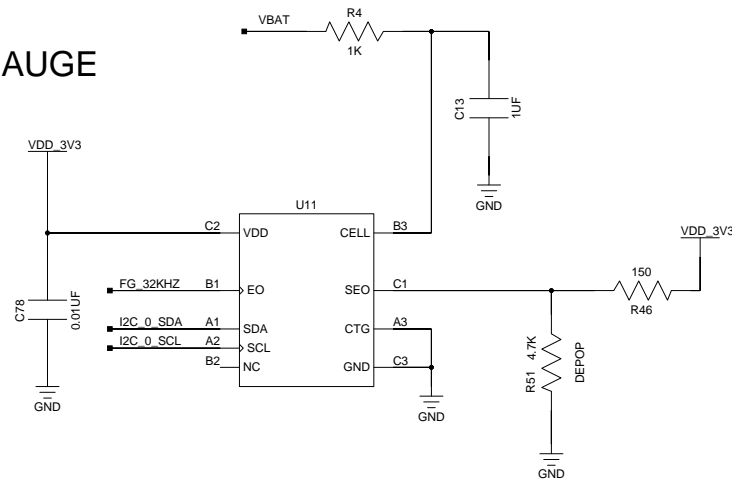
RESET FUNCTION



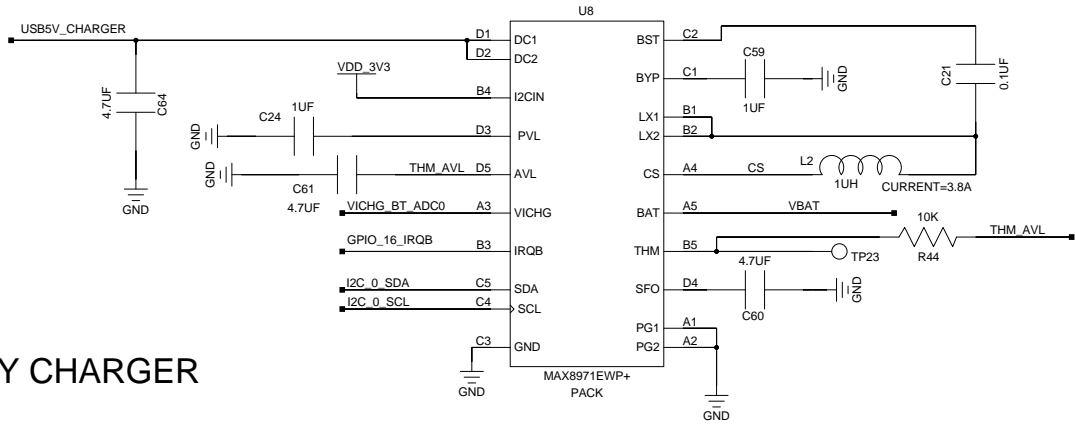
RESET SWITCH



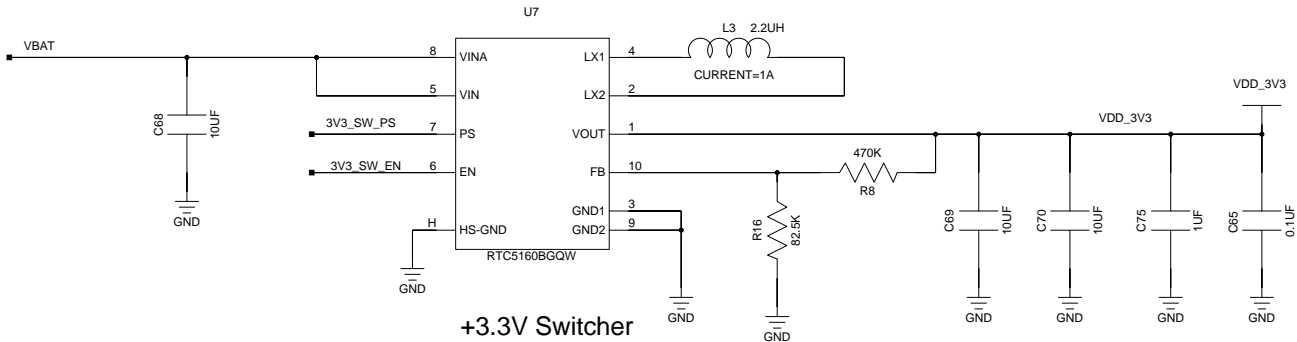
BATTERY FUEL GAUGE



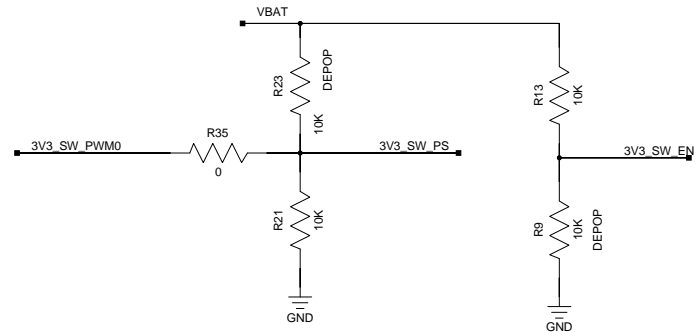
BATTERY CHARGER



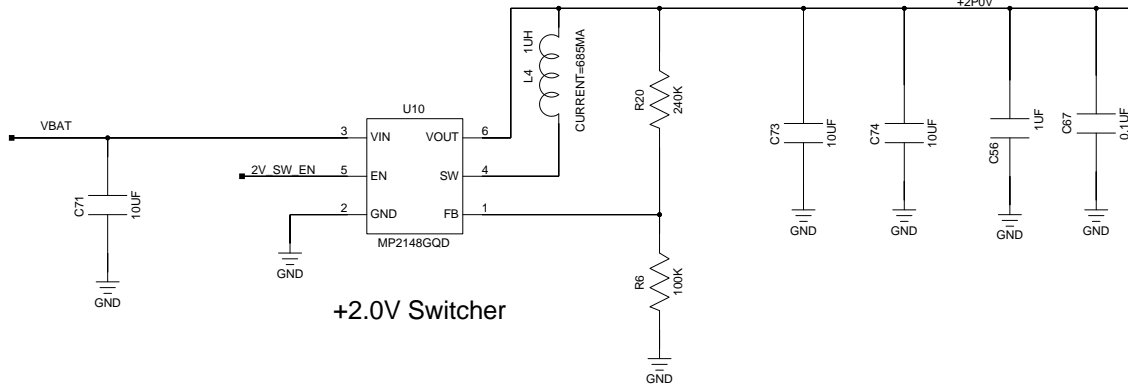
+3.3V Switcher



+3.3V SECTION

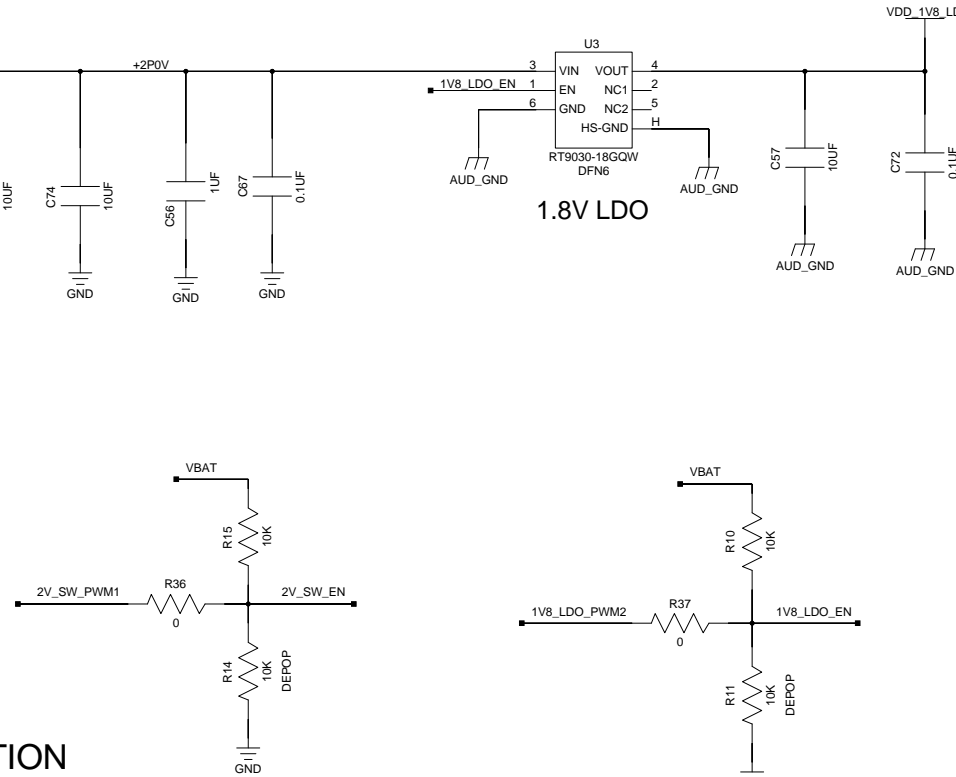


+2.0V Switcher



1.8V LDO

+1.8V SECTION

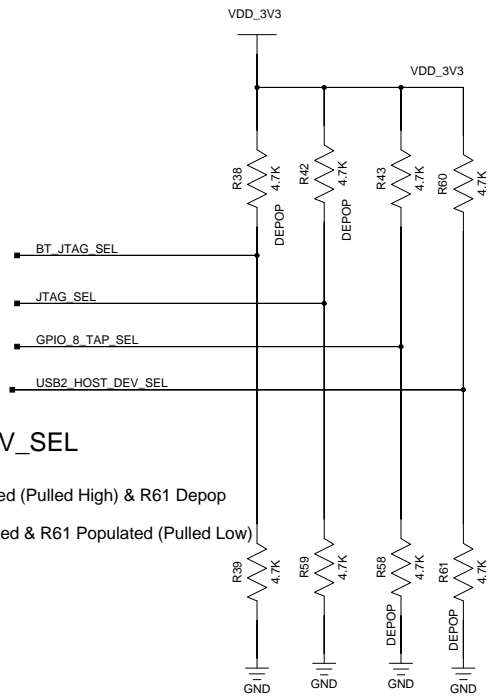


Power Supplies

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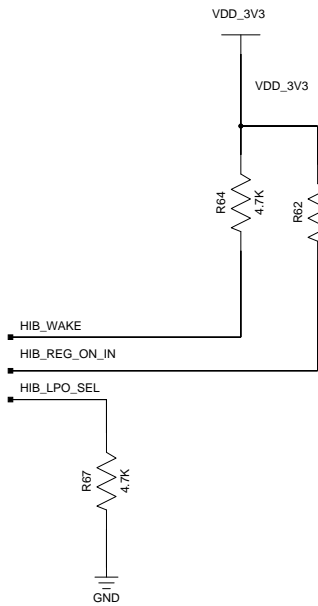
SIZE	TITLE	DATE	SHEET	2	OF	6
B	BCM943907WAE_1	21/05/2015:16:47				



USB2_HOST_DEV_SEL

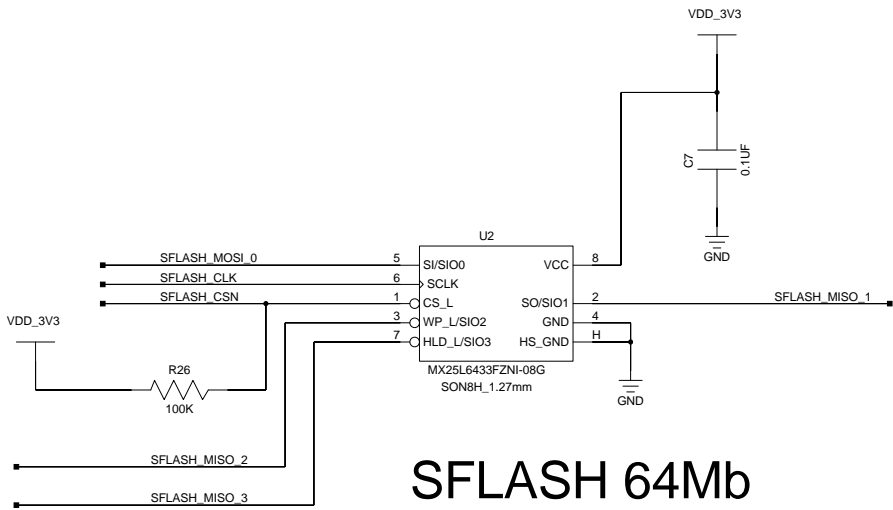
USB Device : R60 Populated (Pulled High) & R61 Depop

USB Host : R60 Depopped & R61 Populated (Pulled Low)

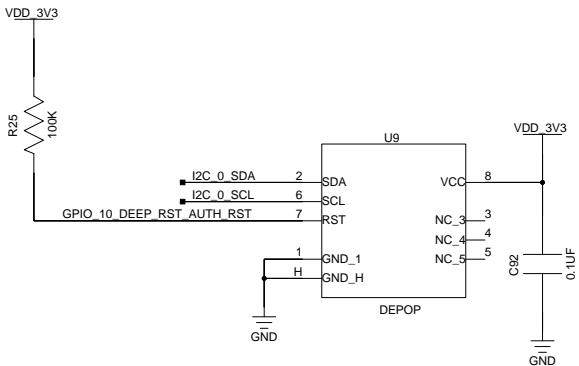
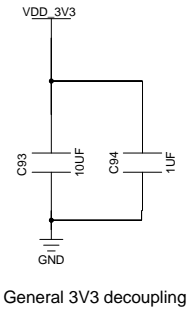


HIBERNATION STRAPPING

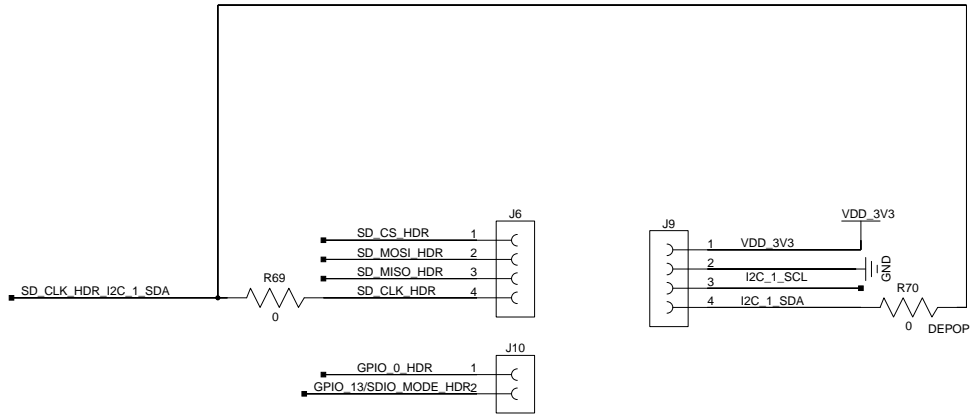
Pin	Strap Function	Strap Pull	
		Chip Default	Board Default
GPIO_1	gSPI Mode	0	
GPIO_7	WCPU Boot Mode : 0 = TCROM Boot 1 = TCMSRAM Boot	0	1 REFER TO AUDIO SWITCHES ARRAY
GPIO_9	USB/HSIC Sel : 0 = HSIC PHY 1 = USB PHY	0	1 REFER TO AUDIO SWITCHES ARRAY
GPIO_11	ACPU Boot Mode : 0 = SOCROM Boot 1 = SOCSRAM Boot	0	
GPIO_13	SDIO Mode : 0 = SDIO Device 1 = SDIO Host	0	
RF_SW_CTRL_5	Host DAP Clock Sel	0	
RF_SW_CTRL_7	Host RSRC Init	0	



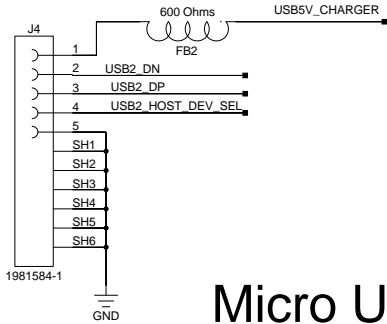
SFLASH 64Mb



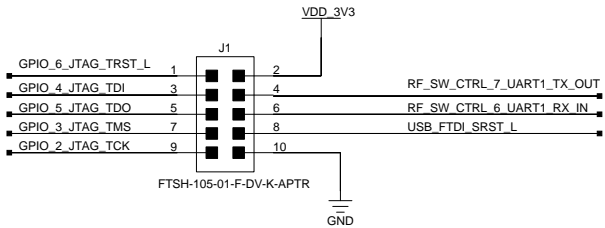
AUTH CHIP



Display 2.54mm Header



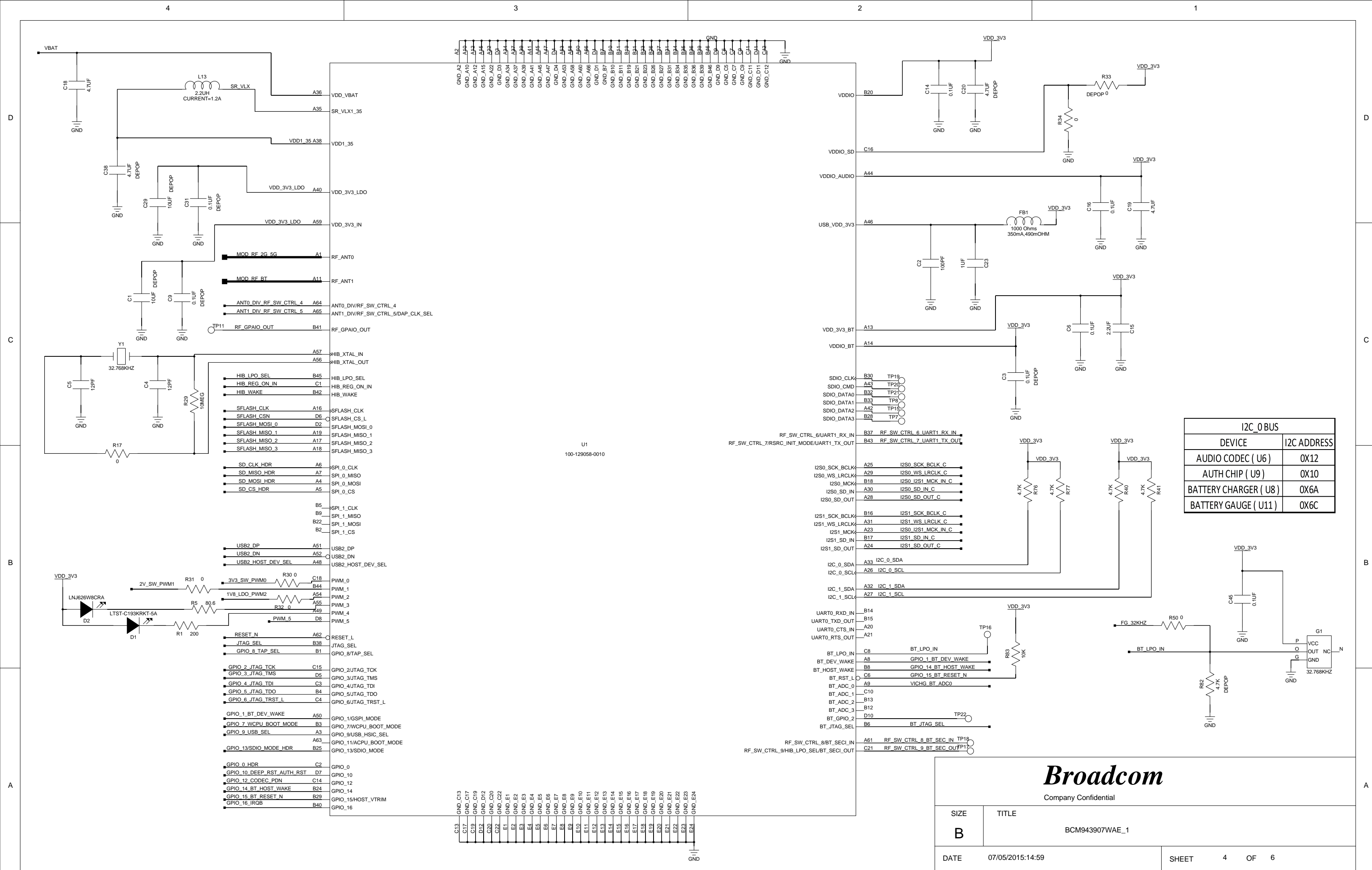
Micro USB

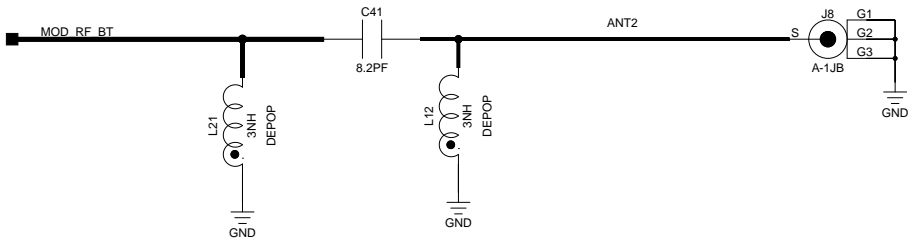
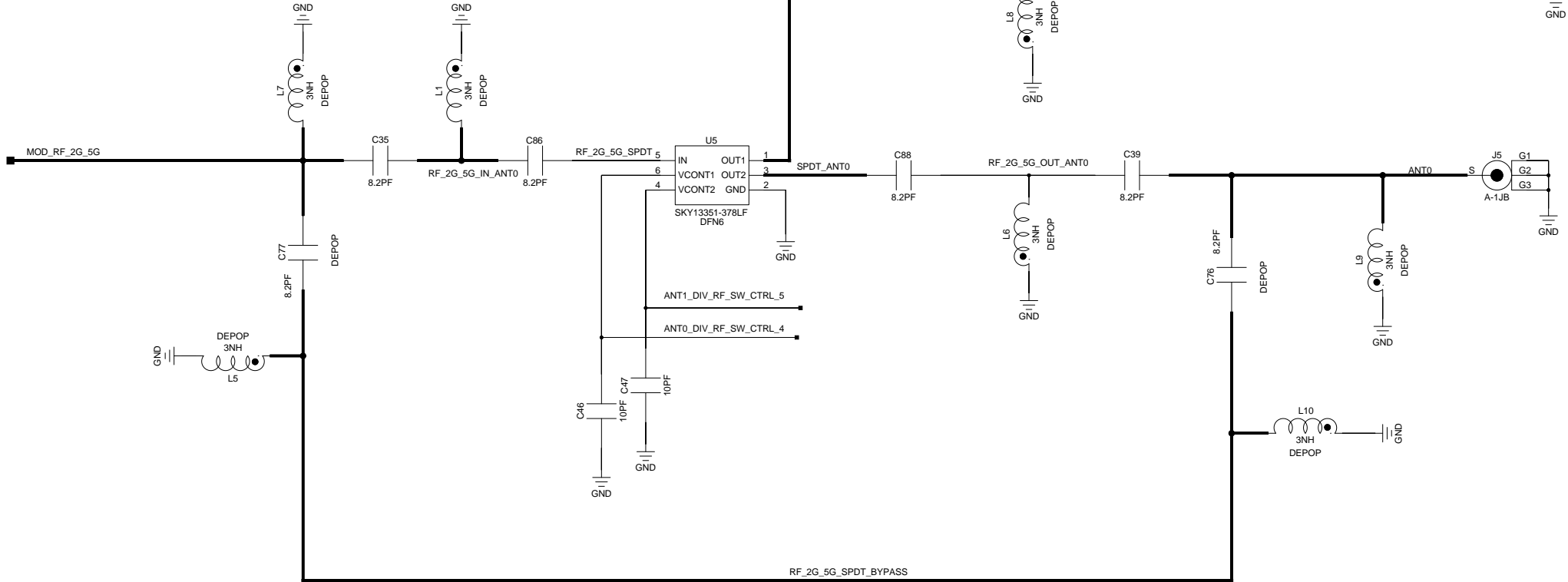


JTAG/UART1 Micro Header

Straps + SFLASH + MISC

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SIZE B	TITLE BCM943907WAE_1		
DATE	21/05/2015:16:36	SHEET	3 OF 6



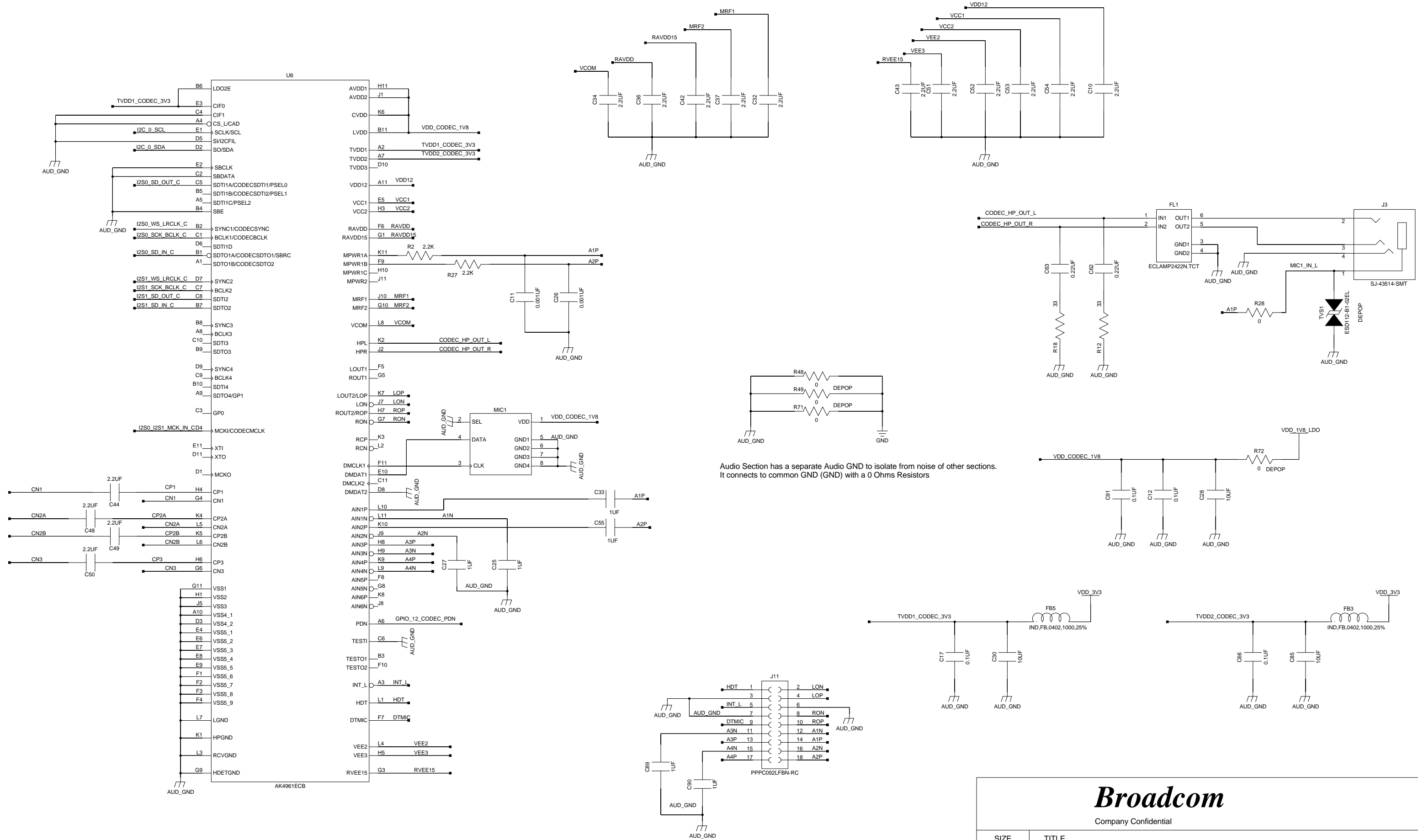


ANTENNA DIVERSITY

U5 2G/5G SPDT Antenna Diversity Switch Control Table		
MODE OF OPERATION Ant Diversity	ANT0_DIV_RF_SW_CTRL_4 SPDT (U5 VCONT1)	ANT1_DIV_RF_SW_CTRL_5 DPDT (U5 VCONT2)
RF_2G_5G to Ant0	1	0
RF_2G_5G to Ant1	0	1

ALL OTHER STATES UNDEFINED

AUDIO CODEC



Audio Section has a separate Audio GND to isolate from noise of other sections. It connects to common GND (GND) with a 0 Ohms Resistor

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SIZE	TITLE	DATE	SHEET	OF
B	BCM943907WAE_1	21/05/2015:10:31	6	6