



(Interrupts)

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(ELEN 120)

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**Objective:** Throughout this lab, our goal is to analyze and build a program that uses interrupts and a program that uses a timer. This task requires initializing all of the control registers for an interrupt and writing the interrupt service routine for the first 2 sections. This also requires configuring the time hardware and writing the interrupt service routine for the third section.

**Procedure:**

**Problem 1:**

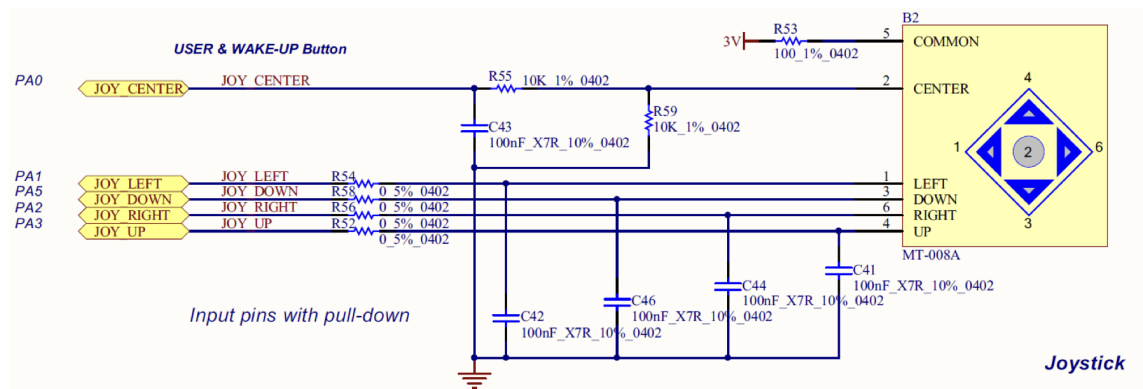
First, we need to create a new project and add it to the ExtInt Files. Then, fill in the enable commands and other necessary GPIO and Interrupt instructions to enable the green LED when the interrupt EXT0(center button) toggles.

**Problem 2:**

Now add the code that toggles the green LED when the EXT5 (down button) is toggled. (This one is more tricky because pins 5-9 share the same interrupt vector and some of the bits are in different registers).

**Problem 3:**

Now we want to manipulate the code so that a 1kHz square wave is displayed on Port B Pin 2. Connect an oscilloscope and measure the output signal.



### Problem 1:

Now, create a new project and add in the ExtInt files. Add additional code to this project so that the green LED toggles when interrupt EXT0 (the center button) is pressed.

### Main.s:

```
12
13     INCLUDE core_cm4_constants.s           ; Load Constant Definitions
14     INCLUDE stm321476xx_constants.s
15     INCLUDE jstick.h
16     INCLUDE leds.h
17
18
19
20
21         AREA    main, CODE, READONLY
22         EXPORT  __main
23         ENTRY   __main
24
25 __main PROC
26     ldr        r0,=RCC_AHB2ENR_GPIOBEN
27     bl         portclock_en                 ; enable port B clock
28
29     ldr        r0,=GPIOB_BASE
30     ldr        r1,=GPIO_MODER_MODER2_0
31     bl         port_bit_pushpull           ;set port B.2 to push pull
32
33     ldr        r0,=RCC_AHB2ENR_GPIOEEN
34     bl         portclock_en                 ; enable port E clock
35
36     ldr        r0,=GPIOE_BASE
37     ldr        r1,=GPIO_MODER_MODER8_0
38     bl         port_bit_pushpull           ;set port E.8 to push pull
39
40     bl         porta_init                   ;initialize port A for this program
41     bl         exti3_init                   ;initialize exti3 interrupt
42     bl         exti0_init                   ;initialize exti0 interrupt
43
44
```

```

44
45
46
47
48 endless b      endless
49     ENDP
50
51
52 EXTI3_IRQHandler PROC
53     EXPORT EXTI3_IRQHandler
54     push    {lr}
55     bl      red_tog
56     pop     {lr}
57     ldr     r2,=(EXTI_BASE+EXTI_PR1)    ;reset pending interrupt for EXTI3
58     mov     r1,#EXTI_PR1_PIF3
59     str     r1,[r2]
60     dsb
61     bx      lr
62     ENDP
63
64 EXTI0_IRQHandler PROC
65     EXPORT EXTI0_IRQHandler
66     push    {lr}
67     bl      green_tog
68     pop     {lr}
69     ldr     r2,=(EXTI_BASE+EXTI_PR1)    ;reset pending interrupt for EXTI0
70     mov     r1,#EXTI_PR1_PIF0
71     str     r1,[r2]
72     dsb
73     bx      lr
74     ENDP
75

```

## Jstick.s New code:

```
69 exti0_init PROC ;initialize the external interrupt detector for PA.0
70 EXPORT exti0_init
71     ldr     r2,=(RCC_BASE+RCC_APB2ENR) ;enable SYSCFG block clock
72     ldr     r1,[r2]
73     orr     r1,#RCC_APB2ENR_SYSCFGEN
74     str     r1,[r2]
75     ldr     r2,=(SYSCFG_BASE+SYSCFG_EXTICR0) ;select PA.3 and the trigger for EXTI0
76     ldr     r1,[r2]
77     bic     r1,#0x00007000 ;This is the default anyway
78     str     r1,[r2]
79     ldr     r2,=(EXTI_BASE+EXTI_RTSR1) ;enable rising edge trigger for EXTI0
80     ldr     r1,[r2]
81     orr     r1,#EXTI_RTSR1_RT0
82     str     r1,[r2]
83     ldr     r2,=(EXTI_BASE+EXTI_FTSR1) ;disable falling edge trigger for EXTI0
84     ldr     r1,[r2]
85     bic     r1,#EXTI_FTSR1_FT0 ;also the default
86     str     r1,[r2]
87     ldr     r2,=(EXTI_BASE+EXTI_IMR1) ;enable EXTI0 interrupt (unmask)
88     ldr     r1,[r2]
89     orr     r1,#EXTI_IMR1_IM0
90     str     r1,[r2]
91     ldr     r2,=(NVIC_BASE+NVIC_ISER0) ;enable the EXTI0 interrupt in NVIC_ISER0
92     ldr     r1,=(1<<4)
93     str     r1,[r2]
94     bx      lr
95 ENDP
96 ALIGN
97
98 END
99
```

## Problem 2:

Now, **Copy your last project** and modify it so that the green LED toggles when interrupt EXT5 (the down button) is pressed *instead* of the center button. This is a little bit harder since pins 5-9 share one interrupt vector and some of the bits are in different registers. When the interrupt is triggered, you need to make sure it came from pin 5 and not pins 6-9.

## Main.s:

```
12
13     INCLUDE core_cm4_constants.s          ; Load Constant Definitions
14     INCLUDE stm32l476xx_constants.s
15     INCLUDE jstick.h
16     INCLUDE leds.h
17
18
19
20
21     AREA    main, CODE, READONLY
22     EXPORT  __main
23     ENTRY   __main
24
25 __main    PROC
26     ldr     r0,=RCC_AHB2ENR_GPIOBEN
27     bl      portclock_en                  ; enable port B clock
28
29     ldr     r0,=GPIOB_BASE
30     ldr     r1,=GPIO_MODER_MODER2_0
31     bl      port_bit_pushpull            ;set port B.2 to push pull
32
33     ldr     r0,=RCC_AHB2ENR_GPIOEEN
34     bl      portclock_en                  ; enable port E clock
35
36     ldr     r0,=GPIOE_BASE
37     ldr     r1,=GPIO_MODER_MODER8_0
38     bl      port_bit_pushpull            ;set port E.8 to push pull
39
40     bl      porta_init                    ;initialize port A for this program
41     bl      exti3_init                    ;initialize exti3 interrupt
42     bl      exti5_init                    ;initialize exti0 interrupt
43
44
45
46
47
48     endless b      endless
49     ENDP
50
```

```

51
52 EXTI3_IRQHandler PROC
53     EXPORT  EXTI3_IRQHandler
54     push    {lr}
55     bl      red_tog
56     pop     {lr}
57     ldr     r2,=(EXTI_BASE+EXTI_PR1)    ;reset pending interrupt for EXTI3
58     mov     r1,#EXTI_PR1_PIF3
59     str     r1,[r2]
60     dsb
61     bx      lr
62     ENDP
63
64 EXTI9_5_IRQHandler PROC
65     EXPORT  EXTI9_5_IRQHandler
66
67     push    {lr}|
68     ldr     r2,=(EXTI_BASE+EXTI_PR1)    ;reset pending interrupt for EXTI5
69     LDR     r1,[r2];
70     TST     r1,#EXTI_PR1_PIF5;
71     BEQ     c
72     bl      green_tog
73     pop     {lr}
74     mov     r1,#EXTI_PR1_PIF5
75     str     r1,[r2]
76     dsb
77 c      bx      lr
78     ENDP
79
80
81
82
83     ALIGN
84     AREA    myData, DATA, READWRITE
85
86     ALIGN
87
88
89     END

```

### Jstick.s new code:

```
68
69 exti5_init PROC           ;initialize the external interrupt detector for PA.5
70     EXPORT exti5_init
71     ldr    r2,=(RCC_BASE+RCC_APB2ENR)      ;enable SYSCFG block clock
72     ldr    r1,[r2]
73     orr    r1,#RCC_APB2ENR_SYSCFGEN
74     str    r1,[r2]
75     ldr    r2,=(SYSCFG_BASE+SYSCFG_EXTICR0)  ;select PA.5 and the trigger for EXTI5
76     ldr    r1,[r2]
77     bic    r1,#0x00007000                  ;This is the default anyway
78     str    r1,[r2]
79     ldr    r2,=(EXTI_BASE+EXTI_RTSR1)        ;enable rising edge trigger for EXTI5
80     ldr    r1,[r2]
81     orr    r1,#EXTI_RTSR1_RT5
82     str    r1,[r2]
83     ldr    r2,=(EXTI_BASE+EXTI_FTSR1)        ;disable falling edge trigger for EXTI5
84     ldr    r1,[r2]
85     bic    r1,#EXTI_FTSR1_FT5              ;also the default
86     str    r1,[r2]
87     ldr    r2,=(EXTI_BASE+EXTI_IMR1)        ;enable EXTI0 interrupt (unmask)
88     ldr    r1,[r2]
89     orr    r1,#EXTI_IMR1_IM5
90     str    r1,[r2]
91     ldr    r2,=(NVIC_BASE+NVIC_ISER0)      ;enable the EXTI0 interrupt in NVIC_ISER0
92     ldr    r1,=(1<<23)
93     str    r1,[r2]
94     bx     lr
95     ENDP
96     ALIGN
97
98     END
99
```

In order to make sure that we are only receiving an input from Pin 5 and not 6-9, we create a mask to clear any of the incoming bits that come from the pin 5-9, effectively leaving only the input of pin 5 alone. Then the program can act on the input of pin 5.

### Problem 3:

Copy and run my project called “timer”. Describe what it does in your lab report. Now modify it so that it generates a 1KHz square wave on the Port B Pin 2 output.

Now –connect the output to an oscilloscope to view the output signal. Connect that pin to the oscilloscope and demo the square wave. (you need to connect the scope to the ground as well

This code makes the red led blink once a second. It also generates a square wave out of Pin 2.



```

2      INCLUDE core_cm4_constants.s      ; Load Constant Definitions
3      INCLUDE stm321476xx_constants.s
4
5      AREA    main, CODE, READONLY
6
7
8
9      ;Interrupt Support Code
10
11      tim2_init    PROC                ;initialize Timer 2 for this program and setup its interrupt
12      EXPORT tim2_init
13      ldr    r2,=(RCC_BASE+RCC_APB1ENR1)    ;enable timer 2 clock
14      ldr    r1,[r2]
15      orr    r1,#RCC_APB1ENR1_TIM2EN
16      str    r1,[r2]
17
18      ldr    r2,=(TIM2_BASE+TIM_PSC)        ;Setup the prescaler. Assuming a 4MHz clock, this gives lms timer ticks
19      ldr    r1,#999
20      str    r1,[r2]
21
22      ldr    r2,=(TIM2_BASE+TIM_ARR)        ;Setup the reload. Assuming a lms tick, this gives 1khz overflows
23      ldr    r1,#1
24      str    r1,[r2]
25
26      ldr    r2,=(TIM2_BASE+TIM_CR1)        ;enable the counter in control register 1
27      ldr    r1,[r2]
28      orr    r1,#TIM_CR1_CEN
29      str    r1,[r2]
30
31      ldr    r2,=(TIM2_BASE+TIM_DIER)        ;enable the timer update interrupt
32      ldr    r1,[r2]
33      orr    r1,#TIM_DIER_UIE
34      str    r1,[r2]
35
36      ldr    r2,=(NVIC_BASE+NVIC_ISER0)    ;enable the TIM2 interrupt in NVIC_ISER0
37      ldr    r1,=(1<<28)
38      str    r1,[r2]
39      bx     lr
40
41      ENDP
42      ALIGN
43
44      END
45

```

