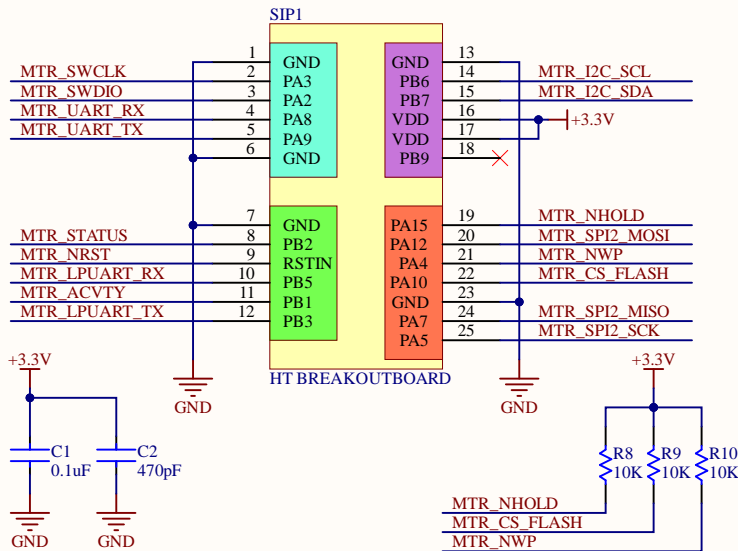
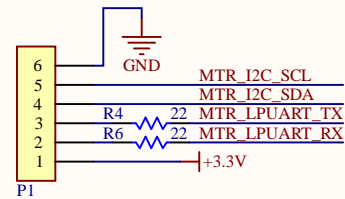


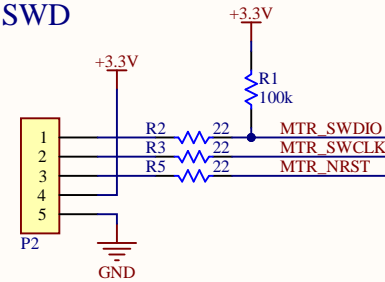
SiP MASTER



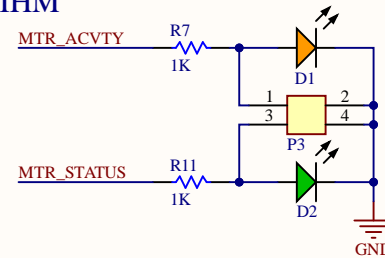
UART1 / I2C



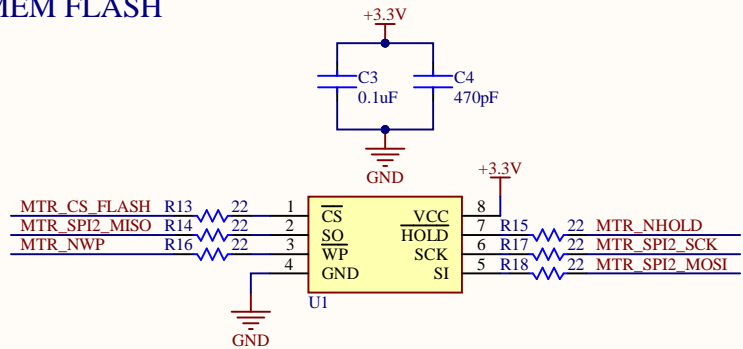
SWD



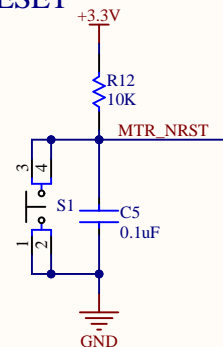
IHM



MEM FLASH



RESET

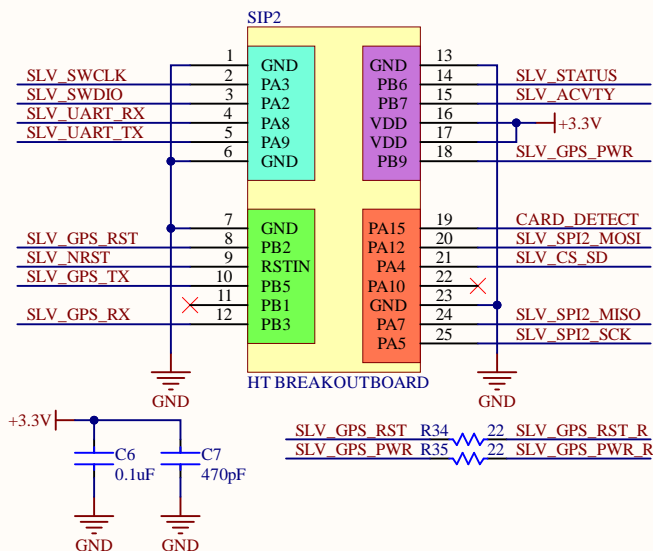


SLV_UART_RX << SLV_UART_RX R19 22 MTR_UART_TX

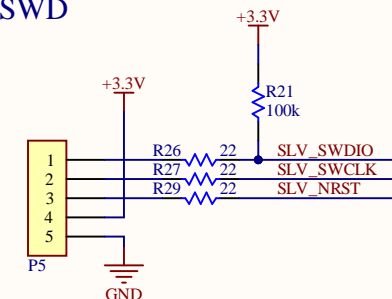
SLV_UART_TX >> SLV_UART_TX R20 22 MTR_UART_RX



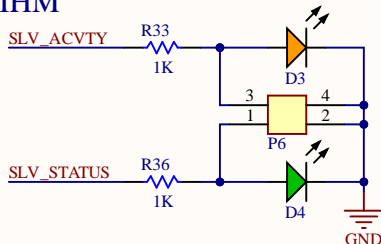
SiP SLAVE



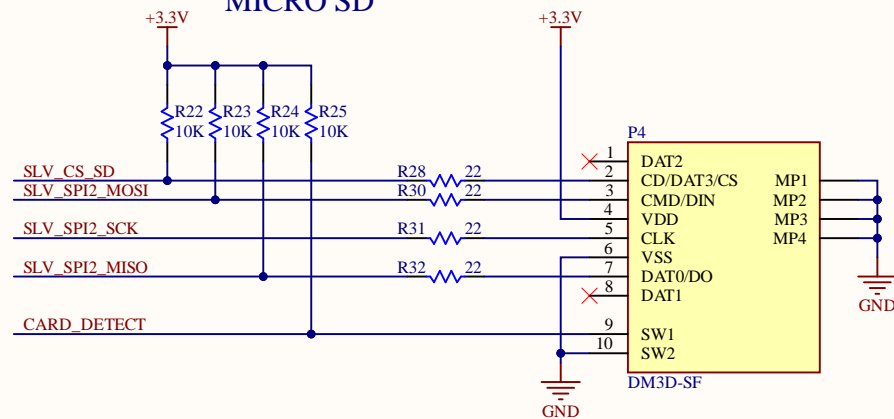
SWD



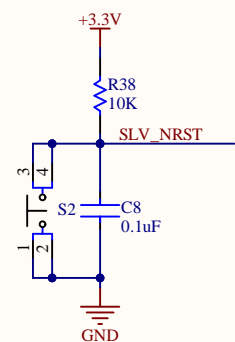
IHM



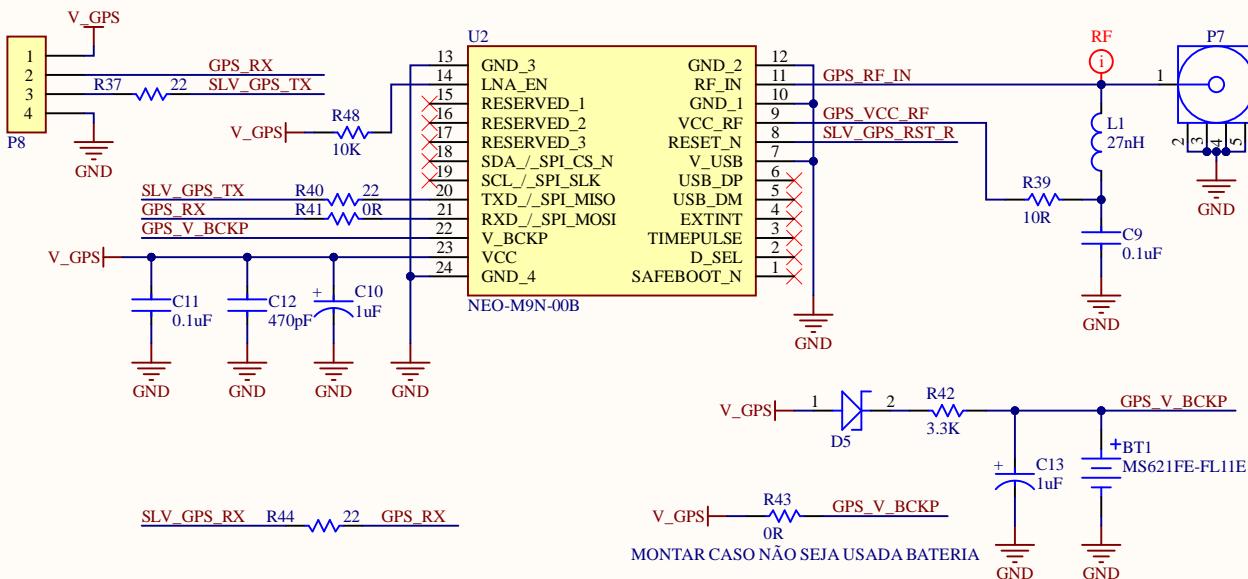
MICRO SD



RESET

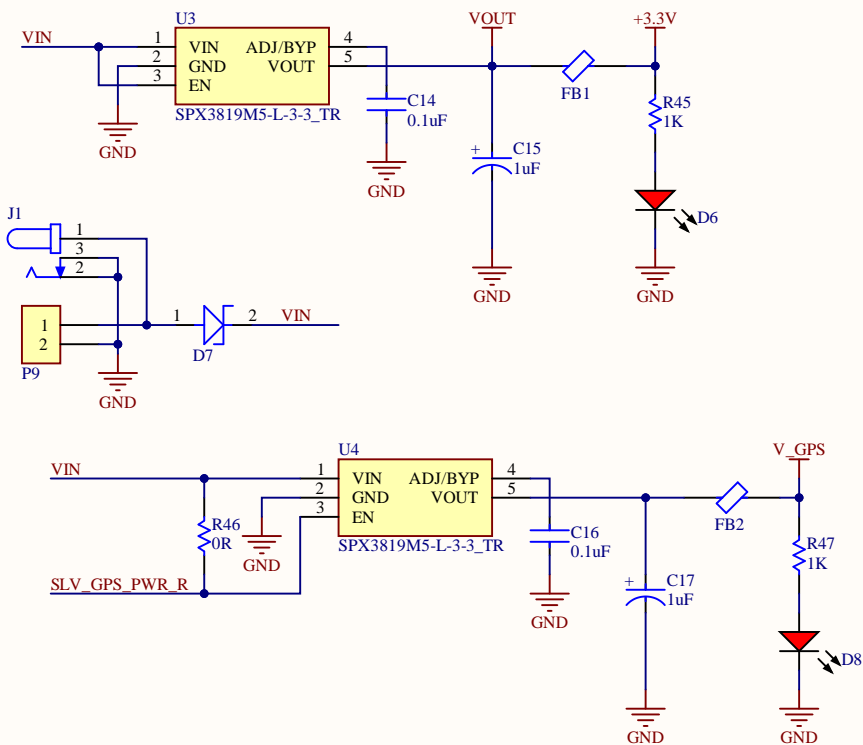


GPS



SLV_UART_TX << SLV_UART_TX
SLV_UART_RX >> SLV_UART_RX
SLV_GPS_PWR >> SLV_GPS_PWR

POWER SUPPLY



SLV_GPS_PWR \Rightarrow SLV_GPS_PWR

+3.3V TP1

GND TP2

V_GPS TP3