NLQ2SLQ	Sub Assignee Est. Start Due Task 1 2019 Tasks Hours Date Date Prog. 16 1	Feb 2019 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 1	Mar 2019	Apr 2019 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 1 2
Inception:	- 17/Jan 08/Feb 100%	Inception:	2	- 5 5 7 5 5 10 11 12 13 14 13 10 17 10 13 20 21 22 23 24 23 20 21 20 23 30 1 2
Project proposal	Unassigned - 17/Jan 23/Jan 100%	Project proposal		
2 understanding the project	Unassigned - 17/Jan 19/Jan 100%	understanding the project		
analysing existing projects	Unassigned - 20/Jan 22/Jan 100%	analysing existing projects		
4 preparing project proposal document	Unassigned - 23/Jan 24/Jan 100%	preparing project proposal document		
5 finalize the document	Unassigned - 25/Jan 25/Jan 100%	finalize the document		
feasibility analysis	Unassigned - 26/Jan 01/Feb 100%	feasibility analysis		
7 feasibility study	Unassigned - 26/Jan 29/Jan 100%	feasibility study		
8 prepare feasibility report	Unassigned - 30/Jan 31/Jan 100%	prepare feasibility report		
9 finalize report	Unassigned - 01/Feb 01/Feb 100%	finalize report		
project planning and scheduling	Unassigned - 03/Feb 08/Feb 100%	project planning and scheduling		
11 project planning	Unassigned - 03/Feb 05/Feb 100%	project planning		
schedule prepartion	Unassigned - 06/Feb 07/Feb 100%	schedule prepartion		
finalize the schedule	Unassigned - 08/Feb 08/Feb 100%	finalize the schedule		
Elaboration:	- 20/Jan 22/Feb 0%	Elaboration:		
Requirements	Unassigned - 20/Jan 15/Feb 0%	Requirements		
Requirements gathering and analysing	Unassigned - 20/Jan 09/Feb 0%	Requirements gathering and analysing		
17 requirements specification	Unassigned - 10/Feb 11/Feb 0%	requirements specification		
requirements validiation	Unassigned - 11/Feb 12/Feb 0%	requirements validiation		
SRS document preparation	Unassigned - 13/Feb 14/Feb 0%	SRS document preparation		
20 finalise SRS Document	Unassigned - 15/Feb 15/Feb 0%	finalise SRS Document		
Design	Unassigned - 16/Feb 22/Feb 0%	Design		
22 Large scale Architecture Design	Unassigned - 16/Feb 17/Feb 0%	Large scale Architecture Design		
23 Architectural Design	Unassigned - 17/Feb 18/Feb 0%	Architectural Design		
Data ingnestion pipeline Desing	Unassigned - 18/Feb 18/Feb 0%	Data ingnestion pipeline Desing		
25 Model Desing	Unassigned - 19/Feb 19/Feb 0%	Model Desing		
Graphic User interface Design	Unassigned - 20/Feb 20/Feb 0%	Graphic User interface Design		
27 prepare SDS Document	Unassigned - 21/Feb 21/Feb 0%	prepare SDS Document		
finalise the SDS Document	Unassigned - 22/Feb 22/Feb 0%	finalise the SDS Docume		
Construction:	- 23/Feb 01/Apr 100%		Constru	iction:
Iteration 1	Unassigned - 23/Feb 06/Mar 100%		Iteration 1	
Build data ingestion pipeline	Unassigned - 23/Feb 24/Feb 0%	Build data ingestion		
32 Validate quality of data	Unassigned - 25/Feb 26/Feb 0%	Validate q		
33 Implement model design	Unassigned - 27/Feb 03/Mar 0%		Implement model design	
preparation for mid evaluation	Unassigned - 04/Mar 06/Mar 0%		preparation for mid evaluation	
iteration 2	Unassigned - 07/Mar 16/Mar 100%		iteration 2	
model refinement	Unassigned - 07/Mar 09/Mar 0%		model refinement	
perform error analysis and accuracy calculations			perform error analysis and accuracy calculations	
train and test initial model	Unassigned - 12/Mar 12/Mar 0%		train and test initial model	
final model train and test	Unassigned - 13/Mar 14/Mar 0%		final model train and test	
40 evaluate model	Unassigned - 15/Mar 16/Mar 0%		evaluate model	
iteration 3	Unassigned - 16/Mar 01/Apr 100%		iteration	n 3
model evaluation on testing distrubutions	Unassigned - 16/Mar 18/Mar 0%		model evaluation on testing distrubutions	
GUI implemetation	Unassigned - 19/Mar 24/Mar 0%		GUI implemetation	
44 model unit testing 45 GUL unit testing	Unassigned - 25/Mar 27/Mar 0%		model unit testing	
45 GUI unit testing	Unassigned - 28/Mar 29/Mar 0%		GUI unit testing	an testing
46 Integration testing Transistion:	Unassigned - 30/Mar 01/Apr 0%		Integra	tion testing Transiction:
Transistion: 48 System validation and beta testing	- 02/Apr 25/Apr 0% Unassigned - 02/Apr 03/Apr 0%			Transistion:
System validation and beta testing finalise the resting report	Unassigned - 02/Apr 03/Apr 0% Unassigned - 04/Apr 05/Apr 0%		T	System validation and beta testing finalise the resting report
finalise the resting report prepare testing report	Unassigned - 04/Apr 05/Apr 0% Unassigned - 06/Apr 06/Apr 0%			finalise the resting report
50 prepare testing report 51 System deployment	Unassigned - 06/Apr 06/Apr 0% Unassigned - 07/Apr 16/Apr 0%			prepare testing report System deployment
System deployment Prepare final report	Unassigned - 07/Apr 16/Apr 0% Unassigned - 18/Apr 23/Apr 0%			System deployment Prepare final report
52 Prepare final report	Unassigned - 18/Apr 23/Apr 0%			Prepare final report
finalise the final report	Unassigned - 24/Apr 25/Apr 0%			finalise the final report