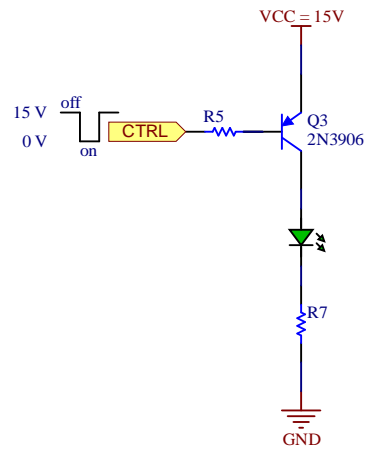
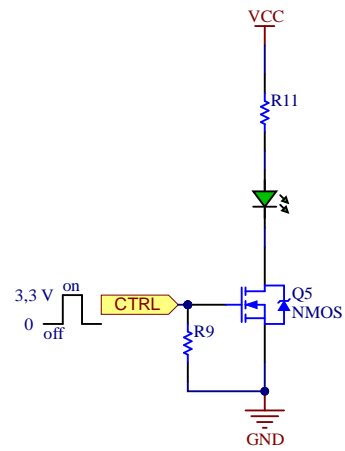


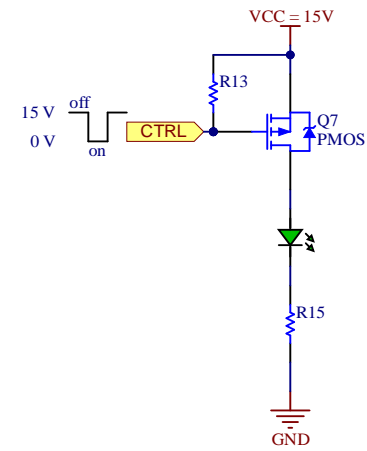
LOW-SIDE NPN



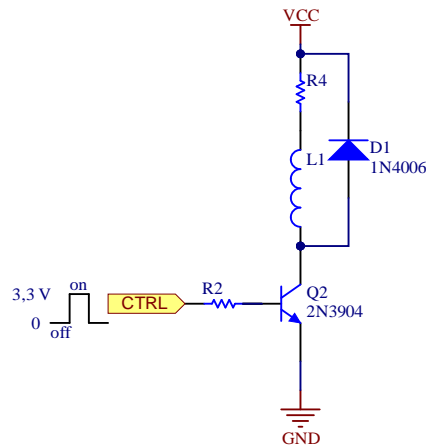
HIGH-SIDE PNP



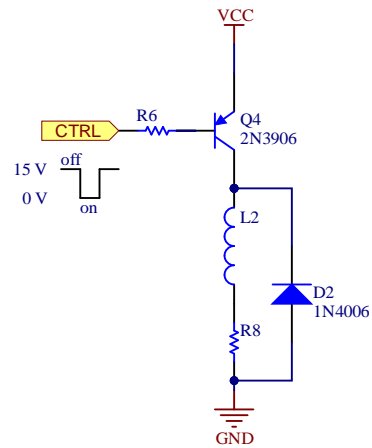
LOW-SIDE N-MOS



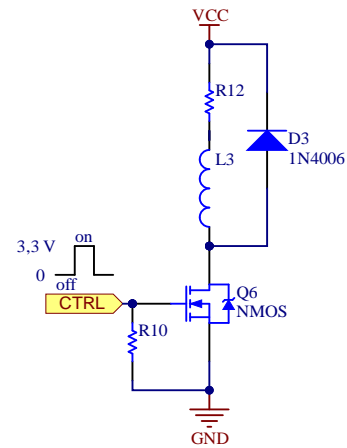
HIGH-SIDE P-MOS



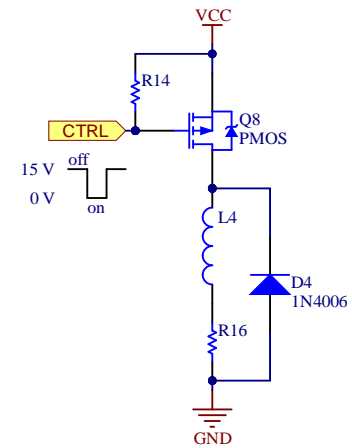
LOW-SIDE NPN



HIGH-SIDE PNP



LOW-SIDE N-MOS



HIGH-SIDE P-MOS

Comment	Description	Designator	Footprint	LibRef	Quantity
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