

Charles V. Wang

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Education

University of Michigan College of Engineering

August 2013 - Present

GPA: 3.95

Expected Graduation: April 2017

Major: B.S.E Computer Science

Minor: Mathematics.

College of Engineering Dean's Honor List: Fall 2013, Winter 2014.

Activities: University of Michigan Men's Glee Club; Hackathons; UROP Research; Programming Team.

Shanghai Jiao Tong University (Joint Institute Program)

May 2014 - August 2014

- Took EECS courses and a philosophy course while being immersed in a Chinese university environment.

Stanford University (Stanford High School Summer College)

June 2011 - August 2011

- Took an electrical engineering design course and introduction course to microeconomics.

Experience

UROP Researcher at the University of Michigan for Asst. Prof. Hui Jiang

September 2013 - April 2014

Designed software that indexes thousands of cancer patients' genomes to quickly determine if a given DNA mutation sequence is contained throughout the genome database (more details on my website).

Lab Research Intern at UC Berkeley under Professor Vivek Subramanian

June 2012 - August 2012

I developed a process that optimized the conductivity of aluminum-doped zinc oxide (ZnO:Al) to be used in biodegradable electrodes and was exposed to advanced research equipment like probe stations, profilometers and even beam lines at SLAC.

Research Intern at Greenberg Traurig in Shanghai

June 2010 - July 2010

- Researched Chinese companies that underwent M&As with American firms to go public in the US.
- Extensively used the SEC's online database to retrieve relevant 8-K forms.

Computer Skills

- C++ and MATLAB: implemented many problem-solving programs.
- C and Verilog: programmed microcontrollers and FPGA boards for projects.
- Java: programmed games & AIs, MP3 and network applications, and other algorithms.
- Android & iPhone App Development: using Java, XML, JSON, PHP, SQL, and an online database.

Projects

More details can be found on my website.

- Built an 8x8x8 LED Cube and programmed an FPGA board in assembly to graph two-variable functions (team of four).
- Built an automated vinyl record cleaner for my senior project (team of two).
- For my Stanford summer engineering design course, I built a portable MP3 player (individual).