Colleen Lau
Tedmon Tran
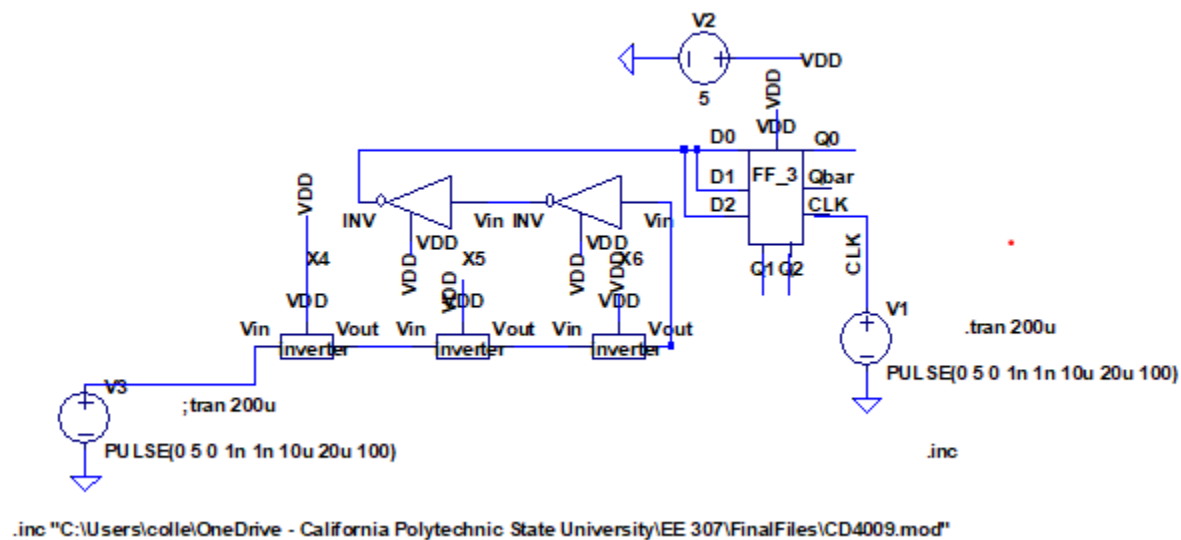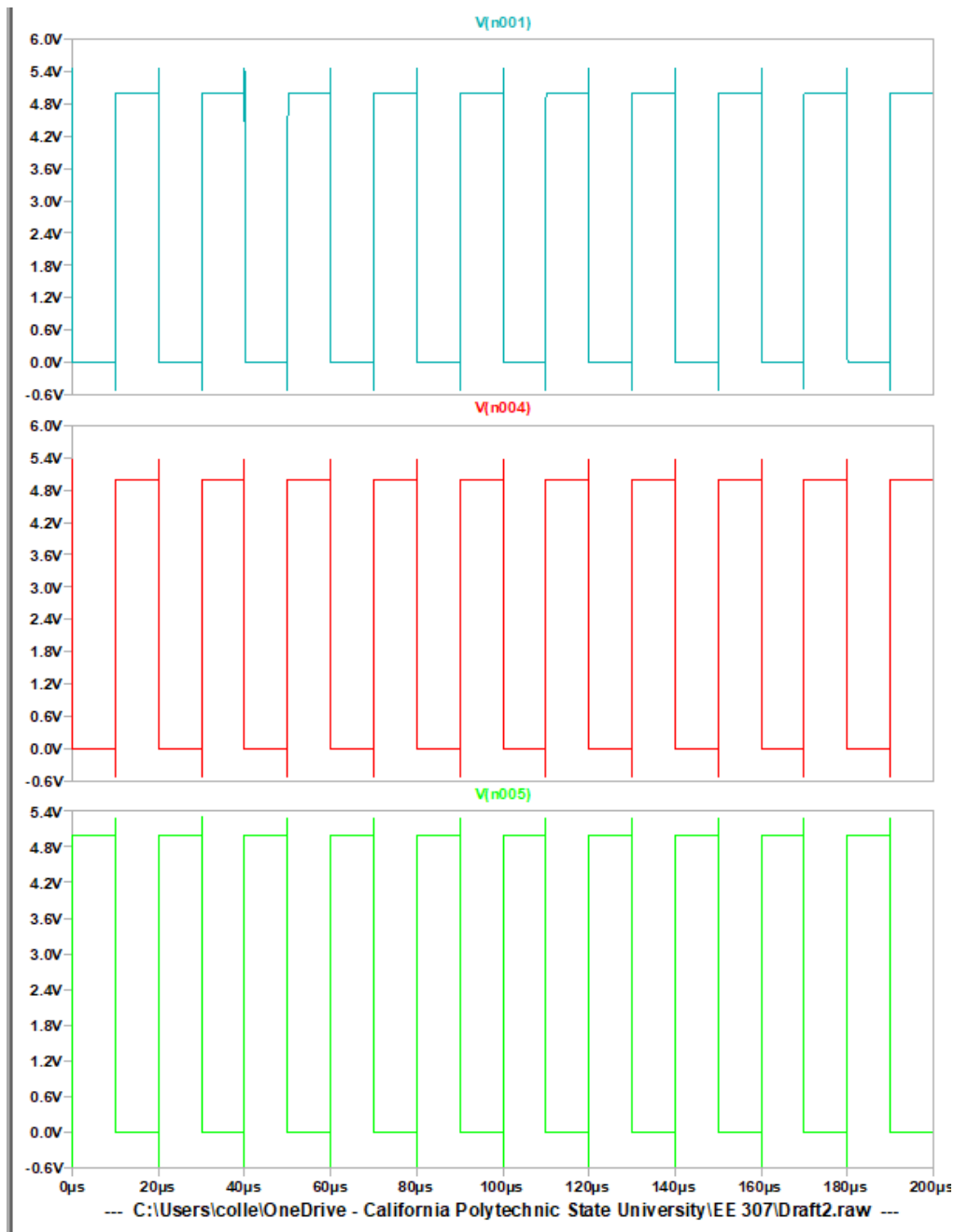EE 307 Smilkstein - HW 8
March 25, 2018

On behalf of our group, we would like to apologize for the timeliness of our homework 8 submission. We appreciate any feedback and commentary for this assignment. We only wish to demonstrate to you that we understand

- the application of D-flip flops to create a multi-address and multi-word register
- the application of AND gates and inverters to create a SRAM memory cell
- the application of NAND gates to create a 2:4 Decoder for the register addresses

and hope to receive any credit if possible. Thank you for your consideration.

.inc "C:\Users\colle\OneDrive - California Polytechnic State University\EE 307\FinalFiles\CD4009.mod"

Register attempted

Colleen Lau
Tedmon Tran
EE 307 Smilkstein - HW 8
March 25, 2018

--- C:\Users\colle\OneDrive - California Polytechnic State University\EE 307\Draft2.raw ---

SRAM to register attempted

Colleen Lau
Tedmon Tran
EE 307 Smilkstein - HW 8
March 25, 2018