

DM7414

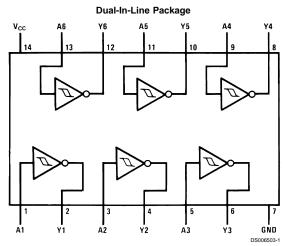
Hex Inverter with Schmitt Trigger Inputs

General Description

This device contains six independent gates each of which performs the logic INVERT function. Each input has hyster-

esis which increases the noise immunity and transforms a slowly changing input signal to a fast changing, jitter free output.

Connection Diagram



Order Number DM5414J, DM5414W or DM7414N See Package Number J14A, N14A or W14B

Function Table

 $Y = \overline{A}$

Input	Output
Α	Y
L	Н
Н	L

H = High Logic Level
L = Low Logic Level

Absolute Maximum Ratings (Note 1)

DM54 DM74 -55°C to +125°C

Supply Voltage Input Voltage

7V 5.5V

Storage Temperature Range

 0°C to $+70^{\circ}\text{C}$ -65°C to +150°C

Operating Free Air Temperature Range

Recommended Operating Conditions

Symbol	Parameter	DM5414		DM7414			Units	
		Min	Nom	Max	Min	Nom	Max	
V _{cc}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V _{T+}	Positive-Going Input	1.5	1.7	2	1.5	1.7	2	V
	Threshold Voltage (Note 2)							
V _{T-}	Negative-Going Input	0.6	0.9	1.1	0.6	0.9	1.1	V
	Threshold Voltage (Note 2)							
HYS	Input Hysteresis (Note 2)	0.4	0.8		0.4	0.8		V
I _{OH}	High Level Output Current			-0.8			-0.8	mA
I _{OL}	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating" Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 3)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = -12 mA				-1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{I} = V_{T_{-}}Min$		2.4	3.4		V
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{I} = V_{T*}Max$			0.2	0.4	V
I _{T+}	Input Current at Positive-Going Threshold	$V_{CC} = 5V$, $V_I = V_{T+}$			-0.43		mA
I _{T-}	Input Current at Negative-Going Threshold	$V_{CC} = 5V$, $V_I = V_{T-}$			-0.56		mA
l _l	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 2.4V				40	μA
I _{IL}	Low Level Input Current	V _{CC} = Max, V _I = 0.4V				-1.2	mA
I _{os}	Short Circuit	V _{CC} = Max	DM54	-18		-55	mA
	Output Current	(Note 4)	DM74	-18		-55	1
I _{CCH}	Supply Current with Outputs High	V _{CC} = Max			22	36	mA
I _{CCL}	Supply Current with Outputs Low	V _{CC} = Max			39	60	mA

Note 2: $V_{CC} = 5V$

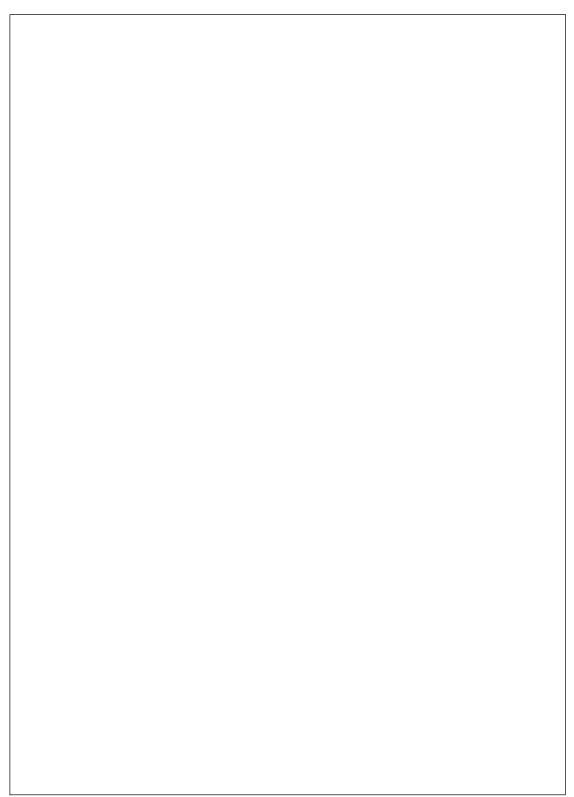
Note 3: All typicals are at V_{CC} = 5V, T_A = 25°C.

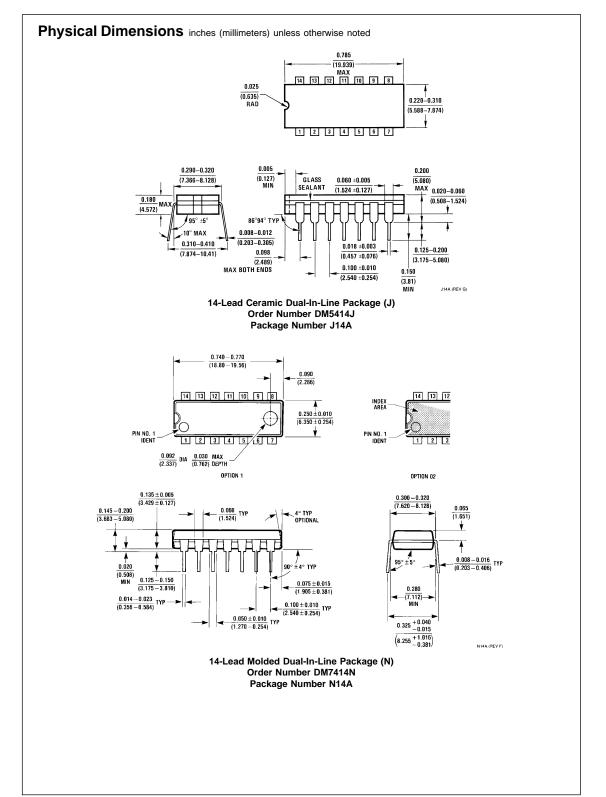
Note 4: Not more than one output should be shorted at a time.

Switching Characteristics at V_{CC} = 5V and T_A = 25°C (for Test Waveforms and Output Load)

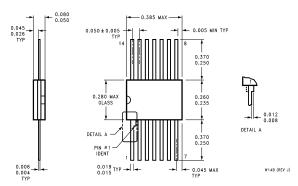
Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	C _L = 15 pF		22	ns
	Low to High Level Output	$R_L = 400\Omega$			
t _{PHL}	Propagation Delay Time			22	ns
	High to Low Level Output				

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Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



14-Lead Ceramic Flat Package (W) Order Number DM5414W Package Number W14B

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