OpenMP

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Contents

- Overview
- 2 parallel pragma
- Work sharing constructs
 - loops (for)
 - scheduling
 - task parallelism (task and taskwait)
- 4 Data sharing clauses
- 5 SIMD constructs

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Goal

- learn OpenMP, by far the most widespread standard API for shared memory parallel programming
- learn that various schedulers execute your parallel programs differently

OpenMP

- de fact standard model for programming shared memory machines
- C/C++/Fortran + parallel directives + APIs
 - by **#pragma** in C/C++
 - by comments in Fortran
- many free/vendor compilers, including GCC

OpenMP reference

- official home page: http://openmp.org/
- specification: http://openmp.org/wp/openmp-specifications/
- latest version is 4.5 (http://www.openmp.org/mp-documents/openmp-4.5.pdf)
- section numbers below refer to those in OpenMP spec 4.0 (http:
 - //www.openmp.org/mp-documents/OpenMP4.0.0.pdf)

GCC and OpenMP

- http://gcc.gnu.org/wiki/openmp
- gcc $4.2 \rightarrow \text{OpenMP spec } 2.5$
- gcc $4.4 \rightarrow \text{OpenMP spec } 3.0 \text{ (task parallelism)}$
- gcc $4.7 \rightarrow \text{OpenMP spec } 3.1$
- gcc $4.9 \rightarrow \text{OpenMP spec } 4.0 \text{ (SIMD)}$

Compiling/running OpenMP programs with GCC

• compile with -fopenmp

```
1 $ gcc -Wall -fopenmp program.c
```

 run the executable specifying the number of threads with OMP_NUM_THREADS environment variable

```
1 $ OMP_NUM_THREADS=1 ./a.out # use 1 thread
$ OMP_NUM_THREADS=4 ./a.out # use 4 threads
```

• see 2.5.1 "Determining the Number of Threads for a parallel Region" for other ways to control the number of threads

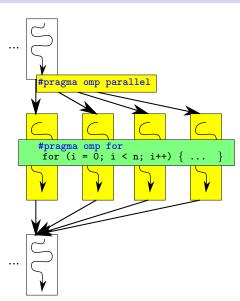
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Two pragmas you must know first

- #pragma omp parallel to launch a team of threads (2.5)
- then #pragma omp for to distribute iterations to threads (2.7.1)

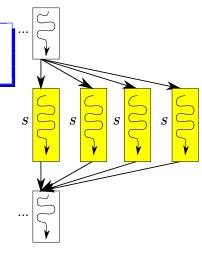
Note: all OpenMP pragmas have the common format: #pragma omp ...



#pragma parallel

• basic syntax:

- basic semantics:
 - create a team of OMP_NUM_THREADS threads
 - the current thread becomes the *master* of the team
 - S will be executed by each member of the team
 - the master thread waits for all to finish S and continue



parallel pragma example

```
#include <stdio.h>
int main() {
   printf("hello\n");

#pragma omp parallel
   printf("world\n");
   return 0;
}
```

```
1 $ OMP_NUM_THREADS=1 ./a.out
2 hello
3 world
4 $ OMP_NUM_THREADS=4 ./a.out
5 hello
6 world
7 world
8 world
9 world
```

Remarks: what does parallel do?

- you may assume an OpenMP thread \approx OS-supported thread (e.g., Pthread)
- that is, if you write this program

```
int main() {
    #pragma omp parallel
    worker();
}
```

and run it as follows,

```
1 $ OMP_NUM_THREADS=50 ./a.out
```

you will get 50 OS-level threads, each doing worker()

How to distribute work among threads?

- #pragma omp parallel creates threads, all executing the same statement
- it's not a means to parallelize work, per se, but just a means to create a number of similar threads (SPMD)
- so how to distribute (or partition) work among them?
 - do it yourself
 - 2 use work sharing constructs

Do it yourself: functions to get the number/id of threads

- omp_get_num_threads() (3.2.2): the number of threads in the current team
- omp_get_thread_num() (3.2.4): the current thread's id (0, 1, ...) in the team
- they are primitives with which you may partition work yourself by whichever ways you prefer
- e.g.,

```
#pragma omp parallel

f

int t = omp_get_thread_num();

int nt = omp_get_num_threads();

/* divide n iterations evenly amont nt threads */

for (i = t * n / nt; i < (t + 1) * n / nt; i++) {

...

}

}

</pre>
```

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Work sharing constructs

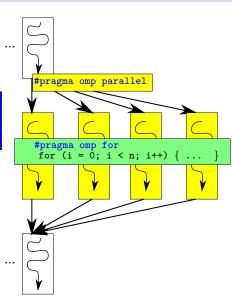
- in theory, parallel construct is all you need to do things in parallel
- but it's too inconvenient
- \bullet OpenMP defines ways to partition work among threads $(work\ sharing\ constructs)$
 - for
 - task
 - section

#pragma omp for (work-sharing for)

basic syntax:

```
#pragma omp for
for(i=...; i...; i+=...){
    S
}
```

- basic semantics: the threads in the team divde the iterations among them
- but how? \Rightarrow scheduling



#pragma omp for restrictions

- not arbitrary for statement is allowed after a for pragma
- strong syntactic restrictions apply, so that the iteration counts can easily be identified at the beginning of the loop
- roughly, it must be of the form:

```
#pragma omp for
for(i = init; i < limit; i += incr)
S</pre>
```

except < and += may be other similar operators

• init, limit, and incr must be loop invariant

Contents

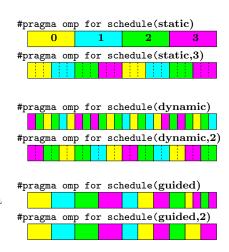
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Scheduling (2.7.1)

- schedule clause in work-sharing for loop determines how iterations are divided among threads
- There are three alternatives (static, dynamic, and guided)

static, dynamic, and guided

- schedule(static[,chunk]): predictable round-robin
- schedule(dynamic[,chunk]): each thread repeats fetching chunk iterations
- schedule(guided[,chunk]): threads grab many iterations in early stages; gradually reduce iterations to fetch at a time
- *chunk* specifies the minimum granularity (iteration counts)



Other scheduling options and notes

• schedule(runtime) determines the schedule by OMP_SCHEDULE environment variable. e.g.,

```
$ OMP_SCHEDULE=dynamic,2 ./a.out
```

• schedule (auto) or no schedule clause choose an implementation dependent default

Parallelizing loop nests by collapse

• collapse(l) can be used to partition nested loops. e.g.,

```
#pragma omp for collapse(2)
for (i = 0; i < n; i++)
for (j = 0; j < n; j++)
S</pre>
```

will partition n^2 iterations of the doubly-nested loop

- schedule clause applies to nested loops as if the nested loop is an equivalent flat loop
- restriction: the loop must be "perfectly nested" (the iteration space must be a rectangular and no intervening statement between different levels of the nest)

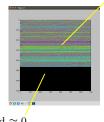
Visualizing schedulers

- seeing is believing. let's visualize how loops are distributed among threads
- write a simple doubly nested loop and run it under various scheduling options

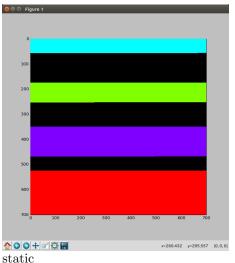
```
#pragma omp for collapse(2) schedule(runtime)
for (i = 0; i < 1000; i++)
for (j = 0; j < 1000; j++)
unit_work(i, j);</pre>
```

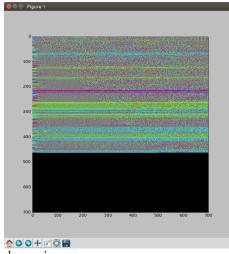
- load per point is systematically skewed:
 - ≈ 0 in the lower triangle
 - drawn from [100, 10000] (clocks) in the upper triangle

load $\sim [100, 10000]$ clocks



Visualizing schedulers





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Task parallelism in OpenMP

- OpenMP's initial focus was simple parallel loops
- since 3.0, it supports task parallelism
- but why it's necessary?
- aren't parallel and for all we need?

Limitation of parallel for

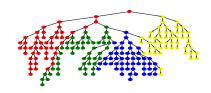
• what if you have a parallel loop inside another

```
for ( ... ) {
    ...
    for ( ...) ...
}
```

• perhaps inside a separate function?

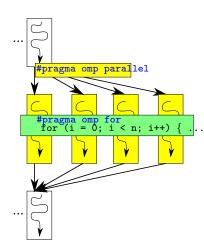
```
main() {
   for ( ... ) {
      ...
      g();
   }
   }
   g() {
   for (...) ...
   }
```

• what for parallel recursions?



parallel for can't handle nested parallelism

- OpenMP generally ignores nested parallel pragma when enough threads have been created by the outer parallel pragma, for good reasons
- the fundamental limitation is its simplistic work-sharing mechanism
- tasks address these issues, by allowing tasks to be created at arbitrary points of execution (and a mechanism to distribute them across cores)



Task parallelism in OpenMP

- syntax:
 - create a task ≈ TBB's task_group::run

```
1 \frac{1}{2} #pragma omp task \frac{1}{S}
```

wait for tasks ≈ TBB's task_group::wait

```
#pragma omp taskwait
```

OpenMP task parallelism template

- don't forget to create a parallel region
- don't also forget to enter a master region, which says only the master executes the following statement and others "stand-by"

```
int main() {
    #pragma omp parallel
    #pragma omp master
    // or #pragma omp single
    ms(a, a + n, t, 0);
}
```

• and create tasks in the master region

```
void ms(a, a_end, t, dest) {
   if (n == 1) {
      ...
} else {
      ...

   #pragma omp task
   ms(a, c, t, 1 - dest);

   #pragma omp task
   ms(c, a_end, t + nh, 1 - dest);

   #pragma omp taskwait
   ...
}
```

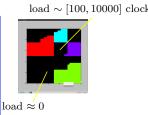
Visualizing task parallel schedulers

• the workload is exactly the same as before

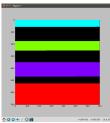
```
#pragma omp for collapse(2) schedule(runtime)
for (i = 0; i < 1000; i++)
for (j = 0; j < 1000; j++)
unit_work(i, j);</pre>
```

• but we rewrite it into recursions

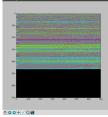
```
void work_rec(rectangle b) {
      if (small(b)) {
      } else {
        rectangle c[2][2];
        split(b, c); // split b into 2x2 sub-rectangles
        for (i = 0; i < 2; i++) {
7
          for (i = 0; i < 2; i++) {
9
   #pragma omp task
            work_rec(b[i][j]);
10
11
12
13
   #pragma omp taskwait
```



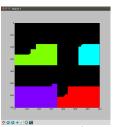
Visualizing schedulers



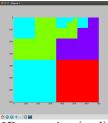
static



dynamic



2D recursive (midway)



2D recursive (end)

A note on current GCC task implementation

- the overhead seems high and the scalability seems low, so it's not very useful now
- TBB, Cilk, and MassiveThreads-backed TBB are all much better
- Intel implementation of OpenMP tasks is good too
- we'll come back to the topic of efficient implementation of task parallelism later

Pros/cons of schedulers

• static:

- partitioning iterations is simple and does not require communication
- may cause load imbalance (leave some threads idle, even when other threads have many work to do)
- mapping between work ↔ thread is deterministic and predictable (why it's important?)

• dynamic:

- no worry about load imbalance, if chunks are sufficiently small
- partitioning iterations needs communication (no two threads execute the same iteration) and may become a bottleneck
- mapping between iterations and threads is non-deterministic
- OpenMP's dynamic scheduler is inflexible in partitioning loop nests

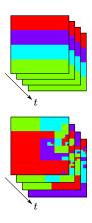
Pros/cons of schedulers

• recursive (divide and conquer + tasks):

- no worry about load imbalance, as in dynamic
- distributing tasks needs communication, but efficient implementation techniques are known
- mapping between work and thread is non-deterministic, as in dynamic
- you can flexibly partition loop nests in various ways (e.g., keep the space to square-like)
- need boilerplate coding efforts (easily circumvented by additional libraries; e.g., TBB's blocked_range2d and parallel_for)

Deterministic and predictable schedulers

- programs often execute the same for loops many times, with the same trip counts, and with the same iteration touching a similar region
- such *iterative* applications may benefit from reusing data brought into cache in the previous execution of the same loop
- a deterministic scheduler achieves this benefit



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Data sharing

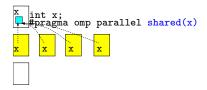
- parallel, for, task pragma accept clauses specifying which variables should be shared among threads or between the parent/child tasks
- 2.14 "Data Environments"
 - private
 - firstprivate
 - shared
 - reduction (only for parallel and for)
 - copyin

Data sharing example

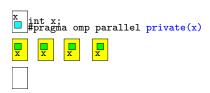
```
$ OMP_NUM_THREADS=2 ./a.out
S at 0x..777f494, P at 0x..80d0e28, L at 0x..80d0e2c
S at 0x..777f494, P at 0x..777f468, L at 0x..777f46c
```

Data sharing behavior

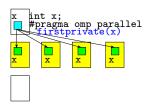
shared



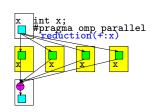
private



firstprivate



reduction



Reduction

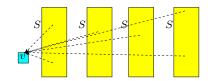
• in general, "reduction" refers to an operation to combine many walues into a single value. e.g., 4

```
• v = v_1 + \dots + v_n
• v = \max(v_1, \dots, v_n)
• \dots
```

- simply sharing the variable (v) does not work (race condition)
- even if you make updates atomic, it will be slow (by now you should know how slow it will be)

```
v = 0.0;
for (i = 0; i < n; i++) {
  v += f(a + i * dt) * dt;
}
```

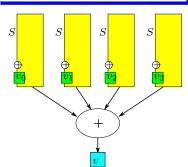
```
v = 0.0;
#pragma omp parallel shared(v)
#pragma omp for
for (i = 0; i < n; i++) {
#pragma omp atomic
v += f(a + i * dt) * dt;
}</pre>
```



Reduction clause in OpenMP

- a more efficient strategy is to
 - let each thread reduce on its private variable, and
 - when threads finish, combine their partial results into one
- reduction clause in OpenMP does just that

```
v = 0.0;
pragma omp parallel shared(v)
pragma omp for reduce(+:v)
for (i = 0; i < n; i++) {
v += f(a + i * dt) * dt;
}</pre>
```



Simple reduction and user-defined reduction (2.7.1)

• reduction syntax:

```
#pragma omp parallel reduction(op:var,var,...)
S
```

- builtin reductions
 - op is one of +, *, -, &, ^, |, &&, and ||
 - (Since 3.1) min or max
- (Since 4.0) a user-defined reduction name
- user-defined reduction syntax:

```
#pragma omp declare reduction (name : type : combine statement)
```

User-defined reduction example

```
typedef struct {
      int x; int y;
   } point;
   point add_point(point p, point q) {
      point r = \{ p.x + q.x, p.y + q.y \};
5
6
      return r:
    // declare reduction "ap" to add two points
    #pragma omp declare reduction(ap: point: omp_out=add_point(omp_out,
              omp_in))
10
    int main(int argc, char ** argv) {
11
      int n = atoi(argv[1]);
12
      point p = \{ 0.0, 0.0 \};
13
      int i:
14
    #pragma omp parallel for reduction(ap : p)
1.5
      for (i = 0; i < n; i++) {
16
        point q = { i, i };
17
        p = add_point(p, q);
18
19
      printf("%d %d\n", p.x, p.y);
20
      return 0:
21
22
```

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SIMD constructs

- simd pragma
 - allows an explicit vectorization of for loops
 - syntax restrictions similar to omp for pragma apply
- declare simd pragma
 - instructs the compiler to generate vectorized versions of a function
 - with it, loops with function calls can be vectorized

simd pragma

• basic syntax (similar to omp for):

```
#pragma omp simd clauses
for (i = ...; i < ...; i += ...)
S</pre>
```

- clauses
 - aligned(var, var, ... : align)
 - uniform(var,var,...) says variables are loop invariant
 - linear(var,var,...:stride) says variables have the specified stride between consecutive iterations

declare simd pragma

• basic syntax (similar to omp for):

```
#pragma omp declare simd clauses
function definition
```

- clauses
 - those for simd pragma
 - notinbranch
 - inbranch

SIMD pragmas, rationales

- most automatic vectorizers give up vectorization in many cases
 - conditionals (lanes may branch differently)
 - 2 inner loops (lanes may have different trip counts)
 - 3 function calls (function bodies are not vectorized)
 - iterations may not be independent
- simd and declare simd directives should eliminate obstacles 3 and 4 and significantly enhance vectorization opportunities

A note on current GCC OpenMP SIMD implementation

- as of now (version 4.9), GCC simd and declare simd ≈ existing auto vectorizer dependence analysis
- declare simd functions are first converted into a loop over all vector elements and then passed to the loop vectorizer

```
float8 f(float8 vx, float8 vy) {
    float8 r;
    float f(float x, float y) {
        return x + y;
    }
    float f(float x, float y) {
        return x + y;
    }
}
float8 f(float8 vx, float8 vy) {
    float8 r;
    for (i = 0; i < 8; i++) {
        float x = vx[i], y = vy[i]
        r[i] = x + y;
    }
    return r;
}

float8 f(float8 vx, float8 vy) {
    float8 r;
    for (i = 0; i < 8; i++) {
        float x = vx[i], y = vy[i]
        return r;
}

float8 f(float8 vx, float8 vy) {
    float8 r;
    for (i = 0; i < 8; i++) {
        float x = vx[i], y = vy[i]
        return r;
}

float8 f(float8 vx, float8 vy) {
        float8 r;
        for (i = 0; i < 8; i++) {
            float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
             return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
             return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}

float x = vx[i], y = vy[i]
              return r;
}
</pre>
```

- the range of vectorizable loops in current GCC (4.9) implementation seems very limited
 - innermost loop with no conditionals
 - doubly nested loop with a very simple inner loop