



60V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D T _C = +25°C	
-60V	$110m\Omega$ @ $V_{GS} = -10V$	-14A	
-60 V	140mΩ @ $V_{GS} = -4.5V$	-12A	

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Analog Switch

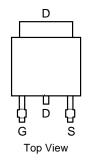
Features and Benefits

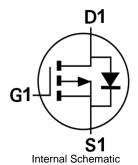
- Low On-Resistance
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- · Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 <a>©3
- · Weight: 0.33 grams (approximate)







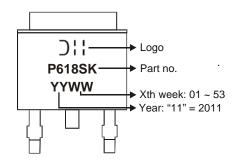
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMP6180SK3-13	Standard	TO252	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information





Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	-60	V	
Gate-Source Voltage	V_{GSS}	±20	V	
Continuous Drain Current (Note 6) $V_{GS} = -10V$ Steady $T_C = +25^{\circ}C$ State $T_C = +100^{\circ}C$		l _D	-14 -10	А
Maximum Body Diode Forward Current (Note 6)	I _S	4.1	Α	
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	25	Α	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	·	Symbol	Value	Units
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	р	1.7	W
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	P_{D}	1.0	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	Б	76	°C/W
Thermal Resistance, Junction to Ambient (Note 3)	t<10s	$R_{\theta JA}$	33	
Total Power Dissipation (Note 6)	$T_A = +25$ °C	P_{D}	2.7	- W
Total Fower Dissipation (Note o)	$T_A = +70^{\circ}C$	FD	1.5	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	Б	50	°C/W
Thermal Resistance, Junction to Ambient (Note o)	t<10s	$R_{\theta JA}$	24	
Total Power Dissipation (Note 6)	$T_C = +25^{\circ}C$	P_{D}	40	W
Total Fower Dissipation (Note o)	$T_{C} = +100^{\circ}C$	FD	16	V V
Thermal Resistance, Junction to Case (Note 6)	Steady state	$R_{\theta JC}$	3.1	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-60	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	-1.2	_	-2.7	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		_	60	110	mΩ	$V_{GS} = -10V, I_D = -12A$	
Static Diain-Source On-Resistance	R _{DS (ON)}	_	80	140	11152	$V_{GS} = -4.5V, I_D = -8A$	
Forward Transfer Admittance	Y _{fs}	_	15	_	S	$V_{DS} = -5V$, $I_{D} = -12A$	
Diode Forward Voltage	V_{SD}	_	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	984.7	_		V _{DS} = -30V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	58	_	pF		
Reverse Transfer Capacitance	Crss	_	45.5	_			
Gate Resistance	R_{G}	_	12.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	8.1	_			
Total Gate Charge (V _{GS} = -10V)	Q_g	_	17.1	_	nC	V _{DS} = -30V. I _D = -12A	
Gate-Source Charge	Q_{gs}	_	3.2	_	IIC	VDS = -30V, ID = -12A	
Gate-Drain Charge	Q_{gd}	_	3.9	_			
Turn-On Delay Time	t _{D(on)}	_	5.9	_			
Turn-On Rise Time	t _r	_	21.2	_		$V_{GS} = -10V$, $V_{DS} = -30V$, $R_{GEN} = 3\Omega$,	
Turn-Off Delay Time	t _{D(off)}	_	30.9	_	ns	$R_L = 2.5\Omega$	
Turn-Off Fall Time	t _f	_	39.1	_			
Body Diode Reverse Recovery Time	t _{rr}	_	19.9	_	ns	I _S = -12A, dI/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q _{rr}	_	1.7	_	nC	I _S = -12A, dI/dt = 100A/µs	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

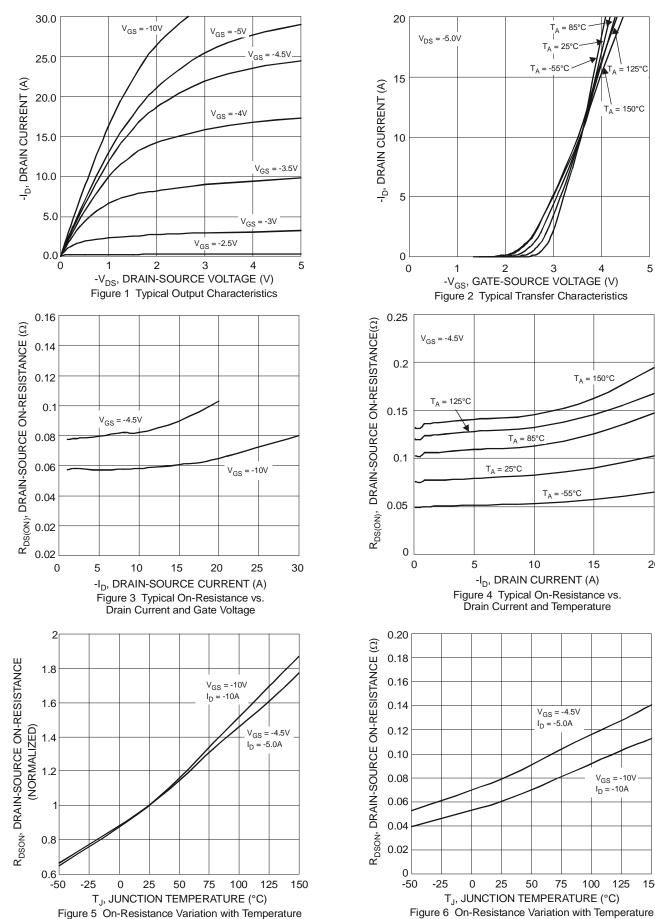
^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

^{7.} Short duration pulse test used to minimize self-heating effect

^{8.} Guaranteed by design. Not subject to production testing

150°C





150

 $V_{GS} = -10V$

125

I_D = -10A

15

20



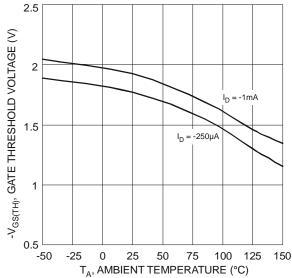
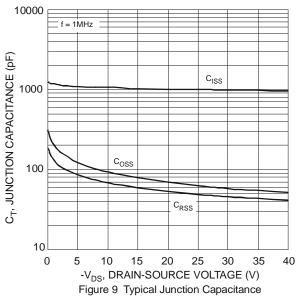


Figure 7 Gate Threshold Variation vs. Ambient Temperature



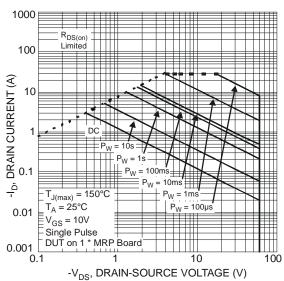
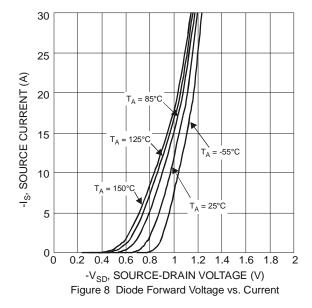
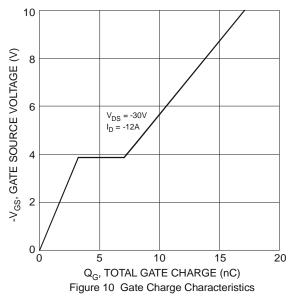
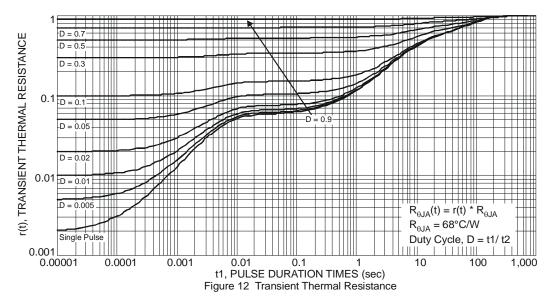


Figure 11 SOA, Safe Operation Area



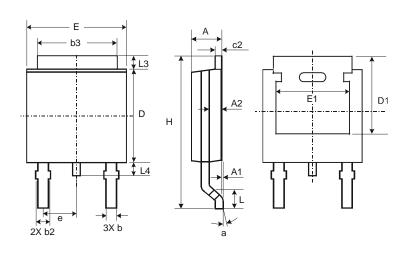






Package Outline Dimensions

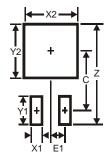
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	_	_		
е	_	_	2.286		
Ε	6.45	6.70	6.58		
E1	4.32 –		_		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
Z	11.6		
X1	1.5		
X2	7.0		
Y1	2.5		
Y2	7.0		
С	6.9		
E1	2.3		



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